

Separate Sheet

Product Specifications of the RAJ240090 and the RAJ240100

Product	RAJ240090	RAJ240100
Part Name	RAJ240090GFP	RAJ240100GFP
Series Cells	3 – 8 Cells	3 – 10 Cells
Configuration	MCU (RL78 CPU core) + AFE (Note 1)	
Operating voltage	VCC = 4.0 V - 50.0 V	
Operating ambient temperature	T _A = -20 - +85°C	
Power consumption current (sleep mode)	25 uA (DFET, CFET : OFF)	
FET control	Nch High side driver	
Communication	UART: 2ch, I2C-BUS (SMBus): 1ch, CAN: 1ch	
Flash memory	Prog. / 128 K, Data/ 7 K	
A/D converter	15-bit resolution sigma-delta A/D converter	
Current integration circuit	18-bit resolution sigma-delta A/D converter	
I/O port	31 (I/O: 20, input: 4, NOD I/O: 4, HV I/O:3)	
Over current detection, wake up	Short/Discharge/Charge, Current Wakeup	
Others	3 V/ 5 V embedded regulator, RTC	
Package	QFP 64-pin 0.5mm pitch	

(Note 1) AFE : Analog Front End

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