

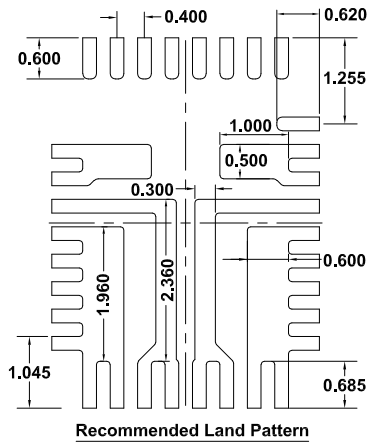
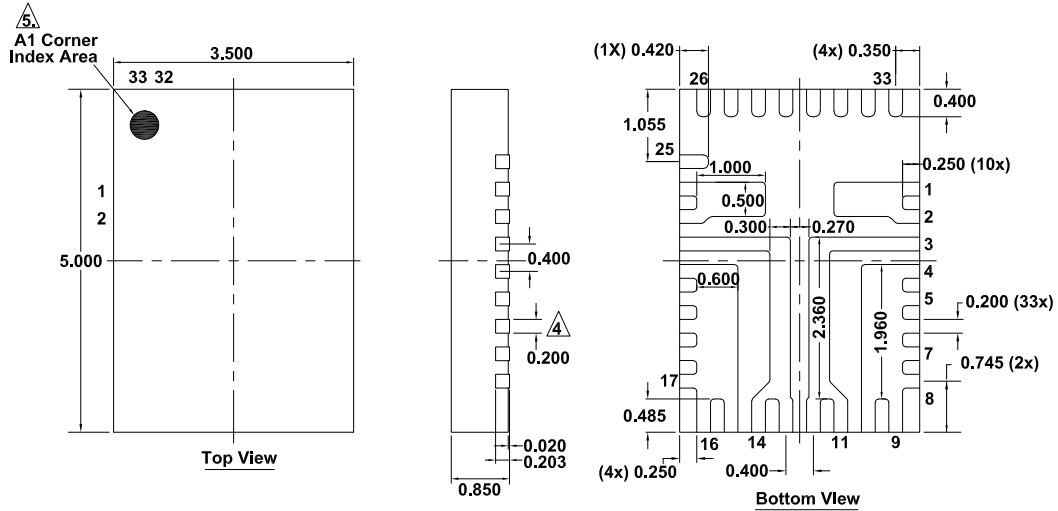
Plastic Packages for Integrated Circuits

Package Outline Drawing

L33.3.5x5

33 Lead Flip Chip Quad Flat No-Lead Package (FCQFN)

Rev 4, 6/2021



Notes:

1. Dimensions are in millimeters.

Dimension in () for reference only.

2. Dimensioning and tolerancing conform to ASME Y14M-1994.

3. Unless otherwise specified, tolerance: Decimal ± 0.05 .

\triangle Dimension applies to the metallized terminal and is measured between 0.15mm and 0.25mm from the terminal tip.

\triangle The configuration of the pin #1 identifier is optional, but must be located within the zone indicated. The pin#1 Identifier can be either a mold or mark feature.