
GaN FETs

GaN FET Current Increase Due to Heavy Ion Testing

Abstract

Heavy ion single event testing of the Renesas 40V, 100V, and 200V GaN FETs showed that all three of the device types experienced an increase in I_{DSS} current under certain conditions of V_{DSS} and LET. Using the results of this testing and the appropriate parameters for a geosynchronous orbit in the single event simulation program CREME96, it was possible to show that the increase in current in a typical geosynchronous orbit would be inconsequential.

Contents

1. Analysis	2
2. Conclusion	3
3. Revision History	4

List of Figures

Figure 1. CREME96 LET Spectrum for a Satellite in a Geosynchronous Orbit Assuming Solar Minimum Conditions and 100 mils Al Shielding	3
--	---

Related Literature

For a full list of related documents, visit our website:

- [ISL70020SEH](#), [ISL70023SEH](#), [ISL70024SEH](#) device pages

1. Analysis

[Table 1](#) is a summary of the worst case I_{DSS} current increase of the four samples that were tested at the Texas A&M University (TAMU) Radiation Effects Facility of the Cyclotron Institute. Before and after each irradiation the I_{DSS} current (two terminal blocking current) was logged with the part in the blocking mode. The I_{DSS} current was also measured for the absolute maximum V_{DSS} voltage ratings before and after each irradiation. The measurements and irradiations were carried out at ambient temperature ($\sim 25^{\circ}\text{C}$) to a fluence of 2.5×10^6 ions/cm² at a flux of approximately 1×10^4 ions/(cm²-s). This brought the total fluence for each device type at each species and V_{DSS} combination to 1×10^7 ion/cm². Each combination of ion species (4) and V_{DSS} (3) was tested on four fresh DUTs. Additional details on this testing can be found in the relevant test reports on the [ISL70020SEH](#), [ISL70023SEH](#), [ISL70024SEH](#) device pages.

Table 1. Worst Case Current Increase of GaN FETs During Exposure to Heavy Ions at a Fluence of 1×10^6 ions/cm²

Part Type	V_{DSS} (V)	Current Increase at LET		
		43Mev•cm ² /mg	60Mev•cm ² /mg	86Mev•cm ² /mg
ISL70020SEH (40V)	32	-	-	Up to 5.4μA
	40	-	-	Up to 9.0μA
ISL70023SEH (100V)	60	-	Up to 1.3μA	Up to 3.1μA
	80	-	Up to 0.5μA	Up to 4.6μA
	100	-	Up to 1.9μA	Up to 5.9μA
ISL70024SEH (200V)	120	Up to 2.0μA	Up to 7.4μA	Up to 8.9μA
	160	Up to 0.9μA	Up to 3.1μA	Up to 32.4μA
	200	Up to 6.7μA	Up to 15.3μA	SEB

As shown in [Figure 1 on page 3](#), a CREME96 LET spectrum file was generated for a satellite in a geosynchronous orbit using all species of heavy ion particles (atomic numbers 2 – 92) with a minimum energy value of 0.1MeV/nuc. Assumptions included solar minimum conditions for worst case cosmic flux transported through 100 mils of aluminum shielding.

The integral flux in [Figure 1](#) is expressed in terms of the number of particles incident in a solid angle about a line normal to a small area on the surface of a sphere (note that the vertical axis is in particles per meter squared per steradian second). The LET shown on the x-axis is measured in Mev•cm²/gm. The colored lines on the plot represent the flux of particles of a given LET. For example, the green lines on the plot show the flux of particles with an LET of 43Mev•cm²/mg (note - gram to milligram conversion required), which is the lowest measured energy that resulted in a current increase. The associated flux for particles of that energy is approximately 3.04×10^{-7} particles/m²-s-sr or 3.04×10^{-11} particles/cm²-s-sr.

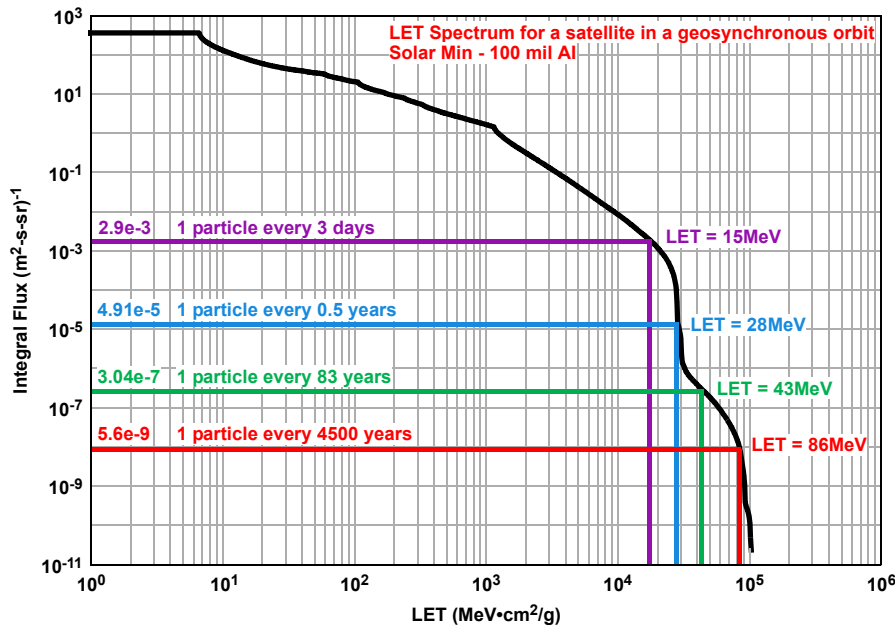


Figure 1. CREME96 LET Spectrum for a Satellite in a Geosynchronous Orbit Assuming Solar Minimum Conditions and 100 mils Al Shielding

In mission terms, we need to determine the particle fluence (or total number of particles in a period of time) at an energy of 43MeV the part would see. Therefore, based on the following equation you can determine how many particles a part would see in a year.

$$\text{Fluence} = \text{Flux} * \text{Time}$$

$$= (3.04 \times 10^{-11} \text{ particles/cm}^2\text{-s-sr})(60 \text{ s/min})(60 \text{ min/hr})(24 \text{ hr/day})(365.25 \text{ days/yr})$$

$$= 9.59 \times 10^{-4} \text{ particles/cm}^2\text{-yr-sr}$$

Statistically speaking, the sensitive volume of the GaN FET ($\leq 1\text{cm}^2$) in the satellite would see a particle with an LET of $\geq 43\text{MeV}$ approximately once every 83 years in a geosynchronous orbit, compared to the much more common lower energy ions above the knee of the curve. For an ion with an LET of $\geq 86\text{MeV}$, it is approximately once every 4500 years. While these more energetic ions are not trivial for SEL considerations, they are insignificant for a cumulative event, like current increase. For the higher energy particles, it takes over 292 million years for the parts to see the current increase caused by 1×10^6 ions/cm² of ions with an LET $\geq 60\text{MeV}$ and over 4 billion years with 86MeV ions. In a typical 20 year GEO mission, the worst case current increase, on average, is in the picoamps or less for any of the ions that caused the current increase in the exposed parts. The results are summarized in [Table 2](#) for ions with LET $\geq 28\text{MeV}$. Only the maximum drain-to-source voltages are shown for the 40V and 100V devices, because they did not show any evidence of SEB during testing. However, the 200V device experienced SEB at 200V and LET of $\geq 86\text{MeV}$, so 160V results are also included.

Table 2. GaN FET Current Increase after 20 Years at 1×10^6 ions/cm² (Amps)

Part Type	V _{DSS} (V)	28MeV·cm ² /mg	43MeV·cm ² /mg	60MeV·cm ² /mg	86MeV·cm ² /mg
ISL70020SEH (40V)	40	3.19E-11	2.89E-14	4.93E-14	4.00E-14
ISL70023SEH (100V)	100	1.09E-11	2.14E-13	2.15E-13	1.45E-13
ISL70024SEH (200V)	160	1.09E-11	2.14E-13	2.15E-13	1.45E-13
	200	6.23E-12	1.61E-12	1.05E-12	SEB

2. Conclusion

While SEE testing of the Renesas 40V, 100V, and 200V GaN FETs determined evidence of current increase at energies above 43MeV·cm²/mg and higher supply voltages, the scarcity of ions of this energy in geosynchronous orbit make the actual increase in current during a typical 20 year mission inconsequential.

3. Revision History

Rev.	Date	Description
1.00	Mar.25.19	Initial release

Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
5. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
 - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
 - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
6. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
7. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
8. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
9. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
10. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
11. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Rev.4.0-1 November 2017)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:
www.renesas.com/contact/

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.