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April 1st, 2010
Renesas Electronics Corporation

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M32C/84, 85, 86, 87, 88 Group

Buzzer Output

1. Abstract

Timer A outputs buzzers using timer mode.

Use the peripheral function listed below:

- Timer A0 in timer mode

2. Introduction

The application example described in this document is applied to the following MCUs and parameter(s):

MCUs: M32C/84 Group
M32C/85 Group
M32C/86 Group
M32C/87 Group
M32C/88 Group

This program can be used with other M16C Family MCUs which have the same special function registers (SFRs) as the above MCUs. Check the manual for any additions and modifications to functions. Careful evaluation is recommended before using this application note.

3. Example Description

3.1 Specifications

- (1) Output a 2 kHz buzzer using timer A0.
- (2) Pull up the port via a pull-up resistor. When the buzzer is off, set the port to high-impedance and fix at the pulled-up potential.
- (3) Connect a 32 MHz oscillator to Xin.

3.2 Operation

- (1) Set the TA0S bit in the TABSR register to 1 (count started).
- (2) To turn the buzzer ON, set the PS1_0 bit in the PS1 register to 1 (the P7_0 pin function selected by the PSL1_0 bit).⁽¹⁾ A 2 kHz pulse is output from the P7_0/TA0OUT pin.
- (3) To turn the buzzer OFF, set the PS1_0 bit in the PS1 register to 0 (I/O pin).
The P7_0/TA0OUT pin is placed in high-impedance state.

NOTE 1:

Set the PSL1_0 bit in the PSL1 register to 1 (TA0OUT selected for the P7_0 pin function) at the initial setting.

Figure 1 shows the Operation Timing.

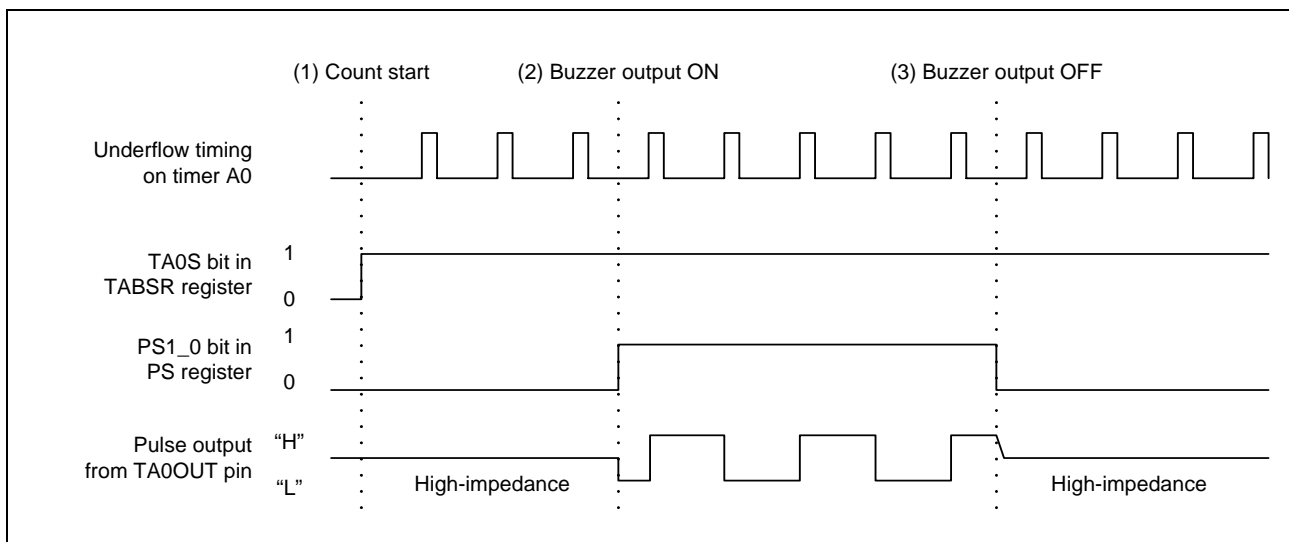


Figure 1 Operation Timing

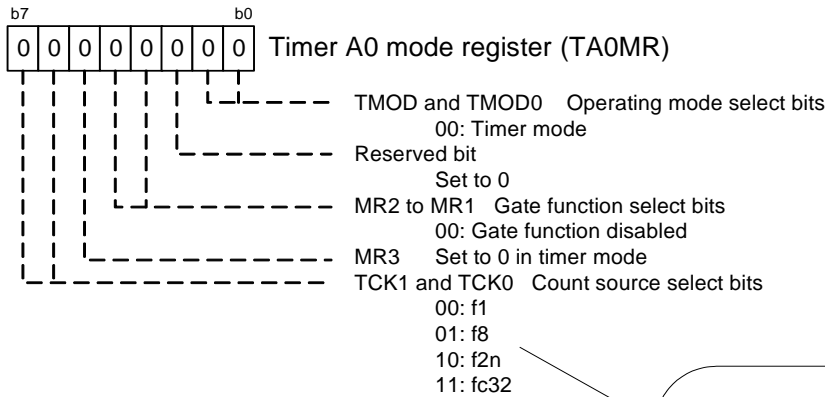
3.3 Setup

This section shows the setup sequence and values to perform the application example described in

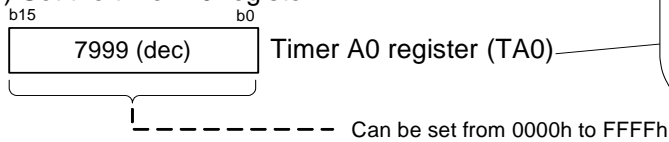
3. Example Description.

Refer to the MCUs Hardware Manual for details of individual registers.

(1) Set the timer A0 mode register

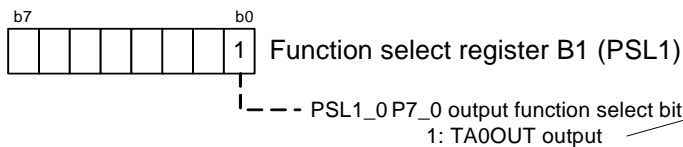


(2) Set the timer A0 register



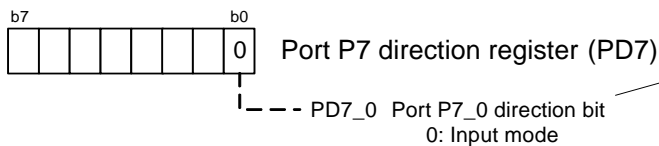
XIN = 32 MHz
 If the TA0 register setting value is n,
 the output pulse frequency of timer A0 is:
 $(f1/(n + 1))/2 = (32 \times 10^6 / (7999 + 1))/2 = 2 \times 10^3 \text{ Hz}$
 = 2 kHz

(3) Set function select register B1



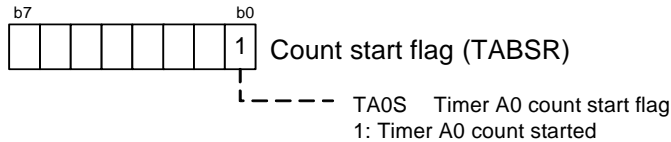
Valid when the PS1_0 bit in the PS1 register is set to 1 (function selected by the PSL1_0 bit). P7_0 functions as the TA0OUT pin and outputs pulses.

(4) Set the port P7 direction register

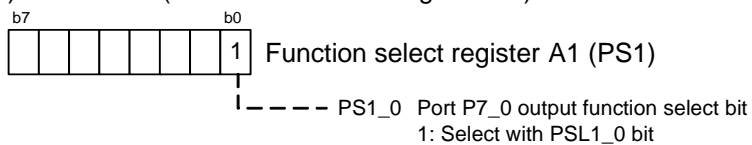


Valid when the PS1_0 bit in the PS1 register is set to 0 (I/O mode). P7_0 is placed in high-impedance state.

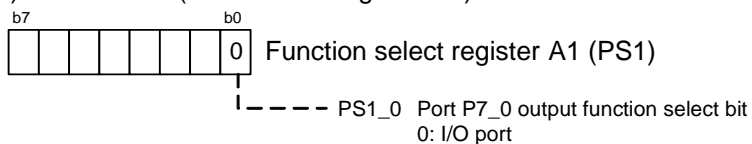
(5) Set the count start flag



(6) Buzzer ON (Set function select register A1)



(7) Buzzer OFF (Set function register A1)



4. Sample Programming Code

A sample program can be downloaded from the Renesas Technology website.
For download, click “Application Notes” in the left-hand side menu of the M16C Family page.

5. Reference Documents

Hardware Manuals

M32C/84 Group Hardware Manual

M32C/85 Group Hardware Manual

M32C/86 Group Hardware Manual

M32C/87 Group Hardware Manual

M32C/88 Group Hardware Manual

The latest version can be downloaded from the Renesas Technology website.

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REVISION HISTORY	M32C/84, 85, 86, 87, 88 Group Buzzer Output
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Rev.	Date	Description	
		Page	Summary
1.00	Sep 10, 2006	–	First Edition issued

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