[Notes]

e² studio,

CS+ Device Information for RX Family

Outline

When using the e² studio and CS+ device information for the RX family, note the following point.

1. Display and writing of I/O registers when the RX113 group is used

1. Display and Writing of I/O Registers When the RX113 Group is Used

1.1 Applicable Products

➢ e² studio V5.3.0 and later

➢ CS+ device information for RX family V2.00.00 and later

1.2 Applicable MCUs

➢ RX Family: RX113 Group

1.3 Details

When the following I/O registers are displayed in the IO panel and watch panel of CS+, and in the IO view of e² studio, the values might not be displayed correctly and not be able to be written.

➢ Applicable I/O registers: The following 13 I/O registers (framed in red)

Module name	Register name	Address	Register Size	R/W	Expected value example	Display example CS+: IO panel/Watch panel e² studio: IO view
CTSU	CTSUSSC	000A 0912h	16bit	R/W	0x0F00	0x00
FLASH	FSCMR	007F C0B0h	16bit	R	0x7F00	0x00
POE	OCSR1	0008 8902h	16bit	R/W	0x0300	0x03
POE	ICSR3	0008 890Eh	16bit	R/W	0x0200	0x02
SYSTEM	SBYCR	0008 000Ch	16bit	R/W	0x8000	0x00
SYSTEM	SCKCR3	0008 0026h	16bit	R/W	0x0100	0x00
SYSTEM	CKOCR	0008 003Eh	16bit	R/W	0xF700	0x00
USB	INTENB0	000A 0030h	16bit	R/W	0xFF00	0x00
USB	PIPE1TRE	000A 0090h	16bit	R/W	0x0300	0x00
USB	PIPE2TRE	000A 0094h	16bit	R/W	0x0300	0x00
USB	PIPE3TRE	000A 0098h	16bit	R/W	0x0300	0x00
USB	PIPE4TRE	000A 009Ch	16bit	R/W	0x0300	0x00
USB	PIPE5TRE	000A 00A0h	16bit	R/W	0x0300	0x00

Example of invalid display in CS+:
SCKCR3 is incorrectly displayed as 0x00.

Example of invalid display in e² studio:
SCKCR3 is incorrectly displayed as 0x00.

1.4 Workaround

1.4.1 CS+

Register the register address in a watch format (with square brackets "[]") and set the byte size as 2.

Note that the display is fixed by this workaround, but writing cannot be performed.
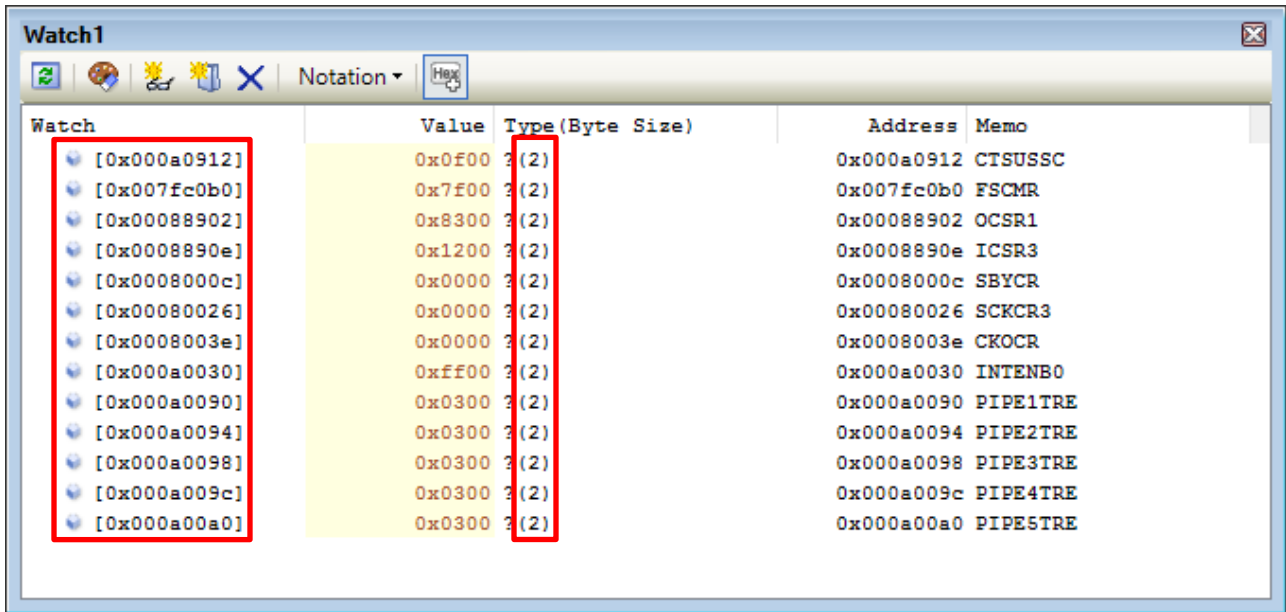
1.4.2 e² studio

In the memory view, display a value in units of one byte[Note] and check the display.

Note that writing can be performed, but access must be performed in units of one byte.

Note: In the following dialog box which is opened by right-clicking in the memory view and then clicking [Format...], specify 1 for the column size.
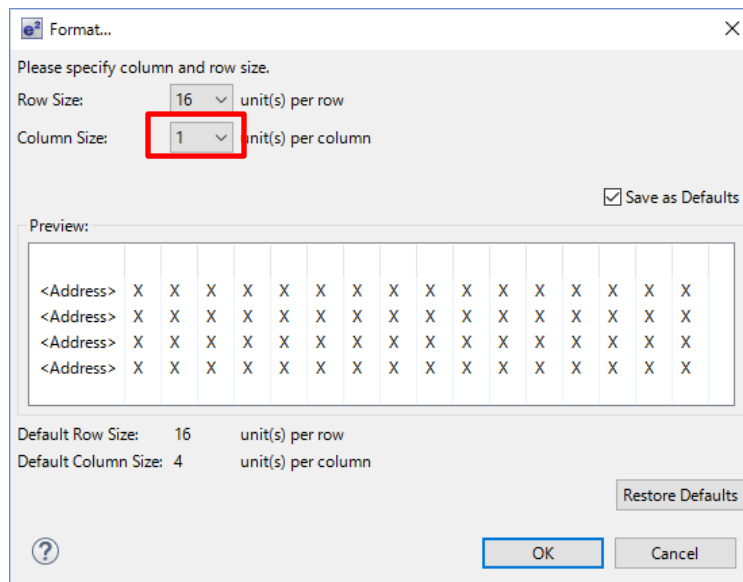
The FSCMR, OCSR1, and ICSR3 I/O registers can be checked by this method. For other I/O registers, the alignment of the address is inverted.

For example, for the SCKCR3 register, 0x00080026 and 0x00080027 are displayed in the memory view as follows. At this time, the value of the SCKCR3 register is 0x0300.

1.5 Schedule for Fixing the Problem

This problem will be fixed in a later version.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr. 16, 2017	-	First edition issued

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