RENESAS Tool News

A Note on Using the Simulator Debugger
for the SuperH RISC Engine Family
-- On Debugging Target Programs after Simulations of SH2A FPUs, SH-4A MCUs, and SH4AL DSPs --

Please take note of the following problem in using the Simulator Debugger for the SuperH RISC engine family:

- On debugging target programs after simulations of SH2A FPUs, SH-4A MCUs, and SH4AL DSPs.

1. **Versions Concerned**

   The simulator debuggers for the SuperH RISC engine family Ver.9.3.01 and later are concerned, which are included with the C/C++ compiler package for the SuperH RISC engine family V.9.00 Release 00 and later.

   To check for the version number of your simulator debugger, follow these steps:

   (1) In the High-performance Embedded Workshop, open the Tool menu and select the Administration command. The Tool Administration dialog box appears.

   (2) Select the name of your simulator debugger in the Registered Components list and click the Properties button. The Property dialog box opens.

   (3) Click the Information tab, and you see the version number of yours.

2. **Description**

   When any of the simulators listed below is selected out of the Target list box in the debug settings dialog box*, bus width is selectable among the Data bus size list in the Set Memory Map dialog box.
   However, simulation is performed without consideration of bus width, the results of

simulations are the same as in the default settings even if the values of data bus width are different from each other.

- SH2A-FPU Cycle Base Simulator
- SH-4A Cycle Base Simulator
- SH-4A Cycle Base Simulator(Little endian)
- SH4AL-DSP Cycle Base Simulator
- SH4AL-DSP Cycle Base Simulator(Little endian)

NOTE:
* You can see the debug settings dialog box by opening the Options menu and select the debug settings command.

3. **Schedule of Fixing the Problem**
We plan to fix this problem in the release of the simulator debugger for the SuperH RISC engine family V.9.06.00.