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A Note on Using the C Compiler packages for the R32C Series and the M16C Series, R8C Family of MCUs

Please take note of the following problem in using the C compiler packages for the R32C series and the M16C series, R8C family:

- With using SQMLint (the MISRA C rule checker) with High-performance Embedded Workshop

1. Products and Versions Concerned

The following products and versions are concerned in this problem:

- (1) C compiler package for the R32C series
V.1.01 Release 00 and V.1.02 Release 00
- (2) C compiler package (M3T-NC30WA) for the M16C series, R8C family
V.5.44 Release 00 through V.5.45 Release 00A

2. Description

When options for the MISRA C rule checker are selected on High-performance Embedded Workshop, they may not be complete with the message saying "Out of memory" displayed, or High-performance Embedded Workshop may be terminated abnormally.

3. Conditions

This problem arises if compiler options for C source files have been selected such a way that they vary from file to file in the Compiler tab of the Renesas *** Standard Toolchain dialog box, and then "MISRA C Rule Check" is selected in the Categories list.

To see the Renesas *** Standard Toolchain dialog box, open the Build menu and select "Renesas *** Standard Toolchain," and, if SQMLint has been installed, "MISRA C rule check" is displayed in the Categories

list.

4. Workaround

Do not select the compiler options for C source files that vary from file to file when SQMLint is used.

5. Schedule of Fixing the Problem

We plan to fix this problem in the next release of the compiler package.

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