

RENESAS TOOL NEWS on April 16, 2006: RSO-HI7000/4-060416D

## Corrections to the HI7000/4 Series User's Manuals

We notify corrections to the HI7000/4 series user's manuals (Document Nos. REJ10B0060-0600 and REJ10B0060-0500) as stated below.

The HI7000/4 supports the SH-1-, SH-2-, SH2-DSP-, SH-2E-, \* SH-2A-, and SH2A-FPU-cored MCUs.

\* The SH-2E-FPU-cored MCUs excluded.

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### 1. Manuals Concerned

- (1) HI7000/4 Series User's Manual Rev.6.00, Doc. No. REJ10B0060-0600  
(included with the HI7000/4 V.2.02)
- (2) HI7000/4 Series User's Manual Rev.5.00, Doc. No. REJ10B0060-0500  
(included with the HI7000/4 V.2.01)

### 2. Corrections

Page 372, the 9th line in Table 5.9

Insert a line between the 8th line (beginning with C\_hidef) and the 9th line (beginning with C\_hivct), and fill out the line as follows:

- (1) In the first column: D\_hidef
- (2) In the second column: kernel\_def.c
- (3) In the third column: Definition information on the kernel side
- (4) In the last column: Not necessary

### 3. Notices on the D\_hidef Section

- (1) The D\_hidef section has a property of C.

- (2) The D\_hidef section is generated when any of the SH-2A- or SH2A-FPU-cored MCUs is concerned, and only if the configurator is set so that the register banks can be used.
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