

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A819A/E	Rev.	1.00
Title	The error correction and addition about LCD controller (LCDC)		Information Category	Technical Notification		
Applicable Product	SH7203 Group SH7263 Group	Lot No.	Reference Document	<ul style="list-style-type: none"> • SH7203 Group Hardware Manual (REJ09B0313-0300) • SH7263 Group Hardware Manual (REJ09B0290-0300) 		
		ALL				

We would like to inform you of the following error correction and addition to the description of the LCD controller (LCDC) in the applicable products.

The following section, figure, and table numbers are those in the SH7203 group hardware manual.

In the case of the SH7263 group hardware manual, the section number is 26.

1. 24.3.19 LCDC Control Register (LDCNTR), Note 3

[Error]

3. After writing to LDCNTR, it takes some time for the display to actually start or stop.

Thus, to access another register of the LCDC after writing to LDCNTR, dummy-read LDCNTR once beforehand.

[Correction]

3. To access another register of the LCDC after writing to LDCNTR, wait for at least four cycles of $P\phi$ or dummy-read STBCR4 once beforehand.

2. 24.4.6 Power Management Registers, Paragraph 2

[Error]

Figures 24.4 to 24.7 are summary timing charts for power-supply control sequences and table 24.6 is a summary of available power-supply control sequence periods.

[Correction]

Figure 24.4 gives the flowcharts for power-supply control sequences, figures 24.5 to 24.8 are summary timing charts for the power-supply control sequences, and table 24.6 is a summary of the available power-supply control-sequence periods.

3. Added Figure 24.4

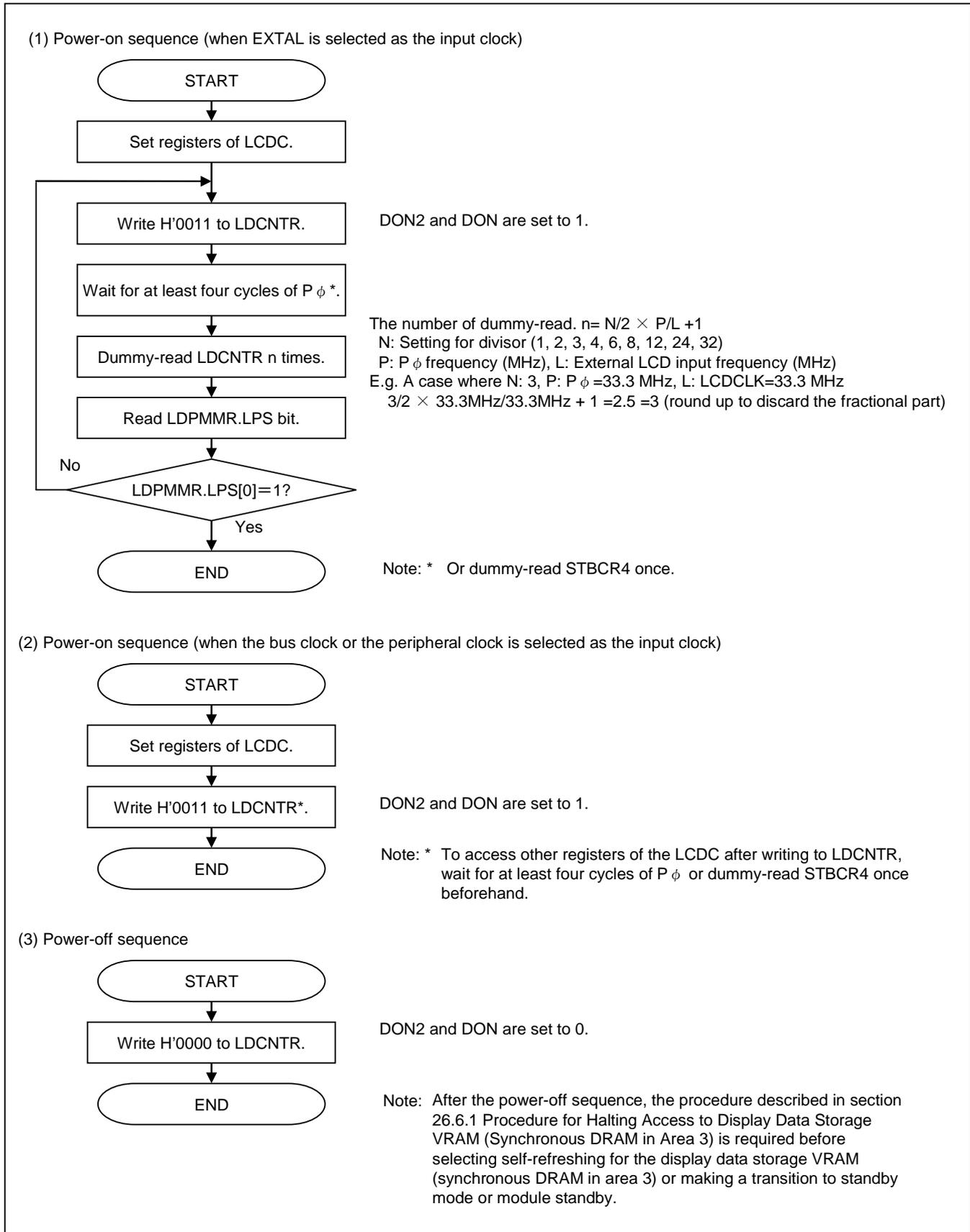


Figure 24.4 Flowchart for Power-Supply Control Sequences