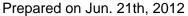
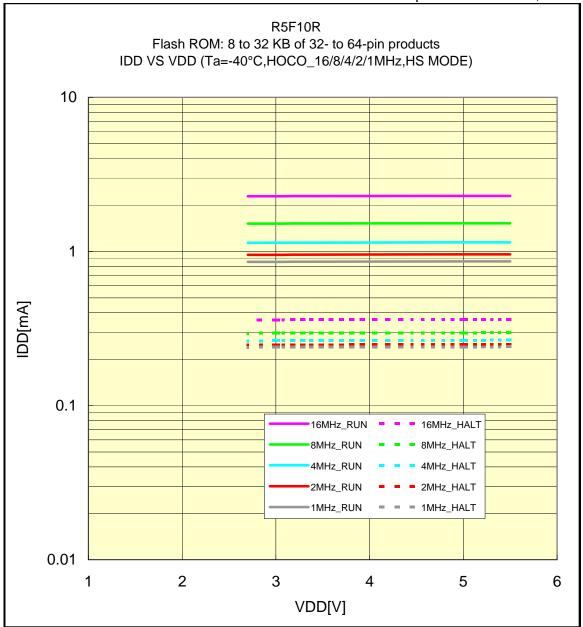
**R5F10R** 

#### IDD VS VDD(-40°C/HOCO\_16/8/4/2/1MHz/HS MODE)

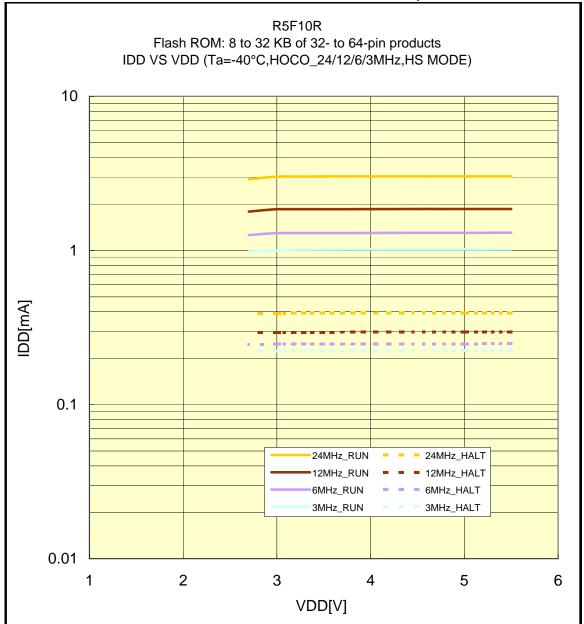




**R5F10R** 

#### IDD VS VDD(-40°C/HOCO\_24/12/6/3MHz/HS MODE)

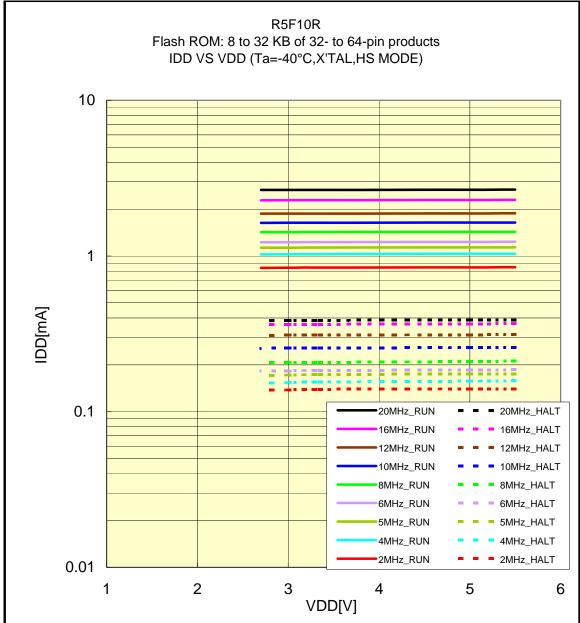




**R5F10R** 

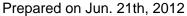
# IDD VS VDD(-40°C/X'TAL/HS MODE)

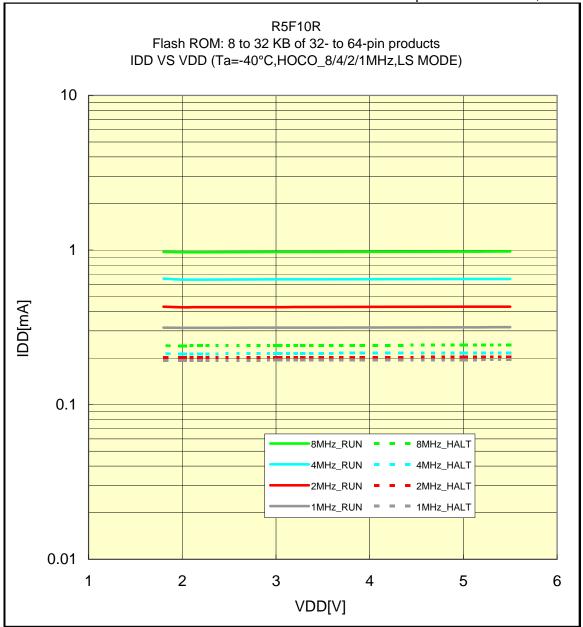




**R5F10R** 

## IDD VS VDD(-40°C/HOCO\_8/4/2/1MHz/LS MODE)

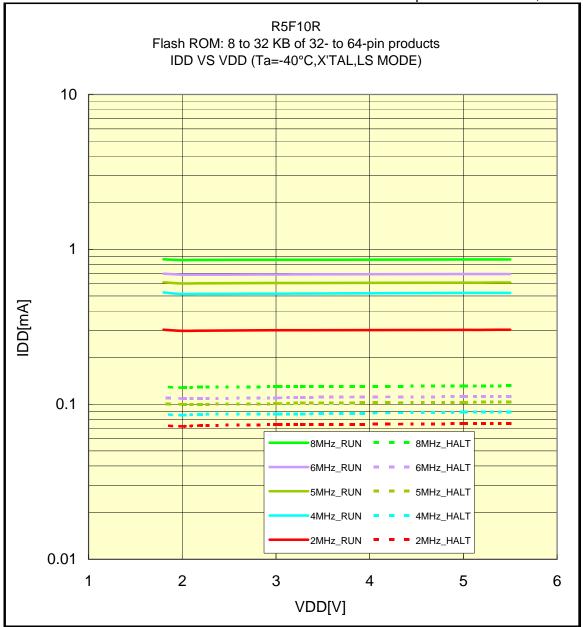




**R5F10R** 

## IDD VS VDD(-40°C/X'TAL/LS MODE)

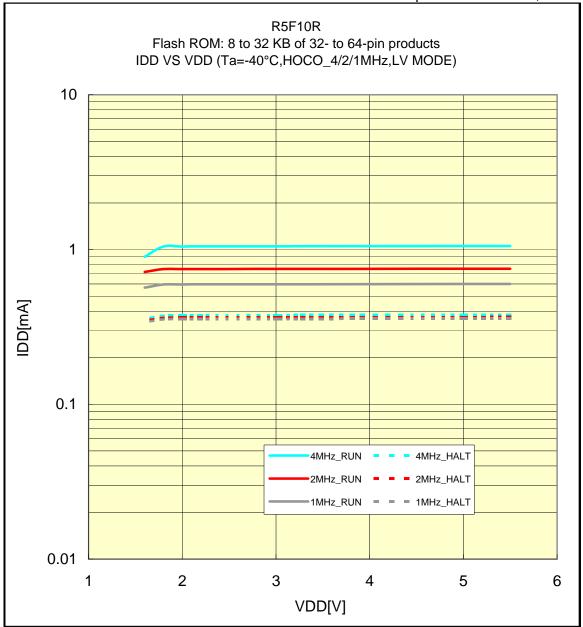




**R5F10R** 

## IDD VS VDD(-40°C/HOCO\_4/2/1MHz/LV MODE)

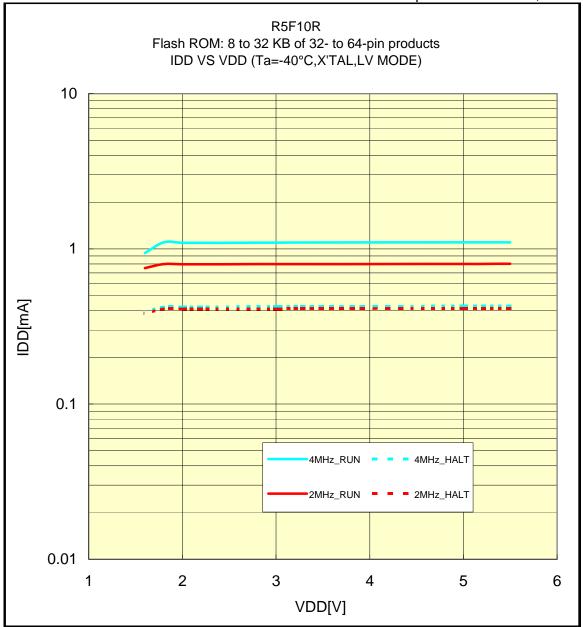




**R5F10R** 

## IDD VS VDD(-40°C/X'TAL/LV MODE)

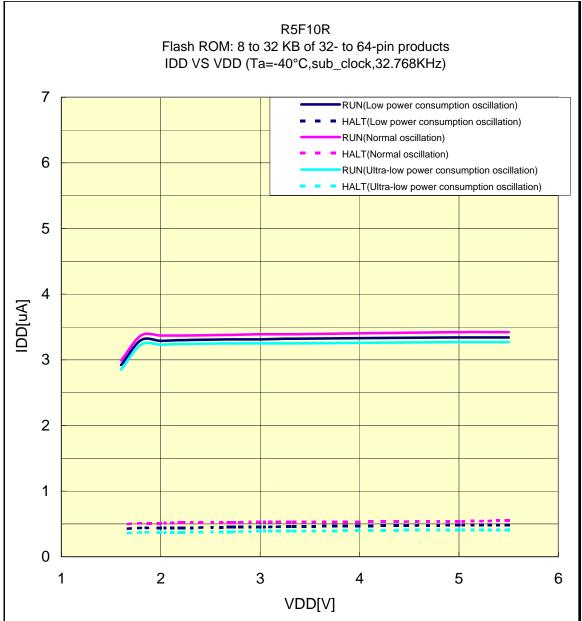




**R5F10R** 

#### IDD VS VDD(-40°C/sub\_clock/32.768KHz)

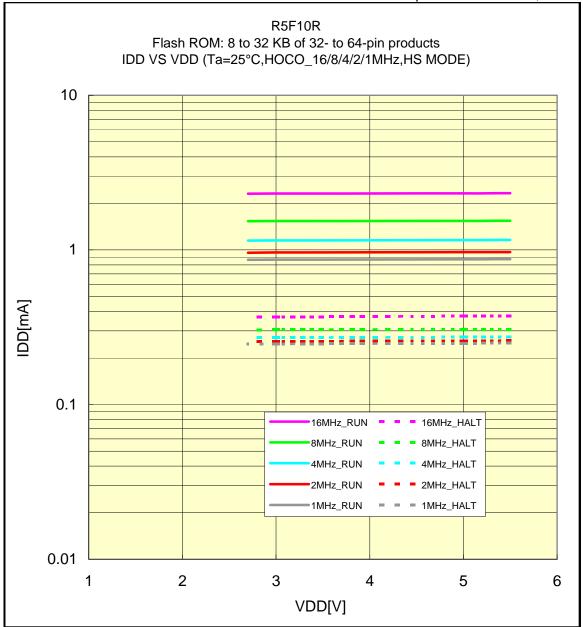




**R5F10R** 

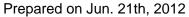
#### IDD VS VDD(25°C/HOCO\_16/8/4/2/1MHz/HS MODE)

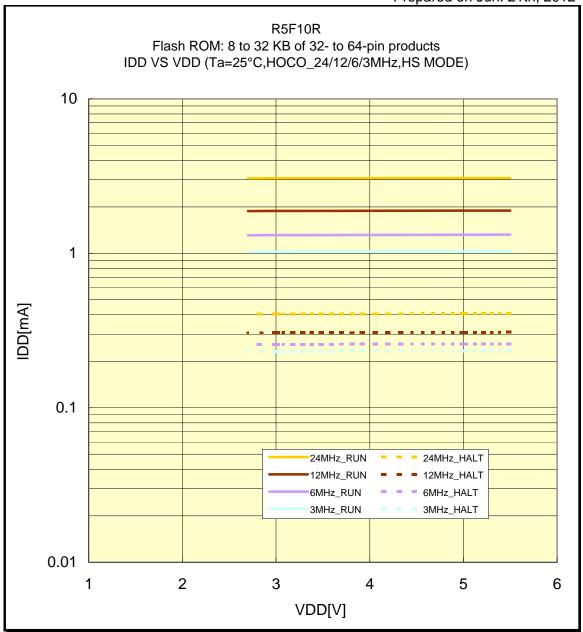




**R5F10R** 

#### IDD VS VDD(25°C/HOCO\_24/12/6/3MHz/HS MODE)

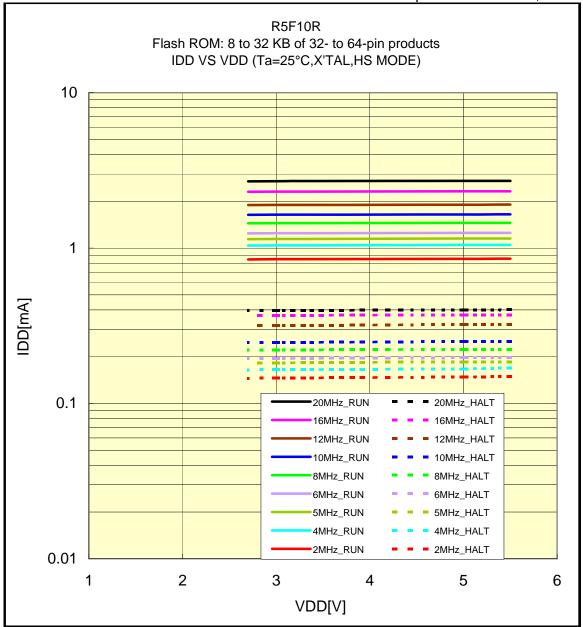




**R5F10R** 

## IDD VS VDD(25°C/X'TAL/HS MODE)

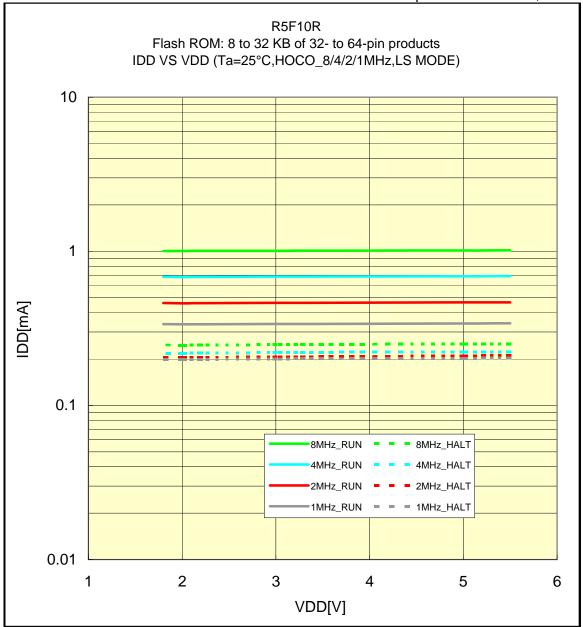




**R5F10R** 

#### IDD VS VDD(25°C/HOCO\_8/4/2/1MHz/LS MODE)

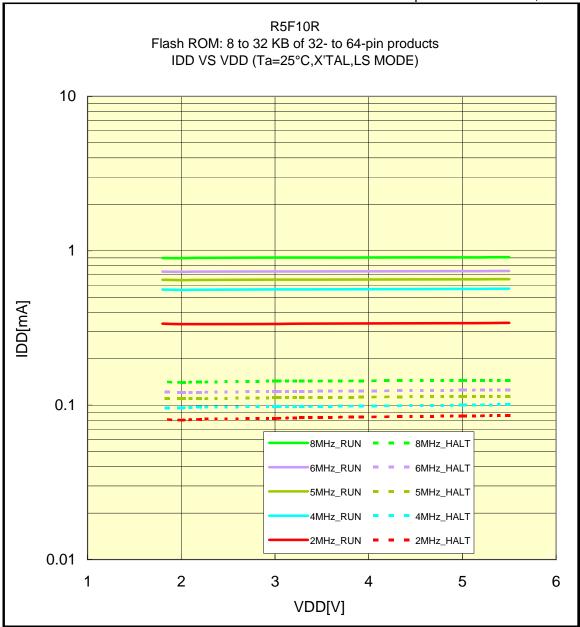




**R5F10R** 

## IDD VS VDD(25°C/X'TAL/LS MODE)

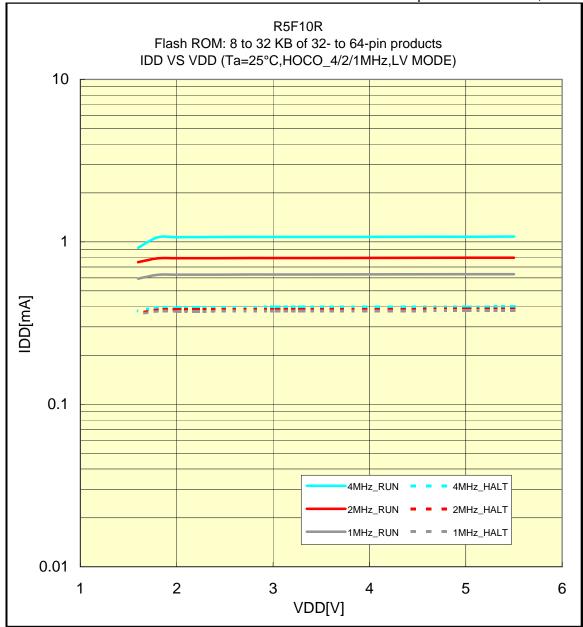




**R5F10R** 

#### IDD VS VDD(25°C/HOCO\_4/2/1MHz/LV MODE)

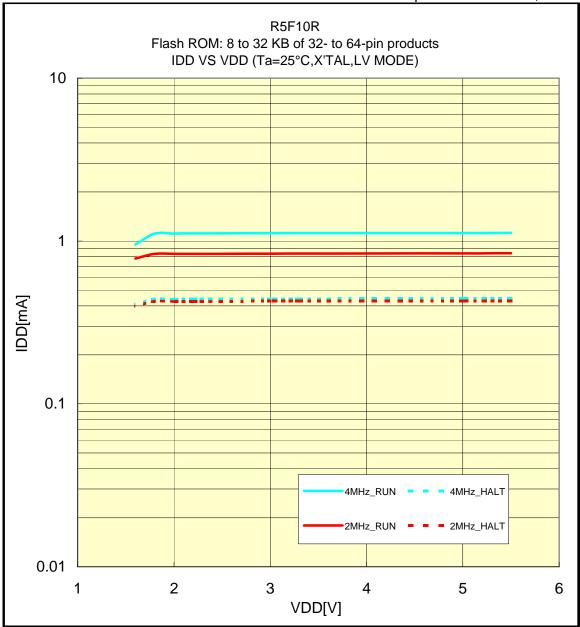




**R5F10R** 

## IDD VS VDD(25°C/X'TAL/LV MODE)

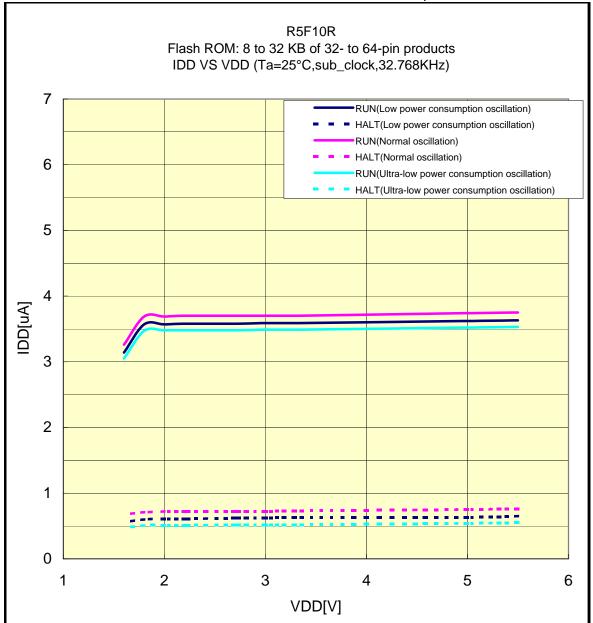




R5F10R Flash ROM: 8 to 32 KB of 32- to 64-pin products

#### IDD VS VDD(25°C/sub\_clock/32.768KHz)

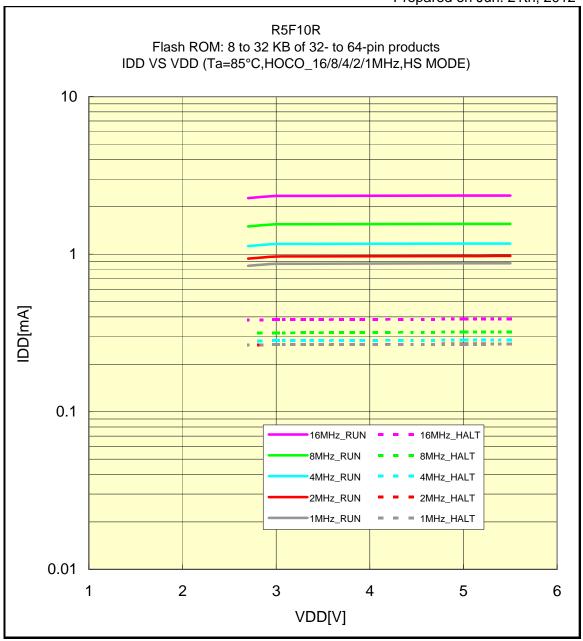




**R5F10R** 

## IDD VS VDD(85°C/HOCO\_16/8/4/2/1MHz/HS MODE)

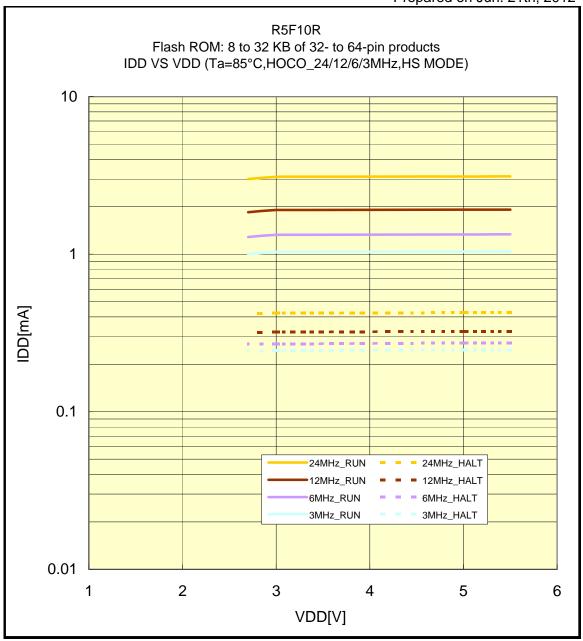
Prepared on Jun. 21th, 2012



**R5F10R** 

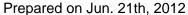
## IDD VS VDD(85°C/HOCO\_24/12/6/3MHz/HS MODE)

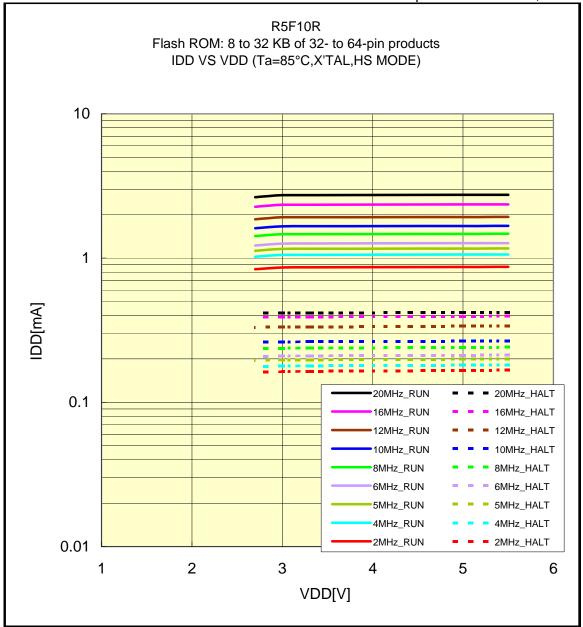
Prepared on Jun. 21th, 2012



**R5F10R** 

## IDD VS VDD(85°C/X'TAL/HS MODE)

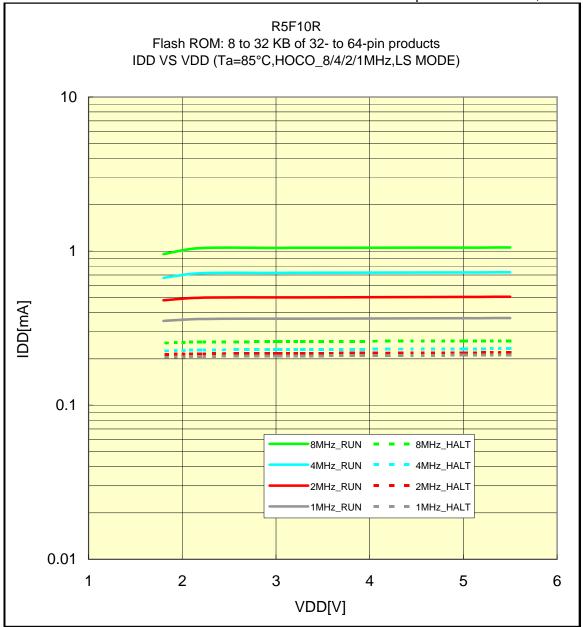




**R5F10R** 

#### IDD VS VDD(85°C/HOCO\_8/4/2/1MHz/LS MODE)

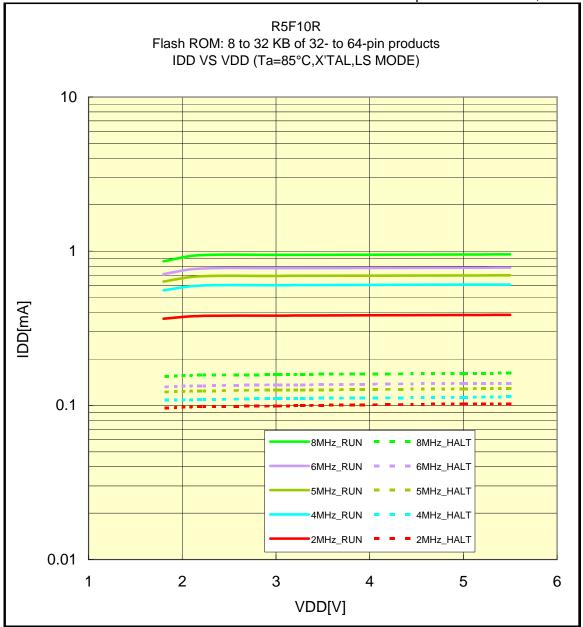




**R5F10R** 

## IDD VS VDD(85°C/X'TAL/LS MODE)

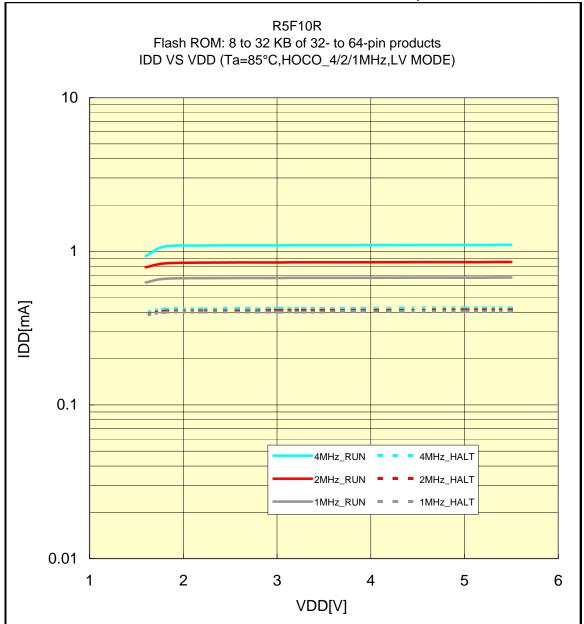




**R5F10R** 

#### IDD VS VDD(85°C/HOCO\_4/2/1MHz/LV MODE)

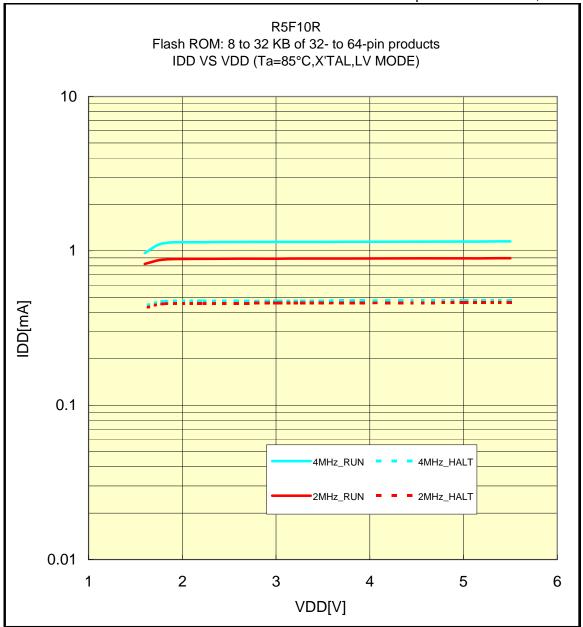




**R5F10R** 

## IDD VS VDD(85°C/X'TAL/LV MODE)

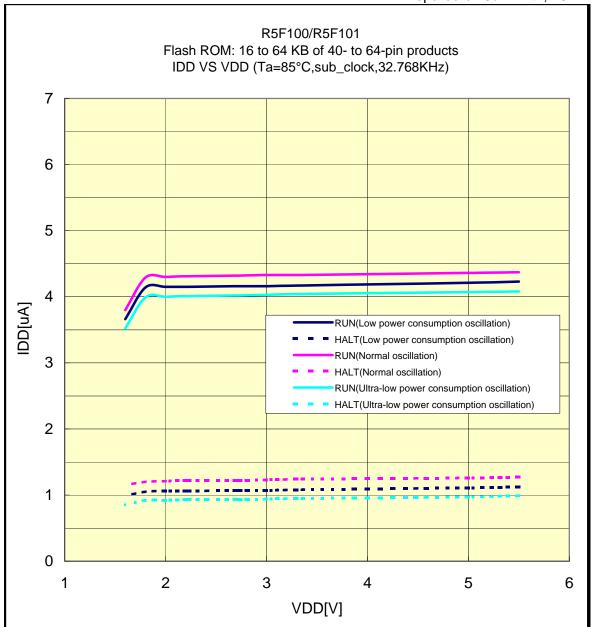




R5F10R Flash ROM: 16 to 64 KB of 40- to 64-pin products

#### IDD VS VDD(85°C/sub\_clock/32.768KHz)

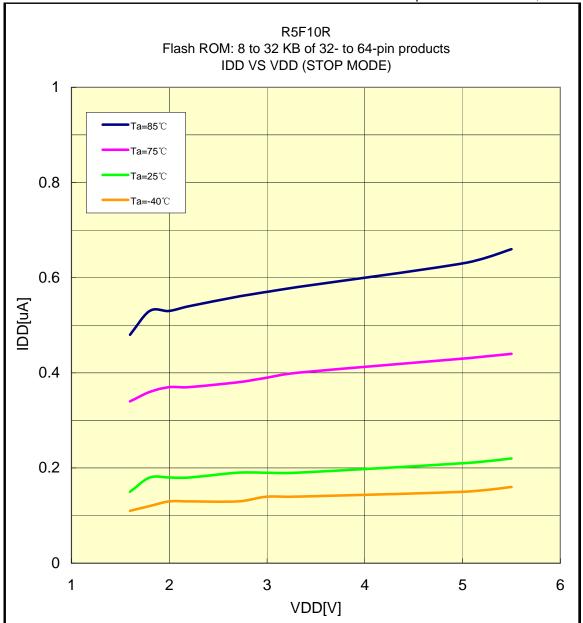




**R5F10R** 

# IDD VS VDD(STOP MODE)





R5F10R

# **IDD VS Ta(STOP MODE)**



