Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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REJ19B0773-0100

RENESAS SINGLE-CHIP 16-BIT MICROCOMPUTER M306N1MCT-XXXFP MASK ROM CONFIRMATION FORM

	Date :	
	Section head	Supervisor
pt	signature	signature
Receipt		
Re		
_		

Note: Please complete all items marked * .

		Company		TEL		Ф	a	Submitted by	Supervisor
*	Customer	name		()	anc	atur		
亦	Odstorrier	Date issued	Date :			Issu	sign		

* 1. Confirmation

Three EPROMs are required for each pattern if this order is performed by EPROMs. One floppy disk is required for each pattern if this order is performed by a floppy disk.

☐ Ordering by EPROMs

If at least two of the three sets of EPROMs submitted contain identical data, we will produce masks based on this data. We shall assume the responsibility for errors only if the mask ROM data on the products we produce differs from this data. Thus, extreme care must be taken to verify the data in the submitted EPROMs.

EPROM type (indicate the type used)

☐ 27C201			□ 27C401		
EPROM ac	ldress		EPROM address		
000016	Product name ASCII code : 'M306N1MCT-'		000016	Product name ASCII code : 'M306N1MCT-'	
000F16 001016			000F16 001016		
1FFFF16 2000016			5FFFF16 6000016		
25555	data ROM (128K) bytes		7FFFF16	data ROM (128K) bytes	
3FFFF16			/FFFF16		

- (1) Set the data in the unused area (the shaded area of the diagram) to "FF16".
- (2) The ASCII codes of the product name "M306N1MCT—" must be entered in addresses 000016 to 000F16. And set the data "FF16" in addresses 000A16 to 000F16.

The ASCII codes and addresses are listed to the right in hexadecimal notation.

Address	
000016	'M' = 4D16
000116	'3' = 3316
000216	'0' = 3016
000316	'6' = 3616
000416	'N' = 4E16
000516	'1' = 31 ₁₆
000616	'M' = 4D16
000716	'C' = 4316

Address	
000816	'T' = 5416
000916	'–' = 2D16
000A16	FF16
000B16	FF16
000C16	FF16
000D16	FF16
000E16	FF16
000F16	FF16

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We recommend the use of the following pseudo-command to set the start address of the assembler source program.

EPROM type	27C201	27C401
The pseudo-command	△.SECTION△ASCIICODE,ROMDATA △.ORG△0C0000H △.BYTE△' M306N1MCT-'	△.SECTION△ASCIICODE,ROMDATA △.ORG△080000H △.BYTE△' M306N1MCT-'

form, the ROM will not be proceed		ie EPRO	JIVIS do	oes not i	match the name of the mask confirmation
assume the responsibility for error this mask file. Thus, extreme care	s only it must b	f the ma	sk RO to ver	M data o	ne mask file generating utility. We shall on the products we produce differs from nask file inthe submitted floppy disk. ormat. And the number of mask files
File code :					(hex)
Mask file name :					.MSK (alpha-numeric 8-digit)
					or the package being ordered. Fill out the he mask ROM confirmation form.
* 3.Usage conditions For our reference to test the part you ordered.	i, please	e fill out	the fo	llowing o	questions about the usage of the products
(1) Type of XIN-XOUT oscillation	circuit				
Ceramic resonator		Qu	artz-cr	ystal oso	cillator
☐External clock input		☐ Oth)
Frequency			`		,
f(XIN) =	МНг				
I(XIIV) =] 1411 12				



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(2) Type of XCIN-XCOUT oscillation circ	uit	
☐ Ceramic resonator	Quartz-crystal oscillator	☐ Not Used
External clock input	Other ()	
Frequency		
f(XCIN) = kHz		
(3) Operation modes		
☐ Single-chip mode	☐ Memory expansion mode	
☐ Microprocessor mode		
(1) 0		
(4) Operating ambient temperature ☐ -10°C~75°C [-40°C~75°C
☐-10°C~85°C		-40°C~85°C
□-10 C~83 C		-40 C~65 C
(5) Operating supply voltage		
☐ 4.2V~4.7V ☐ 4.7	'V~5.2V ☐ 5.2V~5.5V	
(6) CAN controller setting CAN0 Not used Used Transmission rate bps Synchronization Jump Width(SJW) Tq Propagation Time Segment(PTS) Tq Phase Buffer Segment1(PBS1) Tq Phase Buffer Segment2(PBS2) Tq		
Thase build Segmentz(1 bi	S2) Tq	
(7) I ² C (Inter IC) bus function	_	
∐ Not use	Use	
(8)IE (Inter Equipment) bus function		
☐ Not use	Use	
Thank you for your cooperation.		



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Mask ROM number	
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*4. Comments (Indicate none if there is no specific)

