

Renesas Microcomputer All Flash 78K Microcontroller



Renesas Electronics

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Ecological power embracing a world of applications



All our new 8-bit and 16-bit general-purpose microcontrollers



All Flash Continues to Evolve, Contributing to the Success of Customers

Our "All Flash" concept of providing on-chip flash memory throughout our entire lineup of microcontroller products continues to advance. A wide variety of microcontroller products are available with pin counts ranging from 10 to 144 pins and flash memory capacities from 1 KB to 512 KB.

This provides support for software modifications and reduces the total cost of integrating peripheral functions.

Flash microcontrollers from Renesas Electronics combine high performance and low power consumption.

Renesas Electronics also provides a development environment that makes using All Flash microcontrollers simpler and more effective.

Our products and development environments for developers enable our customers to achieve success by exploiting the full potential of our flash memory microcontrollers.

	16-bit Road Map	
78K0R/Kx3 Wide-voltage operation support 144-pin 78K0R/KJ3 128-pin 78K0R/KH3 100-pin 78K0R/KG3 80-pin 78K0R/KF3 64-pin 78K0R/KE3	78K0R/Ix3 Inverter control support 64-pin 78K0R/IE3 52-pin 78K0R/ID3 38/44/48-pin 78K0R/IC3 30-pin 78K0R/IB3	78KOR/Lx3 LCD controller/drivers, analog enhancement, low power 128-pin 78KOR/LH3 100-pin 78KOR/LG3 80-pin 78KOR/LF3
78K0R/Kx3-L Low-power and wide-voltage operation support 100-pin 78K0R/KG3-L 80-pin 78K0R/KF3-L	78K0R/Kx3-C Low-power, digital home electronics communication support 100-pin 78K0R/KG3-C 80-pin 78K0R/KG3-C	μPD78F8043 IO-Link support 56-pin μPD78F8043 μPD78F8058 RF remote control support
64-pin 78K0R/KE3-L 52-pin 78K0R/KD3-L 40/44/48-pin 78K0R/KC3-L	78K0R/Kx3-A Analog enhancement, low power, wide-voltage operation support 64-pin 78K0R/KE3-A	56-pin UPD78F8058 78K0R/Hx3 CAN support, analog enhancement
78K0R/Lx3-M For power meters 100-pin 78K0R/LG3-M	78K0R/Kx3-L (USB) Low-power, USB support 64-pin 78K0R/KE3-L 48-pin 78K0R/KC3-L	100-pin 78K0R/HG3 80-pin 78K0R/HF3 64-pin 78K0R/HE3 48-pin 78K0R/HC3

	8-bit Roa	ad Map	
		78K0S Microcontrollers	78K0S/Kx1+ Low pin count microcontrollers 30/32-pin 78K0S/KB1+ 20-pin 78K0S/KA1+ 16-pin 78K0S/KY1+ 10-pin 78K0S/KU1+
78K0 Microcontrollers	78K0/Lx3 LCD controller/driver 80-pin 78K0/LF3 64-pin 78K0/LE3 52-pin 78K0/LD3 48-pin 78K0/LC3	78K0/Lx3-M For power meters 100-pin 78K0/LG3-M 64-pin 78K0/LE3-M	78K0/Kx2 Wide-voltage operation 80-pin 78K0/KF2 64-pin 78K0/KE2 52-pin 78K0/KD2 38/44/48-pin 78K0/KC2 30/36-pin 78K0/KB2
78K0/Kx2-L Low-power, wide-voltage operation 40/44/48-pin 78K0/KC2-L 30-pin 78K0/KB2-L 20/25/32-pin 78K0/KA2-L 16-pin 78K0/KY2-L	78K0/Kx2-A High-resolution A/D converter, wide-voltage operation 36/48-pin 78K0/KC2-A 30-pin 78K0/KB2-A	78K0/Kx2-C Supports digital home appliance communication 64-pin 78K0/KE2-C 48-pin 78K0/KC2-C	μPD78F8025 LED lighting control 64-pin μPD78F8025
μPD78F071x Inverter motor control 64-pin μPD78F0714 30-pin μPD78F0712 30-pin μPD78F0711	78K0/ix2 Power supplies, lighting inverters, LED lighting control 30/32-pin 78K0/IB2 20-pin 78K0/IA2 16-pin 78K0/IY2	uPD179F1xx For preset remote control 38-pin uPD179F12x 30-pin uPD179F11x	μPD78F0730 USB2.0 function 30-pin μPD78F0730

Application examples

Portable audio, Air conditioners. component stereo systems, home theater systems

LBP. PPC. MFP.

fax machines

Audio

Industrial motors, control equipment, vending machines, power meters

Cameras

SLR cameras

Digital still cameras,

Industrial equipm

digital video cameras,

inkjet printers, scanners,

Blu-ray players, Blu-ray recorders, industrial cameras

refrigerators, washing machines, microwave ovens

All Flash microcontrollers are suitable for various systems using an 8- or 16-bit microcontroller and raise the commercial value of customer systems.



Portable devices PDA, IC recorders

Healthcare equipment

Body fat scales, blood pressure monitors

Other

Electronic instruments. electric bidets, toys, remote controllers, etc. Flash microcontrollers from **Renesas Electronics enable customers** to increase added value throughout the supply chain.



Flash microcontrollers offer overwhelming advantages.

Compared to mask ROM microcontrollers, flash microcontrollers definitely contribute to speeding up system development. Microcontrollers can be ordered before program completion and programs can be written even after the microcontroller has been mounted on the board. Microcontroller order placement and program development can therefore be done concurrently, allowing TAT to be shortened as a result.

In addition, when flash microcontrollers are used for products with many different versions or that are localized for specific regions, the cost of ordering mask ROM microcontrollers is eliminated and purchase and stock management costs can be slashed.

For software designers

Hardware

Software can be changed just before mass production starts and development TAT can also be shortened.



Since mask ROM microcontrollers cannot be ordered until their specifications are finalized, last-minute software changes can be problematic. On the other hand, specifications for flash microcontrollers can be changed just prior to the start of mass production. Thus orders for flash microcontrollers can be placed while the software is still being developed, allowing the development TAT to be shortened.





For manufacturing divisions



for each type of product. In contrast, mass-produced flash microcontrollers facilitate the sharing of parts since they can be used for various products by simply rewriting the software

Merits of flash microcontrollers

Renesas Electronics delivers "All Flash" microcontrollers you can count on to boost the competitiveness of your products.

"Products you can count on" is the concept.



More and more manufacturers are adopting high-performance flash microcontrollers as an effective way to achieve better system performance and shorten development cycles. Gaining improved performance and flash memory used to involve compromises, however, such as increased power consumption and incompatibility

Selection you can count on

Broad lineup of 293 8-bit and 200 16-bit microcontroller products, for a total of 493!

To meet the full range of customer requirements, Renesas Electronics offers an All Flash lineup consisting of 293 8-bit and 200 16-bit microcontroller products available in a variety of pin counts, ROM capacities, and package configurations. Our 8-bit microcontrollers such as the 78K0/Kx2, 78K0/Kx2-A, and uPD78F8025 achieve operation speeds up to 20 MHz, while our 78K0R/Hx3 16-bit microcontroller delivers an operation speed of 24 MHz. Products such as the 78K0/Kx2, 78K0/Kx2-L, 78K0/Kx3, 78K0B/Kx3, 78K0R/Kx3-L, and 78K0R/Lx3 support power supply voltages ranging from 1.8 V to 5.5 V. The 78K0/Kx2-A and 78K0R/Kx3-A are provided with a high-performance 12-bit A/D converter, while the 78K0/Lx3 and 78K0R/Lx3 feature an on-chip LCD driver. Package options include the compact SSOP with low pin counts of 16, 20, or 30 pins. The WFQN package measures 5 × 5 mm in the 32-pin version and 6 × 6 mm in the 40-pin version. These dimensions are up to 46% thinner and realize a package area up to 87% smaller than earlier Renesas Electronics products (80-pin LQFP, 14 × 14 mm). The smaller mounting area contributes to a smaller system size overall. The extensive lineup makes it possible to choose a product that best fits the requirements of the specific application.

Low cost you can count on

Reducing the total cost!

The 78K0R 16-bit microcontrollers are provided with features such as flash memory instead of EEPROM, an oscillator, a voltage detector, and a power-on reset function. The number of components used and the system costs can be reduced in contrast to products not provided with these features. Also, costs can be further reduced because the 78K0R/Kx3-A and 78K0R/Lx3, 78K0/Kx2-A, Kx2-L*3, Ix2*4 include an operational amplifier, and the 78K0R/KC3-L, 78K0R/KD3-L, 78K0R/KE3-L, and 78K0R/Ix3 include a programmable gain amplifier and a comparator.

Low power consumption you can count on

High functionality combined with low power consumption

The 78K0R achieves performance of 30.5 MIPS at 24 MHz*1 through the use of a 16-bit CPU with a 3-stage pipeline architecture. The low power supply current compared with competing products provides improved energy efficiency.

Approx. 1/3 the power consumption of mask ROM products

Compared with the 7.6 mA operating current of a conventional mask ROM microcontroller operating at 5 V/10 MHz (external ceramic resonator), the 78K0/Kx2, 78K0/Kx2-L, and 78K0/Kx2-A, operating under the same conditions, have an operating current of only 2.3 mA (1.9 mA for the 78K0/Lx3) at 10 MHz (external ceramic resonator) and 1.4 mA (1.3 mA for the 78K0/Kx2-L) at 8 MHz (on-chip oscillator). The 78K0 delivers significantly lower power consumption than conventional mask ROM products.

with existing software. Renesas Electronics overcomes these issues, utilizing innovative technologies to deliver microcontrollers you can count on.



Total cost reduction through embedded peripheral IC functions



*1. 78K0R/Kx3-A, 78K0R/Lx3only *2. 78K0R/KC3-L, 78K0R/KD3-L, 78K0R/KE3-L, 78K0R/Ix3only *3. 78K0/KY2-L (µPD78F0555, 0556, 0557), 78K0/KA2-L (µPD78F0565, 0566, 0567), 78K0/KB2-L (uPD78F0576, 0577, 0578) 78K0/KC2-L (uPD78F0586, 0587, 0588) only *4. 78K0/ΙΥ2 (μPD78F0750, 0751, 0752), 78K0/ΙΑ2 (μPD78F0753, 0754), 78K0/ΙΒ2 (μPD78F0755, 0756) only





Realization of lower power consumption than mask ROM products

Condition (5 V power supply voltage)		Comparison with Renesas Electronics products Operating current
Mask ROM products	External ceramic resonator 10 MHz	7.6 mA
Flash memory 78K0/Kx2, 78K0/Kx2-L, 78K0/Kx2-A	External ceramic resonator 10 MHz	2.3 mA *1. Dhrystone 2.1 *2. When using the
78K0/Lx3	External ceramic resonator 10 MHz	1.9 mA 78K0R/KC3-L, 78K0R/KD3-L.
78K0/Kx2, 78K0/Kx2-A, 78K0/Lx3	Internal oscillator 8 MHz	1.4 mA 78K0R/KD3-L, 78K0R/KE3-L,
78K0/Kx2-L	Internal oscillator 8 MHz	1.3 mA 78K0R/Lx3, or 78K0R/Kx3-A

Broad range of products for specific applications you can count on

We offer ideal products for various applications!

Renesas Electronics offers a wide range of products for specific applications. These include the 78K0R/Kx3-C and 78K0/Kx2-C with functionality for linking digital AV devices, the 78K0R/Ix3 with on-chip multifunction timers for precision inverter control, the 78K0R/Lx3-M and 78K0/Lx3-M for power meters, the µPD78F8043 with an on-chip IO-Link transceiver for easy communication with industrial systems, and the µPD179F1xx for use in remote controls for home electronics products. These microcontrollers offer a rich selection of specialized functions in addition to basic functionality, so you can choose a product that is ideal for the specific application

High performance and functionality you can count on

Includes high-performance CPU and sophisticated peripheral functions!

The 78K0R microcontrollers execute most instruction processing in one clock via three-stage pipeline control. 32-bit (16 bits × 16 bits) calculations can also be performed thanks to the on-chip multiplier/divider. Furthermore, a sophisticated timer function can be realized by interlocking the operation of multiple-channel timers. The 78K0R/Ix3 enables A/D conversion in synchronization with 3 phase sine-wave PWM output and timers.

High reliability you can count on

Our products incorporate our experience and technology in the automotive field as well as software protection functions!

All our products incorporate the experience we have gained in the process of supplying microcontrollers for over 1,000 types of applications and the technology we developed for flash microcontrollers for the automotive field. Our products also feature functions that disable reading and malicious software rewriting and erasing, thus offering maximum protection of your valuable software



We offer tools that are inexpensive, simple, and easy to use!

Renesas Electronics offers development tools that are simple and convenient to use. The new CubeSuite+ integrated development environment provides compiler and debugger components as well as functions for managing pin assignments, generating program code for microcontroller peripheral functions, and building projects at high speed. When combined with hardware such as the E1 on-chip debugging emulator with built-in flash memory programming, CubeSuite+ provides powerful support for speedy system development.

Support for mass production vou can count on

In addition to a large lineup of programming tools, we also offer programming services!

Renesas Electronics and partner manufacturers offer a large number of programming tools, making programming possible in many different settings such as development environments and production lines. Moreover, programming services are also available from partner manufacturers both in Japan and overseas, serving a broad range of needs such as large-volume programming after shipping.





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Select the best flash microcontroller for your product or application.

Commercial Name	78K0R/	′KC3-L	78K0R/KD3-L	78K0R/KE3-L	78K0R/KF3-L	78K0R/KG3-L	78K0R/KC3-L	78K0R/KE3-L	78K0R/KE3-A	78K0R/KE3	78K0R/KF3	78K0R/KG3	78K0R/KH3	78K0R/KJ3
Pin Count ROM (bytes)	40/44-pin	48-pin	52-pin	64-pin	80-pin	100-pin	48-pin	64-pin	64-pin	64-pin	80-pin	100-pin	128-pin	144-pin
512 K										78K0R/Kx3		µPD78F1168A (30 K*¹)	μPD78F1178A (30 K*1)	μPD78F1188A (30 K*1)
384 K										Microcontrollers		μPD78F1167A (24 K)	μPD78F1177A (24 K)	μPD78F1187A (24 K)
256 K					μPD78F1028 (12 K*²)	μPD78F1030 (12 K*²)			12-bit A/D Converter 78K0R/Kx3-A	μPD78F1146A (12 K*³)	µPD78F1156A (12 K*³)	μPD78F1166A (12 K*³)	μPD78F1176A (12 K)	μPD78F1186A (12 K)
192 K		78K0R/K	3-L Microc	ontrollers	μPD78F1027 (10 K)	μPD78F1029 (10 K)	78K0R/K Microco		Microcon- trollers	μPD78F1145A (10 K)	μPD78F1155A (10 K)	μPD78F1165A (10 K)	μPD78F1175A (10 K)	μPD78F1185A (10 K)
128 K					μPD78F1012 (8 K*4)	µPD78F1014 (8 K*⁴)	µPD78F1024 (8 K*4)	µPD78F1026 (8 K*4)	μPD78F1018 (7 K)	μPD78F1144A (8 K)	μPD78F1154A (8 K)	μPD78F1164A (8 K)	μPD78F1174A (8 K)	μPD78F1184A (8 K)
96 K					μPD78F1011 (6 K)	μΡD78F1013 (6 K)	µPD78F1023 (8 K*⁴)	μPD78F1025 (8 K*4)	μPD78F1017 (6 K)	μΡD78F1143A (6 K)	μPD78F1153A (6 K)	μΡD78F1163A (6 K)		
64 K	µPD78F1003 (3 K*⁵)	µPD78F1003 (3 K*⁵)	µPD78F1006 (3 K*⁵)	µPD78F1009 (3 K*⁵)	μPD78F1010 (4 K)		μPD78F1022 (6 K)		μPD78F1016 (4 K)	μΡD78F1142A (4 K)	μPD78F1152A (4 K)	μPD78F1162A (4 K)		
48 K	μPD78F1002 (2 K)	μPD78F1002 (2 K)	μPD78F1005 (2 K)	μPD78F1008 (2 K)										
32 K	μPD78F1001 (1.5 K)	μPD78F1001 (1.5 K)	μPD78F1004 (1.5 K)	μPD78F1007 (1.5 K)										
16 K	μPD78F1000 (1 K)													
Package	44-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.80 mm 40-pin WQFN (K8) Thickness: 0.75 mm 6 × 6 mm Pitch: 0.50 mm	7 × 7 mm Pitch: 0.50 mm 48-pin WQFN (K8)	52-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.65 mm	64-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.65 mm Pitch: 0.65 mm 10 × 10 mm Pitch: 0.50 mm 10 × 10 mm Pitch: 0.50 mm 64-pin TQFP (GA) Thickness: 1.00 mm 7 × 7 mm Pitch: 0.40 mm 7 × 7 mm Pitch: 0.40 mm 64-pin FBGA (F1) Thickness: 0.69 mm 4 × 4 mm Pitch: 0.40 mm	80-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.65 mm 80-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.50 mm	100-pin LQFP (GF) Thickness: 1.40 mm 14 × 20 mm Pitch: 0.65 mm 100-pin LQFP (GC) 100-pin FBGA*6 (F1) Thickness: 1.40 mm 14 × 14 mm 14 × 14 mm 14 × 14 mm 14 × 10 mp 14 × 14 mm 14 × 10 mp 14 × 10 mp 14 × 10 mp 10 -pin FBGA*6 (F1) Thickness: 0.69 mm 6 × 6 mm Pitch: 0.50 mm	7 x 7 mm Pitch: 0.50 mm	64-pin LQFP (GB) Thickness: 1.40 mm Pitch: 0.50 mm Pitch: 0.50 mm 64-pin TQFP (GA) Thickness: 1.00 mm 7 × 7 mm Pitch: 0.40 mm 64-pin FBGA (F1) Thickness: 0.91 mm 5 × 5 mm Pitch: 0.50 mm	Thickness: 1.11 mm 6 × 6 mm Pitch: 0.65 mm	64-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.65 mm 64-pin TQFP (GA) 64-pin TQFP (GA) Thickness: 1.00 mm 7 × 7 mm Pitch: 0.40 mm 7 × 7 mm Pitch: 0.40 mm 64-pin FBGA (F1) Thickness: 1.11 mm 6 × 6 mm Pitch: 0.65 mm		100-pin LQFP (GF) Thickness: 1.40 mm 14 × 20 mm Pitch: 0.65 mm 100-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.50 mm	128-pin LQFP (GF) Thickness: 1.40 mm 14 × 20 mm Pitch: 0.50 mm	144-pin LQFP (GJ) Thickness: 1.40 mm 20 × 20 mm Pitch: 0.50 mm

*1. 28 KB when the self programming function is used.
*2. 11 KB when the self programming function is used.
*3. 10 KB when the self programming function is used.
Remarks The packages are shown in their actual size.

*4.7 KB when the self programming function is used. *5.2 KB when the self programming function is used. *6. $\mu PD78F1013,78F1014only$

Legend

μPD78F1188A (30 K*1) Top: Product name Bottom: RAM (bytes)

Select the best flash microcontroller for your product or application.

Commercial Name	78K0R/KF3-C	78K0R/KG3-C	78K0R/HC3	78K0R/HE3	78K0R/HF3	78K0R/HG3	µPD78F8043	µPD78F8058	78K0R/IB3		78K0R/IC3		78K0R/ID3	78K0R/IE3	78K0R/LF3	78K0R/LG3	78K0R/LH3	78K0R/LG3-M*1
Pin Count ROM (bytes)	80-pin	100-pin	48-pin	64-pin	80-pin	100-pin	56-pin	56-pin	30-pin	38-pin	44-pin	48-pin	52-pin	64-pin	80-pin	100-pin	128-pin	100-pin
512 K																		
384 K			78K0	OR/Hx3 Mic	crocontrolle	ers												
256 K		/Kx3-C	μPD78F1035*1 (16 K)	μPD78F1040*1 (16 K)	μPD78F1045*1 (16 K)	µPD78F1050*1 (16 K)	Microcontroller with On-Chip	Micro- controller								78K0R/Lx	3	78K0R/ Lx3-M
192 K		ontrollers / Applications	μPD78F1034*1 (12 K)	μPD78F1039*1 (12 K)	μPD78F1044*1 (12 K)	μPD78F1049*1 (12 K)	IO-Link	with On-Chip RF Transceiver							N	licrocontrol		Micro- controllers for Power Meters
128 K			μPD78F1033*1 (8 K)	μPD78F1038*1 (8 K)	µPD78F1043*1 (8 K)	µPD78F1048*1 (8 K)	μPD78F8043 (7 K)	μPD78F8058*1 (8 K*²)			78K0 Microco				μPD78F1502A, μPD78F1512A (7 K)	μPD78F1505A, μPD78F1515A (7 K)	μPD78F1508A, μPD78F1518A (7 K)	μPD78F8070 (7 K)
96 K	μPD78F1847A (8 K*²)	µPD78F1849A (8 K*²)	μPD78F1032*1 (6 K)	μPD78F1037*1 (6 K)	µPD78F1042*¹ (6 K)	μPD78F1047*1 (6 K)	μPD78F8042 (6 K)	μPD78F8057*1 (8 K*²)			for Inverte				μPD78F1501A (6 K)	μPD78F1504A (6 K)	μPD78F1507A (6 K)	
64 K	μPD78F1846A (6 K)	µPD78F1848A (6 K)	μPD78F1031*1 (4 K)	μPD78F1036*1 (4 K)	μPD78F1041*1 (4 K)	μPD78F1046*1 (4 K)	μPD78F8041 (4 K)	µPD78F8056*1 (8 K*2)				μPD78F1215 (3 K*³)	μPD78F1225 (3 K*³)	µPD78F1235 (3 K*³)	μPD78F1500A, μPD78F1510A (4 K)	μPD78F1503A, μPD78F1513A (4 K)	μPD78F1506A, μPD78F1516A (4 K)	
48 K												μPD78F1214 (2 K)	μPD78F1224 (2 K)	μPD78F1234 (2 K)				
32 K							μPD78F8040 (4 K)		μPD78F1203 (1.5 K)	μPD78F1213 (1.5 K)	μPD78F1213 (1.5 K)	μPD78F1213 (1.5 K)	μPD78F1223 (1.5 K)	μPD78F1233 (1.5 K)				
16 K									μPD78F1201 (1 K)	µPD78F1211 (1 K)	μPD78F1211 (1 K)							
			48-pin LQFP (GA) Thickness: 1.40 mm 7 × 7 mm Pitch: 0.50 mm			100-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.50 mm	56-pin WQFN (K8) Thickness: 0.75 mm 8 × 8 mm Pitch: 0.50 mm			38-pin SSOP (MC) Thickness: 1.70 mm 7.62 mm (300) Pitch: 0.65 mm			52-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.65 mm	64-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.65 mm	80-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.65 mm	100-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.50 mm	128-pin LQFP (GF) Thickness: 1.40 mm 14 × 20 mm Pitch: 0.50 mm	100-pin LQFP Thickness: 1.40 mm 14×14 mm Pitch: 0.50 mm
			•		•				•	•		•						
Package							56-pin FBGA*1 (F1) Thickness: 0.91 mm 4 x 7 mm Pitch: 0.50 mm							64-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.50 mm				

*1. Under development
*2. 7 KB when the self programming function is used.
*3. 2 KB when the self programming function is used.
Remarks The packages are shown in their actual size.

Legend

μPD78F1188A (30 K^{*1}) Top: Product name Bottom: RAM (bytes)

Select the best flash microcontroller for your product or application.

Commercial Name	78K0S/KU1+	78K0S/KY1+	78K0S/KA1+	78K0S/KB1+	78K0/KB2		78K0/KC2		78K0/KD2	78K0/KE2	78K0/KF2
Pin Count ROM (bytes)	10-pin	16-pin	20-pin	30/32-pin	30/36-pin	38-pin	44-pin	48-pin	52-pin	64-pin	80-pin
128 K									μΡD78F0527A, μΡD78F0527DA*² (7 K)	μPD78F0537A, μPD78F0537DA*2 (7 K)	μPD78F0547A, μPD78F0547DA*² (7 K)
96 K									μPD78F0526A (5 K)	μPD78F0536A (5 K)	μΡD78F0546A (5 K)
60 K								μPD78F0515A, μPD78F0515DA* ² (3 K)	μΡD78F0525A (3 K)	μPD78F0535A (3 K)	μPD78F0545A (3 K)
48 K								μPD78F0514A (2 K)	μΡD78F0524A (2 K)	μPD78F0534A (2 K)	μPD78F0544A (2 K)
32 K					μPD78F0503A, μPD78F0503DA*2 (1 K)	μPD78F0513A, μPD78F0513DA*² (1 K)	μPD78F0513A, μPD78F0513DA*2 (1 K)	μPD78F0513A (1 K)	μΡD78F0523A (1 K)	μΡD78F0533A (1 K)	
24 K			+ Microcontrollers Int Microcontrollers)		μPD78F0502A (1 K)	μPD78F0512A (1 K)	μPD78F0512A (1 K)	μPD78F0512A (1 K)	μΡD78F0522A (1 K)	μΡD78F0532A (1 K)	
16 K					μPD78F0501A (768)	µPD78F0511A (768)	μPD78F0511A (768)	μPD78F0511A (768)	μΡD78F0521A (768)	μPD78F0531A (768)	
8 K			μPD78F9224 (256)	μPD78F9234 (256)	μPD78F0500A (512)						
4 K	μPD78F9202, μPD78F9502 (128)	μPD78F9212, μPD78F9512 (128)	μPD78F9222 (256)	μPD78F9232 (256)					78K0/Kx2 Microcontr	ollers	
2 K	μPD78F9201, μPD78F9501 (128)	μPD78F9211, μPD78F9511 (128)	μPD78F9221 (128)								
1 K	μPD78F9200, μPD78F9500 (128)	μPD78F9210, μPD78F9510 (128)									
Package	Thickness: 1.20 mm 5.72 mm (225) Pitch: 0.65 mm	16-pin SDIP*1 (CS) 16-pin SSOP*1 (MA) Thickness: 2.80 mm Thickness: 1.50 mm 7.62 mm (300) 5.72 mm (225) Pitch: 1.778 mm 5.72 mm (225) Pitch: 0.65 mm 16-pin WLBGA*1 (FH) Thickness: 1.44 mm Thickness: 0.40 mm 5.72 mm (225) 2 x 2.3 mm Pitch: 0.65 mm Pitch: 0.50 mm	20-pin SDIP (CS) Thickness: 2.80 mm 7.62 mm (300) Pitch: 1.778 mm 20-pin SSOP (MC) Thickness: 1.20 mm 7.62 mm (300) Pitch: 0.65 mm	32-pin SDIP (CS) Thickness: 2.80 mm 7.62 mm (300) Pitch: 1.778 mm 30-pin SSOP (MC) Thickness: 1.20 mm 7.62 mm (300) Pitch: 0.65 mm	30-pin SSOP (MC) Thickness: 1.20 mm 7.62 mm (300) Pitch: 0.65 mm 36-pin FLGA (FC) Thickness: 0.91 mm 4 × 4 mm Pitch: 0.50 mm	38-pin SSOP (MC) Thickness: 1.70 mm 7.62 mm (300) Pitch: 0.65 mm	44-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.80 mm	48-pin LQFP (GA) Thickness: 1.40 mm 7 × 7 mm Pitch: 0.50 mm	52-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.65 mm	64-pin LQFP (GK) 64-pin LQFP (GK) 64-pin LQFP (GK) 114 × 14 mm 12 × 12 mm 10 × 10 mm 12 × 12 mm 10 × 10 mm Pitch: 0.80 mm Pitch: 0.65 mm Pitch: 0.50 mm 64-pin TQFP (GK) 64-pin FLGA (FC) 64-pin FPBGA (F1) Thickness: 1.00 mm 7 × 7 mm Pitch: 0.50 mm 7 × 7 mm Pitch: 0.40 mm 5 × 5 mm 4 × 4 mm Pitch: 0.40 mm 5 × 5 mm 4 × 4 mm Pitch: 0.40 mm Fitch: 0.50 mm Pitch: 0.40 mm	80-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.65 mm 80-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.50 mm

*1. µPD78F9210, 78F9211, 78F9212only
 *2. Supports on-chip debugging of 78K0/Kx2
 Remarks The packages are shown in their actual size.

Legend

μPD78F1188A (30 K^{*1}) Top: Product name Bottom: RAM (bytes)

Select the best flash microcontroller for your product or application.

Commercial Name	78K0/KY2-L	78K0/KA2-L	78K0/KB2-L	78K0/KC2-L	78K0/KB2-A	78K0/KC2-A	78K0/KC2-C	78K0/KE2-C	µPD17	9F1xx	µPD78F0730
Pin Count ROM (bytes)	16-pin	20/25/32-pin	30-pin	40/44/48-pin	30-pin	36/48-pin	48-pin	64-pin	30-pin	38-pin	30-pin
128 K											
96 K											
60 K							μPD78F0762 (3 K)	μΡD78F0765 (3 K)			
48 K							μPD78F0761 (2 K)	μΡD78F0764 (2 K)			
32 K			μPD78F0573, μPD78F0578 (1 K)	μΡD78F0583, μΡD78F0588 (1 K)	μPD78F0591 (1 K)	μΡD78F0593 (1 K)	μPD78F0760 (1 K)	μΡD78F0763 (1 K)	μΡD179F114 (1 K)	μΡD179F124 (1 K)	
24 K								Kx2-C ntrollers	μΡD179F113 (1 K)	μΡD179F123 (1 K)	
16 K	μPD78F0552, μPD78F0557 (768)	μΡD78F0562, μΡD78F0567 (768)	μPD78F0572, μPD78F0577 (768)	μΡD78F0582, μΡD78F0587 (768)	μPD78F0590 (1 K)	μPD78F0592 (1 K)	for Digital AV	Applications	μPD179F112 (768)	μPD179F122 (768)	μΡD78F0730 (3 K)
8 K	μPD78F0551, μPD78F0556 (512)	μPD78F0561, μPD78F0566 (512)	μPD78F0571, μPD78F0576 (512)	μPD78F0581, μPD78F0586 (512)	12-bit A/D Converter 78K0/Kx2-A				μPD179F111 (512)		
4 K	μPD78F0550, μPD78F0555 (384)	μPD78F0560, μPD78F0565 (384)			Microcontrollers				μPD179F110 (512)		USB
2 K									Microcont Preset Rem		Micro- controllers
1 K		78K0/Kx2-L M	icrocontrol	lers							
Package		20-pin SSOP (MC) 32-pin WQFN (K8) Thickness: 1.20 mm Thickness: 0.75 mm 7.62 mm (300) 5 × 5 mm Pitch: 0.65 mm Pitch: 0.50 mm 25-pin FLGA (FC) Thickness: 0.69 mm 3 × 3 mm Pitch: 0.50 mm		44-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.80 mm 48-pin LQFP (GA) Thickness: 1.40 mm 7 × 7 mm Pitch: 0.50 mm 40-pin WQFN (K8) Thickness: 0.75 mm 6 × 6 mm Pitch: 0.50 mm	30-pin SSOP (MC) Thickness: 1.20 mm 7.62 mm (300) Pitch: 0.65 mm	48-pin LQFP (GA) Thickness: 1.40 mm 7 × 7 mm Pitch: 0.50 mmImage: Image: Imag	48-pin LQFP (GA) Thickness: 1.40 mm 7 × 7 mm Pitch: 0.50 mm	64-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.50 mm	30-pin SSOP (MC) Thickness: 1.20 mm 7.62 mm (300) Pitch: 0.65 mm	38-pin SSOP (MC) Thickness: 1.70 mm 7.62 mm (300) Pitch: 0.65 mm	30-pin SSOP (MC) Thickness: 1.20 mm 7.62 mm (300) Pitch: 0.65 mm

Remarks The packages are shown in their actual size.

L	е	g	е	n	d	

μPD78F1188A (30 K^{*1}) Top: Product name Bottom: RAM (bytes)

Select the best flash microcontroller for your product or application.

Commercial Name	78K0/IY2	78K0/IA2	78K0/IB2	μPD78F8025		μPD78F071x	78K0/LC3	78K0/LD3	78K0/LE3
Pin Count ROM (bytes)	16-pin	20-pin	30/32-pin	64-pin	30-pin	64-pin	48-pin	52-pin	64-pin
128 K									
96 K									
60 K									μPD78F0445, μPD78F0455, μPD78F0465 (2 K)
48 K		78K0/lx2							μΡD78F0444, μΡD78F0454, μΡD78F0464 (2 K)
32 K	for Power S	crocontroll upplies,Lightii	ng Inverters,	μΡD78F8025 (1 K)		μPD78F0714 (1 K)	μPD78F0403, μPD78F0413 (1 K)	μΡD78F0423, μΡD78F0433 (1 K)	μΡD78F0443, μΡD78F0453, μΡD78F0463 (1 K)
24 K	and L	ED Lighting C	ontrol				μPD78F0402, μPD78F0412 (1 K)	μΡD78F0422, μΡD78F0432 (1 K)	μΡD78F0442, μΡD78F0452, μΡD78F0462 (1 K)
16 K	μPD78F0742, μPD78F0752 (768)	μPD78F0744, μPD78F0754 (768)	μPD78F0746, μPD78F0756 (768)	μPD78F8024 (512)	μPD78F0712 (768)		μPD78F0401, μPD78F0411 (768)	μPD78F0421, μPD78F0431 (768)	μΡD78F0441, μΡD78F0451, μΡD78F0461 (768)
8 K	μPD78F0741, μPD78F0751 (512)	μPD78F0743, μPD78F0753 (512)	μPD78F0745, μPD78F0755 (512)	Microcontrollers	μPD78F0711 (768)		μPD78F0400, μPD78F0410 (512)	μPD78F0420, μPD78F0430 (512)	
4 K	μPD78F0740, μPD78F0750 (384)			for LED Lighting Control				78K0/Lx3	Microcontrollers
2 K						crocontrollers for Inverter MotorControl			
1 K									
Package	16-pin SSOP (MA) Thickness: 1.50 mm 5.72 mm (225) Pitch: 0.65 mm	20-pin SSOP (MC)	7.62 mm (300) Pitch: 0.65 mm	64-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.65 mm	30-pin SSOP (MC) Thickness: 1.20 mm 7.62 mm (300) Pitch: 0.65 mm	64-pin TQFP (GK) Thickness: 1.00 mm 12 × 12 mm Pitch: 0.65 mm	48-pin LQFP (GA) Thickness: 1.40 mm 7 × 7 mm Pitch: 0.50 mm	52-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.65 mm	64-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.65 mm Pitch: 0.50 mm 10 × 10 mm 10 × 10 mm Pitch: 0.50 mm Pitch: 0.50 mm 0 × 10 mm 10 × 10 mm Pitch: 0.50 mm Pitch: 0.50 mm 7 × 7 mm Pitch: 0.40 mm 7 × 7 mm Pitch: 0.40 mm

*1. Under development
 *2. μPD78F044x, 78F045x only
 Remarks The packages are shown in their actual size.

1.		-		
_	eg	er	10	

µPD78F1188A
(30 K*1)

Top: Product name Bottom: RAM (bytes)

78K0/LF3	78K0/LE3-M ⁻¹	78K0/LG3-M ^{∗1}
80-pin	64-pin	100-pin
μΡD78F0485, μΡD78F0495, μΡD78F0475 (2 K)		μPD78F8055 (2 K)
μΡD78F0484, μΡD78F0494, μΡD78F0474 (2 K)		μPD78F8054 (2 K)
μΡD78F0483, μΡD78F0493, μΡD78F0473 (1 K)	μPD78F8053 (1 K)	
μΡD78F0482, μΡD78F0492, μΡD78F0472 (1 K)		
μΡD78F0481, μΡD78F0491, μΡD78F0471 (768)	μPD78F8052 (768)	
	Microco	/Lx3-M ontrollers er Meters
80-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.65 mm 80-pin LQFP (GK) Thickness: 1.40 mm 12 × 12 mm Pitch: 0.50 mm	64-pin LQFP (GB) Thickness: 1.40 mm 10 × 10 mm Pitch: 0.50 mm	100-pin LQFP (GC) Thickness: 1.40 mm 14 × 14 mm Pitch: 0.50 mm

All the required peripheral functions are provided on chip, saving you money and space.

Total cost reduction achieved through the following on-chip peripheral functions



Power consumption vs. frequency 4.35 Power consumption vs. CPU performance (at 1 MHz operation) [mW/MHz] [mA/MIPS] 0.57 0.21 78K0R 78K0R "A" company's 16-bit MCU (78K0R/KE3-L Lowered standby power consumption realized through lower standby current and enhanced watch count function As a result, energy saving for applications and longer battery life can be achieved. Standby power мси Reset IC Real-time clock IC Conventional 3.0 µA MCUs 8K0R/Kx3-78K0B/Kx3-0 **Power-on reset function** 1.0 µA 78K0B/lx3 provided, but current is suppressed at this level 78K0R/Kx3-A 0.9 µA 78K0R/Lx3 Function that implements low power consumption has been added RTC (real-time counter)

• No need for updating with the CPU! Calendar function for automatic updating until 2099. Sustained watch operation without wakeup! Power consumption can be reduced.
Built-in alarm function starts the microcontroller at an arbitrary set time (day, hour, minute) (1) Ð Conventi watch tim 0.5 s ma RTC

Low power consumption you can count on (16-bit)

16-bit microcontroller performance combined with low power consumption Sophisticated application functions can be realized while maintaining low power consumption.

World's lowest power consumption for 16-bit microcontrollers



Remarks At 3 V power supply Renesas Electronics measurement values





The low power consumption is comparable to that of conventional mask ROM products, allowing you to build more eco-friendly systems.

Low power consumption comparable to that of mask ROM products



The internal oscillator allows fast startup, eliminating the need for oscillation wait time and reducing average power consumption.

Power-consumption reduction achieved by fast startup



Microcontroller for inverter control (78K0R/Ix3)

1. On-chip multi-function timer enabling fine inverter control

Twelve timer channels each having a 16-bit counter and a capture/compare register are provided in one unit. In addition to individual timer operations, multiple channels can be operated in conjunction to enable fine inverter control. Various waveforms can also be output.

> Example 1: [6-phase triangular wave PWM output function (with dead time)] (180° excitation) Controllable motors: Brushless DC motors, AC motors



Example 2: [Non-complementary method modulation output function] (120° excitation) Controllable motors: Brushless DC motors



2. System minimization and cost reductions realized by incorporating circuits required for motor control

Example: Refrigerator



The circuits required for inverter control, such as the amplifier, comparators, noise filters, and A/D converter which were conventionally provided as external circuits, have been incorporated into the microcontroller. The number of components has been reduced to achieve system minimization and reduce costs.

Broad range of products for specific applications you can count on (1/4)

Broad range of products for specific applications you can count on (2/4)



Microcontroller for industrial system sensors (µPD78F8043)

Renesas Electronics has commercialized the mPD78F8043, a 16-bit 78K0R microcontroller with an on-chip transceiver that can communicate with IO-Link devices. By using the mPD78F8043, you can build a sophisticated sensor network. We have also provided a software stack to help you develop your system more efficiently.

1. IO-Link

Many industrial systems today include controllers that operate in combination with multiple sensors and actuators. To respond to the increasing sophistication of these sensors and actuators, today's industrial systems must have capabilities such as acquiring quantitative data using digital communication as well as diagnostic features. IO-Link is a new and popular standard for standardizing communication between the controllers and sensors & actuators in industrial systems.

2. Features of IO-Link

- Used to connect controllers to sensors and actuators in industrial systems. • Complies with the IEC61131-2 standard.
- · Supports asynchronous serial communication and pulse modulation.
- · Supports transmission and reception of quantitative data and parameters, and self-diagnosis.
- Maximum communication rate: 230.4 kpbs
- Point-to-point connection
- Operating mode can be switched between IO-Link communication mode and standard I/O mode.
- Existing cables (M12, etc.) can be used.

3. µPD78F8043 microcontroller with on-chip IO-Link transceiver

- •A 16-bit 78K0R microcontroller with an on-chip IO-Link device transceiver . Includes a DMA controller to reduce the software load when transferring data.
- Has overcurrent and wakeup detection capabilities.
- We provide a software stack for IO-Link communication that lets you concentrate on developing your application.

RF microcontroller (µPD78F8058)

1. Microcontroller and RF transceiver integrated into a single package

The mPD78F8058 integrates a 16-bit microcontroller and 2.4 GHz RF transceiver into a single package. Now you can design your system without having to add an external RF transceiver. Your system will have fewer components and can be made much smaller

RF transceiver specifications

- O Complies with IEEE802.15.4-2006 (modulation system:
- O-QPSK, spread system: DSSS, communication rate: 250 kbps) O PHY block
- •16 channels operating in a 2.405 to 2.480 GHz ISM band Sensitivity: -95 dBm, input level: 3 dBm (max.)
- RSSI (received signal strength indicator) ADC
- and I/Q (in-phase/quadrature phase) DAC included
- Auto ACK response
- Security engine

2. Supportive development environment

Renesas Electronics provides an RF transceiver-compliant starter kit-the TK-RF8058+SB (from TESSERA Technology Inc.)-which you can use to develop a small-scale, low-power wireless communication system. See Connecting (ZigBee®) on the Application examples page for details.

3. Example applications

Wireless remote control (RF4CE compliant) Digital TVs





Broad range of products for specific applications you can count on (3/4)



CEC-78K0/KE2C*

By using CEC, multiple AV devices can be controlled by using one remote controller

Remarks HDMI (High-Definition Multimedia Interface): Standardized digital audio/video I/O interface for home electronics and AV devices. CEC (Consumer Electronics Control): Control protocol (control method) for device control signals standardized by HDMI.

CEC function development software

window screenshots

Microcontrollers for power meters (78K0R/Lx3-M, 78K0/Lx3-M)

All the functions required by a single-phase power meter on a single chip

Renesas Electronics delivers on a single chip all the functions required to realize a single-phase power meter, making it possible to reduce system size. Extensive peripherals also mean that the 78K0R/Lx3-M and 78K0/Lx3-M can be used for a variety of power meter applications.



- 2 channels for current and 2 channels for voltage*1 High-resolution analog-to-digital conversion On-chip phase regulator regulates input signal phase shift caused by external circuits or components
- Precision power metrology Detection of active power, reactive power, apparent power, RMS voltage, and RMS current Active power calculation error: 0.1% (typ.) Reactive power calculation error: 0.5% (typ.)

Current integration

A current integrator can be specified to be used or not used for each current channel, and different sensors can be connected.



CEC-78K0R/KG3C*

* Made by TESSERA Technology Inc.

Anti-tamper (fault detection) feature Peak detection Zero-cross detection SAG detection Period and frequency measurement

On-chip LCD controller

Selectable among three display modes*2 (internal-resistance division, voltage boost, capacitance division) to match the LCD application

• Remote-control transmitter (78K0/Lx3-M only) Remote-control transmission is achieved by using a timer and UART.

Real-time counter

The power supplies are separated, allowing the microcontroller to run on the real-time clock even when the power supply is stopped.

16-bit multiplier/divider (78K0R/Lx3-M only)

*1. 2 channels for current and 1 channel for voltage (3 channels in total) in the µPD78F8052 and 78F8053.
 *2. The 78K0/Lx3-M supports selection between external-resistance division and internal-resistance division

We offer ideal products for various applications. You can choose the optimal product for your needs.

Microcontrollers for power supplies, lighting inverters, and LED lighting control (78K0/Ix2), microcontrollers for LED lighting control (µPD78F8025) (1/2)

Renesas Electronics has developed a dedicated driver capable of independently driving lighting control, which can be used to facilitate system configuration. By using the 78K0/Ix2, you can achieve low power consumption through PFC/dimmer control and by linking operations with a network. The µPD78F8025 allows efficient and reliable control thanks to its switching-type constant current driver and extensive on-chip protection circuits, including circuits to prevent overcurrent and overheating.







Microcontrollers for power supplies, lighting inverters, and LED lighting control (78K0/lx2), microcontrollers for LED lighting control (µPD78F8025) (2/2)

A wide range of tools to aid the efficient development of high-performance lighting • Renesas Electronics provides evaluation boards dedicated to each lighting application. Everything you need to evaluate your system, including manuals, circuit diagrams, and development tools, can be downloaded from our website, providing you with fine-tuned, comprehensive development support. Liahtina General Lighting Illumination Lighting Communication Fluorescent Lighting LED Solution Master Evaluation Solution Board er + driver board 78K0/IA2 mPD168804 78K0/IB2 Ballast (inverter) Lighting step-down HBLED evaluation board PWM evaluation board fluorescent light EZ-BLST-003 HBI FD ication maste comm EZ-0006 evaluation board aluation board EZ-0005 EZ-0007 EZ-0008 (78K0/lx2) device (MCU)

[Lighting solution evaluation boards]



· Solution boards can be evaluated separately.

When evaluating lights that feature communication capabilities, each solution board can be evaluated separately in combination with a master evaluation board.

[Automatic software generator] Applilet[®] EZ for HCD

Appliet EZ for HCD automatically generates sample software for LED lighting, which can then be written to the microcontroller on the board. Appliet EZ for HCD is easy to operate even for first-time users, and will lighten your software development load.



High-performance CPU embedded

We provide reliable performance for system function expansion.

High performance of 30.5 MIPS in a 16-bit microcontroller



Achieves high performance with 16-bit, 3-stage pipeline architecture

Reason for high performance

Flow of time (-						
1	Instruction 1	Fetch	ID	MEM			3-stage pipeline,
78K0R	Instruction 2		Fetch	ID	MEM		takes 1 clock for
1	Instruction 3			Fetch	ID	МЕМ	1 instruction
							1

Performance-enhancing oscillator Oscillators enable realization of a high-performance watchdog timer, a reduction in the number of external resonators, and improved timer resolution

78K0R/Kx3







Functions for enhancing performance Reduces the CPU processing load. DMA (78K0R) Data exchanges can be performed automatically between the special function registers (SFRs) of the peripheral hardware and the internal RAM without the CPU, using interrupts from the timer, serial interface, or A/D converter, or software triggers. Functions Applications • Number of channels: 4 (78K0R/Hx3) • CSI, UART (continuous transfer) 2 (other than 78K0R/Hx3) • A/D converter (continuous read of •Transfer unit: 8 bits/16 bits analog data, etc.) • Maximum number of transfers: 1024 •Timer (A/D conversion result, port value Transfer type: 2-cycle transfer read. etc., at fixed intervals) •Transfer mode: Single transfer mode Software trigger (DMA startup trigger •Transfer targets: SFRs ⇔ internal RAM can be generated through software) Multiplier (78K0/lx2) Functions • Executes processing of 8 bits × 8 bits = 16 bits • Executes processing of 16 bits × 16 bits = 32 bits Nultiplication input data Multiplication input data register A register B Multiplier/divider (78K0R/Kx3-L, 78K0R/Kx3-C, 78K0R/Hx3, 78K0R/Ix3, 78K0R/Kx3-A, 78K0R/Lx3, µPD78F8043, µPD78F8058) (78K0/Kx2, 78K0/Kx2-A, 78K0/Kx2-C, 78K0/lx2, µPD78F071x)*1 *2 Functions •16bits × 16bits = 32bits, 32bits ÷ 32bits = 32bits (78K0R) Multiplication result (product) Multiplication/division data register B MDBL MDBH MDCH

uPD78F0535A, uPD78F0536A, uPD78F0537A), and 78K0/KF2.

Divisor

Multiplier







The block diagram of the 78K0's multiplier/divider differs in structure from that of the 78K0R.
 78K0/Kx2 multiplier/divider: Implemented in 78K0/KC2 (µPD78F0514A, µPD78F0515A), 78K0/KD2 (µPD78F0524A, µPD78F0525A, µPD78F0526A, µPD78F0527A), 78K0/KE2 (µPD78F0534A,

High performance and functionality you can count on (2/2)



The serial array unit provides one shift register and one buffer register per channel, allowing the configuration of a 3-wire serial communication function and simple I²C function with one channel. Using two channels, a full-duplex UART function can be realized.



CAN controller (78K0R/Hx3)

• Complies with CAN protocol standard ISO 11898. Both standard and extended frames can be sent and received. •Transfer rate: Up to 1 Mbps On-chip 16-message buffer



LIN-UART (78K0R/Hx3)



LCD controller/driver (78K0R/Lx3)

Three different display methods can be selected according to the LCD application



Enhanced analog features (78K0R/Lx3*1, 78K0R/Kx3-A, 78K0/Kx2-A*2)

analog features, including 12-bit AV _{REFM} /ANI15 O	
A/D converters, 12-bit D/A ANI9 Converters, operational amplifiers, and an analog voltage reference. These features AMP2-/ANI8 AMP2-	ional amplifier ₂
converted into high-resolution digital signals, and eliminate the need to externally attach analog	AVDDO AVDDO
components, allowing you to reduce the size of your system. AMP0+/ANI2 AMP0/ANI1 AMP0/ANI1 AMP0/ANI1 AMP0/ANI0 AMP0-/ANI0 AMPO-/ANI0 AMPO-/ANI0 AMPO-/ANI0 AMPO-/ANI0 AMPO-/ANI0 AMPO-/ANI0 AMPO-/ANI0 AMPO-/ANI0	

These high-performance and easy-to-use 16-bit microcontrollers provide excellent compatibility with 8-bit microcontrollers. (78K0R)

Upward compatible for instructions from 8-bit microcontrollers





Major pins are pin compatible



The reliability technologies developed for automotive flash microcontrollers can be found in all our flash microcontrollers, making them a safe choice.



A flash security setting function is provided to protect your software from malicious rewriting and reading.

Software protection function



A boot swap function is provided to protect important programs even when power shuts down during self-programming.

Problems during self-rewriting

Boot swap function





The enhanced watchdog timer (WDT) offers improved reliability and functionality equivalent to that of an external WDT.

WDT independent from CPU



The watchdog timer incorporates enhanced functions.

Window WDT



An option byte function is incorporated to enable important system operation settings by hardware, eliminating setting errors caused by inadvertent program loops.

Option byte function

	RESET is released, you can set tting value with the hardware.	000C3H
000С0Н	ROM address (78K0F	000C1H R only) 000C0H
Setting 1	Watchdog timer operation in HALT/STOP mode	Stop/enable operation
Setting 2	Watchdog timer overflow time	3.88 ms (TYP.) to 3971.88 ms (TYP.)
Setting 3	Watchdog timer operation	Stop/enable operation
Setting 4	Watchdog timer window open time	25%/50%/75%/100%*1
Setting 5	Watchdog timer interval interrupt	Use/do not use
The BOM add	ress is set to 0080H on the 78K0 and 78K0S. Setti	na conditions differ depending

on the product.

WDT function



Various development environments for each development phase are available.

Lineup of development environment (78K0R)





Renesas Development Environment Website: http://www.renesas.com/tool/

We provide inexpensive, easy-to-use, and convenient development environments, allowing you to

Rich development environment you can count on (2/2)



E1 (On-chip debugging emulator) USB PC Software development environment Product planning System design Hardware design Software design Coding Production Compilation/assembly Unit inspection Debugging System debugging System evaluation Comme ation Type Integrated development environment (w/ compiler & simulator SP78K0S Software package Project manager PM+ C compiler CC78K0S RA78K0S Assembler SM+ for 78K0S/Kx1+, System simulator SM78K0S ID78K0S-QB Integrated debugger Real-time OS

*1. See the following URL for information on functions supported by CubeSuite+: http://www.renet Supported only in the 78K0R/Kx3
 Supported only in the 78K0R/Kx3 Integrated development environment CubeSuite+ (Free evaluation version available)

Sample program Applilet2 for 78K0S/Kx1+

- Used to compile and debug programs, manage pin layouts, generate code for microcontroller peripherals, and execute high-speed building.
- Software package

Flash programmer software

Self-programming library

Device driver configurator

- Project manager, C compiler, assembler, system simulator (part), Integrated debugger, etc. provided on a single CD-ROM disk.
- Project manager
- Various development tools integrated in Windows.
- •The project manager can execute a series of operations, such as editing, building, and starting the debugger.
- C compiler
- ANSI C standard compliant.
- Supports extended specifications unique to 78K0, 78K0S, 78K0R microcontrollers.
- System simulator
- Same GUI design as that of an integrated debugger.
- Evaluation possible without target prior to target completion.
- Integrated debugger
- Operates on Windows. • Easy to understand and use GUI (Graphical User Interface).
- Buttons provided for frequently used commands.
- Can be started up with a simple mouse click.



	Project manager Real-time OS Device driver configurator Compiler/assembler System simulator Integrated debugger Flash programmer software a	or	Integrated development environment CubeSuite+ (ree evaluation version available)
	riash programmer software	Jen-pro	
78	3K0 Microcontrollers		78K0R Microcontrollers
C	ubeSuite*1		CubeSuite*1
SI	P78K0		SP78K0R
PI	M+		PM+
C	C78K0		CC78K0R
R	A78K0		RA78K0R
SI	M+ for 78K0/Kx2,		SM+*2 (instruction + peripheral simulation)
SI	M78K0		SM+ for 78K0R (instruction simulation)
ID	078K0-QB		ID78K0R-QB
-			R178V4
R	enesas Flash Programmer		Renesas Flash Programmer
Li	brary		Library
A	pplilet2*3, Applilet3*4		Applilet3*5
	cubesuite+ Supported only in the 78K0/Ix2	and 78K	0/Kx2-L
		h are de	scribed in assembly language into

- Made up of the following six programs:
- Structured assembler preprocessor
 Object converter
- Assembler
- Linker
- •Also includes project manager PM+.
- Real-time OS
- •µITRON4.0 specification compliant.
- Renesas Flash Programmer
- Support for controlling programming from a PC
- GUI designed specifically for flash programming
- •Works with E1 on-chip debugging emulator (with programming function) as the programmer
- Self-programming library
- •The flash memory can be programmed by using the microcontroller itself, without using a programmer
- Built-in boot swap function for protecting the boot area at power down.
- Device driver configurator
- Easily generates initialization programs for troublesome peripheral functions (timers, UART, etc.).

- Librarian
- List converter



Various functions achieved with 78K0, 78K0R All Flash features and libraries New functions can be easily constructed. One example is introduced below.

Speaking (ADPCM: Adaptive Differential Pulse Code Modulation)

System control and voice function now in one chip! Contributes to reduced costs.

Conventional Improve the synthesis Compact Compact 78K0, 78K0R Improve the synthesis Improve the synthesis Improve the synthesis Synthesis Improve the synthesis Voice data*2 Improve the synthesis Synthesis Improve the synthesis <													
Two librari capabilitie	es are s, and	provide the sim	d: the ADPCM plified ADPCN	I-SP with both I-SP2 with onl	y exten	sion ca	pabilities.						
Library					<u> </u>	<i>.</i>	· · · · ·						
	10.000												
					35µs,	max.							
ADPCM-SP2			· · · · · · · · · · · · · · · · · · ·		-	-							
	78	K0	600 bytes	8 bytes	-	-	17µs, max.						
When Remarks 2. Proc	78K0, 78K0R Image: Compression and extension capabilities, and the simplified ADPCM-SP2 with only extension capabilities. ADPCM library (ADPCM-SP) features Two libraries are provided: the ADPCM-SP with both compression and extension capabilities, and the simplified ADPCM-SP2 with only extension capabilities. Library Supported Processing functors (during 20MHz drive) PCM-SP 78K0R 3K bytes 32 bytes 35µs, max. 30 µs, max. DPCM-SP2 78K0R 3K bytes 32 bytes 1 - 17µs, max. marks 1. The above processing times are processing time is required for output processing. marks 2. Processing in ecessary every 126 ms in the case of 8 kHz sampling voice. Voice data compression can be chosen from 3 patterns. Library Implified audio quality High audio quality High compression at the bytes/second 3K bytes/second 2K bytes/second 4K bytes/second 2K bytes/second 4K bytes/seco												
Library	Compact! 78K0, 78K0R Ic ADPCM library*1 + Voice data*2 Image: D/A Image: D/A Imag												
		High a	udio quality 🔶			→ Higł	n compression						
ADPCM-SP		<u> </u>	1 2	3K bytes/se	cond	~							
ADPCM-SP2	2	4K b	vtes/second	_		2K b	vtes/second						
lemarks The 3	KBps sp			by the ADPCM-S	SP2 librar								

Connecting (ZigBee®)

Our All Flash microcontrollers comply with ZigBee PRO, providing total support for low-power wireless network applications. You can start developing your application straight away.



Application examples



[Evaluation board lineup] (Boards made by TESSERA Technology Inc.)

Product specifications

(16-bit 1/4)

<th< th=""><th>Commercial name</th><th>e</th><th></th><th></th><th>7</th><th>8K0R/K</th><th>(E3</th><th></th><th></th><th>78</th><th>3K0R/K</th><th>F3</th><th></th><th></th><th></th><th>78</th><th>8K0R/I</th><th>(G3</th><th></th><th></th><th></th><th>78</th><th>3K0R/K</th><th>H3</th><th></th><th></th><th>7</th><th>3K0R/K</th><th>J3</th><th></th></th<>	Commercial name	e			7	8K0R/K	(E3			78	3K0R/K	F3				78	8K0R/I	(G3				78	3K0R/K	H3			7	3K0R/K	J3		
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Multiplier/divider Multiplier: 16 bits × 16 bits = 32 bits Low-voltage detector (LVI) 1.91/2.07 (initial value)/2.22/2.38/2.54/2.99/3.15/ 3.30/3.45/3.61/3.76/3.92/4.07/4.22 V ±0.1 V (selectable by software), low-voltage detection for an external input pin (EXLVI) can be performed Power-on clear (POC) 1.59 V ±0.09 V Other peripheral functions Key interrupt function Operating temperature -40 to +85°C	On-chip debug	o debug er/divider																d													
Power-on clear (POC) 1.59 V±0.09 V Other peripheral functions Key interrupt function Operating temperature -40 to +85°C	Multiplier/divider															Multiplier: 1			ts												
Other peripheral functions Key interrupt function Operating temperature -40 to +85°C	Low-voltage dete	ector (LVI)						1.9	1/2.07 (initia	al value)/2.2	22/2.38/2.5	3/2.68/2.84	/2.99/3.15/	3.30/3.45/	/3.61/3.76/	3.92/4.07/4	l.22 V ±0.1	V (selectable	by software), low-volta	age detectio	n for an ext	ernal input	pin (EXLVI)	can be per	ormed					
Operating temperature -40 to +85°C		,																													
																-															
			upped														-40 to +8	С													

*1. 10 KB when the self programming function is used. *2. 28 KB when the self programming function is used.

Product specifications (16-bit 2/4)

Commercial name	•						78K	0R/KC	3-L					78K0	R/KD	3-L	78K	0R/KE			78K()R/KF	3-L		7	8K0R	/KG3	·L	78	K0R/K	C3-L	78K0R/	/KE3-L	78K	0R/KE3-A
Pin count			_	40-	nin			44-r				48-pin	1		2-pin			64-pin				30-pin					-pin	_		48-pi		64-1			64-pin
Product name			8		50	03	0	3	50	03	5		33	4	35	06	20	8 8	60	0	=	<u>6</u>	27	58	5	4	63	30	52		54	25	92	16	5 8
			µPD78F100	µPD78F1001	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F100	µPD78F10	µPD78F10	µPD78F10	µPD78F10;	µPD78F10;	µPD78F10	µPD78F10	µPD78F10	µPD78F10	µPD78F10	µPD78F10;	µPD78F10	µPD78F10;	µPD78F10;	µPD78F10	µPD78F10
Flash memory (by	rtes)		16 K	32 K	48 K	64 K	16 K	32 K	48 K	64 K	32 K	48 K	64 K	32 K	48 K	64 K	32 K	48 K	64 K	64 K	96 K	128 K	192 K	256 K	96 K	128 K	192 K	256 K	64 I	K 96 K	128 K	96 K	128 K	64 K	96 K 128 K
RAM (bytes)			1 K	1.5 K	2 K	3 K*1	1 K	1.5 K	2 K	3 K*1	1.5 K	2 K	3 K*1	1.5 K	2 K	3 K*1	1.5 K	2 K	3 K*1	4 K	6 K	8 K*2	10 K	12 K*3	6 K	8 K*2	10 K	12 K*3	6 K	8 K*2	8 K*2	8 K*2	8 K*2	4 K	6K 7K
External bus	External memory expar	nsion space						_							_			_				_					_				_				-
interface	Bus type							-							-			-				-					-				-				-
	Address bus							-							-			-				_					-				-				-
	Data bus							-							-			-				-					_				-				-
Power supply	Normal operation mode	e														1.8 to 5.	5 V												3.0 t	o 3.6 V (1.8	to 3.6 V w	vhen USB no	ot used)	1.	.8 to 5.5 V
voltage	Flash memory program	nming mode														1.8 to 5.8	5 V														1.8 to 3.6				.8 to 5.5 V
Minimum instructi	ion execution time											0.0	5 µs (20 l	MHz: V _{DD} :	= 2.7 to	5.5 V), 0.	2 µs (5 N	/Hz: V _{DD} =	= 1.8 to 2	2.7 V)										0.05 μs (2 0.2 μs (20 MHz: V _{DD} = 5 MHz: V _{DD} =	2.7 to 3.6 V), 1.8 to 3.6 V)		0.05 µs (20 M 0.2 µs (5 M	MHz: $V_{DD} = 2.7$ to 5.5 V), MHz: $V_{DD} = 1.8$ to 5.5 V)
Clock	Main clock	High-speed system clock												Ceramic/	/crystal/	external	clock: 2	to 20 MHz	z										Cera	•		clock: 2 to 2		2	crystal/external clock: 2 to 20 MHz
		High-speed internal oscillation clock											1 M	Hz ±13%,	, 8 MHz	±1.8%, 2	20 MHz ±	2.4%												1 MHz ±13% 20) (target), 8 MH) MHz ±2.4%(Hz ±1.8%(target (target)	t),	1 MHz ±13% († 20 N	(target), 8 MHz ±2%(target), MHz ±2.4%(target)
	Subclock			-	-											Crystal:	32.768 H	κHz												Cry	stal: 32.76	38 kHz		Crysta	al: 32.768 kHz
	Low-speed internal osc	cillation clock													30 kHz	±10% (fo	or WDT)			1											lz ±10% (1	or WDT)		30 kHz	±10% (for WDT)
I/O ports	Total			33				37				41			45			55				71					9			39*4		53			53
	CMOS I/O			3.	51			33				34			38			48				62				8	0			30		43	3		46
	CMOS input			2	2			4				4			4			4				4					1			4		4			4
	CMOS output			-	-			-				1			1			1				1					1			_		1			1
	N-ch open-drain							_				2			2			2				4					1			4		4			2
Timer	16-bit timer	Number of channels		8	8			8				8			8			8				12				1	2			8		8			12
		Function					Interval t	imer/exte	rnal ever	nt counte	r/frequer	ncy divisi	ion funct	ion/pulse	interval	measure	ment/pul	se width r	measurer	ment/one-sł	iot pulse	e output/F	WM outp	but					freq F	uency division	function/pulse	event counter/ e interval measu ie-shot pulse ou out	irement/ itput/	frequency divis measurement/	er/external event counter/ ision function/pulse interval /pulse width measurement/ ulse output/PWM output
		PWM		6 channe	els max.			7 channe	ls max.		7 ch	annels n	nax.	7 cł	hannels	max.	7 c	hannels m	nax.		10 cł	nannels m	iax.			10 chan	nels max			3 channels	max.	5 channe	els max.	7 ch	annels max.
	Watchdog timer (WDT)			1	1			1				1			1			1				1					1			1		1			1
	Real-time counter (RTC	2)			-			1				1			1			1				1					1			1		1			1
Serial interface	CSI: 2 channels, UART:	1 channel		-	-			-				-			-			-			1		2			l		2		-		-	-		-
	CSI: 1 channel, UART:	1 channel		-	-			-				-			-			-				-					-			1		1			1
	,	: 1 channel, simple I ² C: 2 channels		-	-			-				-			-			-				-					-			-		-	-		-
		1 channel, simple I ² C: 1 channel		1	1			1				1			1			1				2					2			1		2			2
		(supporting LIN): 1 channel		1	1			1				1			1			1				-					-			-		-	-		-
	CSI: 1 channel, simple	I ² C: 1 channel		-	-			-				-			-			-				-					-			-		-	-		-
	CSI			-	-			-				-			-			-				-					-			-		-	-		-
	UART: 1 channel, simpl			-				-				-			-			-				-					-			-		-		_	-
	UART (supporting LIN)			-				-				-			-			-				1					1			1		1			1
	UART			-				-				-			-			-				-					-			-		-			-
	Simple I ² C			-				-				-			-			-				_					-			-		-			_
LCD controller/dri	I ² C			_								1			1			1				1					1			1		1			1
LOD Controller/dfl	Segment signal output			-								_			_			_				_					-			-		-			_
	Common signal output			_				-				_			_			_				_					-			-		_			_
A/D converter	Sommon signal output			10 bits				10 bits			10) bits × 1	1		_ 10 bits	x 11	4		2		10	-) bits × 12)				s × 16			10 bits >	< 8	10 bits		47	2 bits × 12
D/A converter								10 0113				_			10 bits	~ 11		-	2			_	-				3 × 10				N O				2 bits × 2
DMA controller				2				2				2			2			2				2					2			2		2		12	2 0115 × 2
Interrupt	External			8				9				9			9			9				13					3			7		11			12
	Internal			22				24				25			25			25			33		35	5	3	3		35		36		41			33
On-chip debug					-			27								Supporte	ed													00	Supporte			ç	Supported
Multiplier/divider										M	ultiplier/	divider:	16 bits ×	16 bits =				= 32 bits. I	remainde	er: 32 bits									Mu	ltiplier/divider:			32 bits ÷ 32		s, remainder: 32 bits
Low-voltage deter	ctor (LVI)				1.	91/2.07 (ii	nitial valu	e)/2.22/2.	38/2.53/									-		re), low-volta	ige dete	ction for a	an externa	al input p	oin (EXLVI) can be	performe	d	1.91/	2.07(initial valu 3.30/3.45 V± -voltage detec	e)/2.22/2.38/2 0.1 V (selecta	2.53/2.68/2.84/2 able by software) ternal input pin (.99/3.15/		*5
Power-on clear (P	POC)											Powe	r-on rese	et: 1.61 V -	±0.09 V.	power-d	own rese	t: 1.59 V ±	±0.09 V										P				: ower-dov	vn reset: '	1.59 V ±0.09 V
Other peripheral f	,								Co	mparator	: 2 chani					-		terrupt fun		Key inte	rrupt fur	nction		K	ey interru	pt functi	on		-			ey interrupt		Operational	I amplifier: 3 channels, nterrupt function
Operating temper										1		, p		gaarta	,	-40 to +8									,						-40 to +85			KC y II	nterrupt function 0 to +85°C
	If programming function is u	sed *4 One	is for contr	rolling the l	USB buff	er																							1				:		

*1. 2 KB when the self programming function is used.
*2. 7 KB when the self programming function is used.
*3. 11 KB when the self programming function is used.

*4. One is for controlling the USB buffer.
*5. 1.91/2.07 (initial value)/2.22/2.38/2.53/2.68/2.84/2.99/3.15/3.30/3.45/3.61/3.76/3.92/4.07/4.22 V ±0.1 V (selectable by software), low-voltage detection for an external input pin (EXLVI) can be performed

Product specifications

(16-bit 3/4)

Commercial name	e			78K0R/KG3-C	78K0R/HC3*1	78K0R/HE3*1	701	(0R/HF3*1	78K0R/HG3*1	µPD78F8043	µPD78F8058*1
							+			1	
Pin count Product name			80-pin	100-pin	48 -pin	64 -pin		80-pin	100 -pin	56-pin	56-pin
Floduct name			µРD78F1846 µРD78F1847	µPD78F1848	LIPD78F1031 LIPD78F1035 LIPD78F1035 LIPD78F1035	µРD78F1036 µРD78F1036 µРD78F1036 µРD78F1036	µРD78F1041 µРD78F1045	µPD78F1040	µPD78F1046 µPD78F1046 µPD78F1046 µPD78F1046	µPD78F804(µPD78F8056
Flash memory (by	ytes)		96 K 128 K	96 K 128 K	64 K 96 K 128 K 192 K 256 K			128 K 192 K 256 K	64 K 96 K 128 K 192 K 256 K	32 K 64 K 96 K 128 K	64 K 96 K 128 K
RAM (bytes)			6 K 8 K*2	6 K 8 K*2	4K 6K 8K 12K 16K	4 K 6 K 8 K 12 K 16 K	4 K 6 K	8 K 12 K 16 K	4K 6K 8K 12K 16K	4K 4K 6K 7K	8K*2 8K*2 8K*2
External bus interface		nsion space	-	-	-	-		-	-	-	-
			-	-	_	_		_	_	_	_
			-	_	_	-		_	_	_	_
Power supply		9		o 5.5 V		!	2.7 to 5.5 V		<u>i</u>	3.0 to 5.5 V	1.8 to 3.6 V
voltage				o 5.5 V			2.7 to 5.5 V			3.0 to 5.5 V	1.8 to 3.6 V
Minimum instruct	tion execution time		0.05 µs (20 MHz:	V _{DD} = 2.7 to 5.5 V)		42 ns (24	MHz: V _{DD} = 2.7 to 5.5	V)		0.05 µs (20 MHz: V _{DD} = 3.0 to 5.5 V)	0.05 μs (20 MHz: V _{DD} = 2.7 to 3.6 V 0.2 μs (5 MHz: V _{DD} = 1.8 to 3.6 V)
Clock	Main clock	High-speed system clock	Ceramic/crystal/exter	rnal clock: 2 to 20 MHz		Ceramic/o	crystal/external clock: 2	2 to 20 MHz		Ceramic/crystal/external clock: 2 to 20 MHz	Commission and all shares
		High-speed internal oscillation clock	8 MHz ±2% (target)	, 20 MHz ±2% (target)		4 MHz ±2	% (target), 8 MHz ±2%	(target)		1 MHz (TYP.), 8 MHz (TYP.), 20 MHz (TYP.)	1 MHz (TYP.), 8 MHz (TYP.), 20 MHz (T
	Subclock		Crystal: 3	32.768 kHz			-			-	Crystal: 32.768 kHz
	Low-speed internal os	illation clock	30 kHz ±10	0% (for WDT)		30 kHz	±10% (for WDT)			30 kHz ±10% (for WDT)	30 kHz ±10% (for WDT)
I/O ports	Total		71	89	41	55		71	89	26*3	18*5
	CMOS I/O		62	80	32	46		62	80	23	13
	CMOS input		4	4	4	4		4	4	1	4
			1	1	1	1		1	1	-	1
	rtes) bus		4	4	4	4		4	4	2	-
Timer	16-bit timer		frequency division function pulse width	ernal event counter/ v/pulse interval measurement/ measurement/	17 Interval timer/exte	21 rnal event counter/frequency division function	/pulse interval measure	21 ement/pulse width measureme	25 nt/one-shot pulse output/PWM output	12 Interval timer/external event counter/pulse width	12 Interval timer/external event counter pulse interval measurement/ pulse width measurement/
	Data bus supply Normal operation mode Flash memory programming mode m instruct Main clock High-speed system clo High-speed internal oscilla Subclock Low-speed internal oscillation clock Is Total CMOS I/O CMOS input CMOS output Number of channels N-ch open-drain Function Vatchdog timer (WDT) PWM Real-time counter (RTC) PWM Vatchdog timer (WDT) Real-time counter (RTC) tterface CSI: 2 channels, UART: 1 channel CSI: 1 channel, UART: 1 channel CSI: 2 channels, UART: 1 channel CSI: 2 channels, UART: 1 channel, simple IPC: 2 clo CSI: 1 channel, UART: 1 channel CSI: 1 channel, UART: 1 channel CSI: 2 channels, UART: 1 channel CSI: 1 channel, Simple IPC: 1 cl CSI: 1 channel, simple IPC: 1 cl CSI: 1 channel, Simple IPC: 1 cl CSI: 1 channel, simple IPC: 1 cl CSI: 1 channel, simple IPC: 1 cl CSI: 1 channel, simple IPC: 1 cl CSI: 1 channel, simple IPC: 1 cl CSI: 1 channel, simple IPC: 1 cl UART (supporting LIN) UART			9 channels max.	14 channels max.	17 channels max.	17.	channels max.	21 channels max.	measurement/one-shot pulse output/PWM outpu 6 channels max.	t one-shot pulse output/PWM output 2 channels max.
	al bus and a serie of the serie		1	9 channels max. 1	14 channels max.	1	170	1	1	1	2 channels max.
	bytes) al bus ce bytes) al bus ce Bus type Address bus Data bus Supply Normal operation mode P Flash memory programming mode um instruction execution time Main clock Low-speed internal oscillation clock tts Total CMOS I/O CMOS output N-ch open-drain CMOS output N-ch open-drain I6-bit timer I6-bit timer Vatchdog timer (WDT) Real-time counter (RTC) ES1: 2 channels, UART: 1 channel CS1: 1 channel, simple I ⁺ C: 1 channel CS1: 1 channel, simple I ⁺ C: 1 channel CS1: 0 channel		1	1	_	_		-	_	_	1
Serial interface		,	1	1	-	_		_	_	_	_
	CSI: 1 channel, UART:	1 channel	_	-	-	-		-	-	-	-
	CSI: 2 channels, UART	1 channel, simple I2C: 2 channels	_	_	-	-		-	-	-	-
	CSI: 1 channel, UART:	1 channel, simple I ² C: 1 channel	2	2	_	-		-	-	1	1
	CSI: 2 channels, UART	(supporting LIN): 1 channel	-	-	-	-		-	-	-	-
	CSI: 1 channel, simple	I ² C: 1 channel	-	-	-	-		-	1	-	-
	CSI		-	-	2	3		3	3	-	1 (used exclusively for internal communication with the RF transcei
		e l ² C: 1 channel	-	-	-	1		1	1	-	-
			-	-	2	2		2	2	1 1 (used exclusively for internal communication	1
			-	-	-	-		-	-	with the IO-Link transceiver)	1 (transmission only)
			-	- 1	1	1		1	-	- 1	-
I CD controller/dr	Simple I ² C I ² C roller/driver Segment signal output		-	_	_			_		_	_
200 00111011011011			_	_	_	_		_	_	_	_
			_	_	_	_		-	_	_	_
A/D converter			10 bits × 12	10 bits × 16	10 bits × 11	10 bits × 15	1	I0 bits × 16	10 bits × 24	10 bits × 6	-
D/A converter	orter roller External		_	-	_	_		_	_	-	-
DMA controller			2	2	4	4		4	4	2	2
Interrupt	troller External Internal debug		9	9	10	11		12	12	5*4	4
	Internal		35	35	41	47		47	49	28	27
On-chip debug	Internal ebug er/divider			ported			Supported			Supported	Supported
Multiplier/divider			Multiplier/divider: 16 32 bits ÷ 32 bits = 32	bits × 16 bits = 32 bits, t bits, remainder: 32 bits		Multiplier/divider: 16 bits × 16 bits =	= 32 bits, 32 bits ÷ 32 b	bits = 32 bits, remainder: 32 bit	is	Multiplier/divider: 16 bits × 16 bits = 32 bits, 32 bits ÷ 32 bits = 32 bits, remainder: 32 bits	Multiplier/divider: 16 bits × 16 bits = 32 bit 32 bits ÷ 32 bits = 32 bits, remainder: 32 bits
Low-voltage dete	ector (LVI)		4.22 V ±0.1 V (selectable detection for an extension	1.45/3.61/3.76/3.92/4.07/ e by software), low-voltage ernal input pin (EXLVI) performed	2.84/2.99/3.15/3.30/3.	15/3.61/3.76/3.92/4.07/4.22 V ±0.1 V (selectable	by software), low-volt	age detection for an external ir	nput pin (EXLVI) can be performed	3.15/3.30/3.45/3.61/3.76/3.92/4.07/4.22 V ±0.1 V (selectable by software), low-voltage detection for an external input pin (EXLVI) can be performed	1.91/2.07 (initial value)/2.22/2.38/2.53/2.6 2.84/2.99/3.15/3.30/3.45 V ±0.1 V (selecta by software), low-voltage detection for a external input pin (EXLVI) can be perform
Power-on clear (F	POC)		Power-on reset power-down res	t: 1.61 V ±0.09 V, set: 1.59 V ±0.09 V		Power-on reset: 1.61 V ±0.0	9 V, power-down reset	: 1.59 V ±0.09 V		Power-on reset: 1.61 V ±0.09 V, power-down reset: 1.59 V ±0.09 V	Power-on reset: 1.61 V ±0.09 V, power-down reset: 1.59 V ±0.09 V
Other peripheral f	functions		CEC, remote cont key interr	trol signal reception, upt function		CAN controller, d	ata flash memory: 16 K	B, key interrupt function		IO-Link transceiver	RF transceiver
Operating temper	rature		-40 to	o +85°C			-40 to +85°C			-40 to +85°C	-40 to +85°C

*2. 7 KB when the self programming function is used.*3. Three of these pins are connected to the IO-Link transceiver.

*4. Two of the external interrupt sources are connected the IO-Link transceiver.
 *5. Four of these pins are connected to the RF transceiver.
 Remarks The specifications of products still under development are subject to change without notice.

Product specifications (16-bit 4/4)

78K0R/IB3 78K0R/IC3 78K0R/ID3 78K0R/IE3 78K0R/LF3 78K0R/L 100-pir 48-pin 80-pin 30-pin 38-pin 44-pin 52-pin 64-pin Flash memory (bytes) 16 K 32 K 16 K 32 K 16 K 32 K 32 K 48 K 64 K 32 K 48 K 64 K 32 K 48 K 64 K 64 K 96 K 128 K 64 K 96 K RAM (bytes) 1K 1.5K 1.5K 2 K 3 K*1 1.5 K 2 K 1 K 1.5 K 1K 1.5K 3 K*1 1.5 K 2 K 3 K*1 4 K 6 K 7 K 4 K 6 K External bus External memory expansion space _ interface Bus type _ _ _ _ _ _ Address bus Data bus _ _ _ _ _ _ Power supply Normal operation mode 2.7 to 5.5 V 1.8 to 5.5 V voltage Flash memory programming mode 2.7 to 5.5 V 1.8 to 5.5 \ Minimum instruction execution time 0.05 μs (20 MHz: V_{pp} = 2.7 to 5.5 V) 0.05 µs (20 MHz: V_{DD} = 2.7 to 5.5 V), 0.2 Clock Main clock High-speed system clock Ceramic/crystal/external clock: 2 to 20 MHz Ceramic/crystal/external clo High-speed internal oscillation clock 8 MHz ±1.8%, 40 MHz*2 +2.9%/-4.1% 1 MHz ±13%, 8 MHz ±2%, 20 Subclock Crystal: 32.768 kHz Crystal: 32.768 _ Low-speed internal oscillation clock 30 kHz ±10% (for WDT) 30 kHz ±10% (for I/O ports Total 23 31 37 41 45 55 51 67 CMOS I/O 38 48 46 60 21 27 33 34 CMOS input 4 4 2 4 4 4 4 4 CMOS output 1 1 1 N-ch open-drain 2 2 2 2 Timer 16-bit timer Number of channels 12 12 12 12 12 12 12 12 Function Interval timer/external event counter/frequency division function/pulse interval measurement/pulse width measurement/one-shot pulse output/ PWM output/3-phase sine-wave Interval timer/external event counter/frequency division function/pulse interval meas PWM output (supporting 2-phase modulation)/half-bridge 2-channel drive/full-bridge drive/real-time output/interrupt signal thinning function/ AD conversion trigger output PWM 7 channels max. 9 channels max. 9 channels max. 11 channels max. 9 channels max 5 channels max 9 channels max 7 channels m Watchdog timer (WDT) 1 1 1 1 1 1 1 1 Real-time counter (RTC) 1 1 1 1 1 1 1 Serial interface CSI: 2 channels, UART: 1 channel _ _ _ _ _ _ CSI: 1 channel, UART: 1 channel CSI: 2 channels, UART: 1 channel, simple I²C: 2 channels _ _ _ _ _ _ CSI: 1 channel, UART: 1 channel, simple I²C: 1 channel 2 1 1 1 1 CSI: 2 channels, UART (supporting LIN): 1 channel 1 _ 1 1 _ _ CSI: 1 channel, simple I²C: 1 channel CSI _ _ _ _ _ _ _ _ UART: 1 channel, simple I2C: 1 channel UART (supporting LIN) 1*3 1*³ 1 _ _ 1 _ _ UART Simple I²C _ _ _ _ _ _ _ _ I²C 1 1 1 1 LCD controller/driver _ _ _ _ _ _ Display method can be switched between internal voltage boosti Segment signal output 31 (27)*4 40 (36)*4 _ Common signal output _ 4 (8)*4 4 (8)*4 _ _ _ A/D converter 10 bits × 6 10 bits × 8 10 bits × 10 10 bits × 11 10 bits × 11 10 bits × 12 12 bits*5 × 8 12 bits*5 × 1 D/A converter 12 bits × 2*6 12 bits × 2* _ _ _ _ _ _ DMA controller 2 2 2 2 2 2 2 2 Interrupt External 8 8 8 8 8 12 6 8 Internal 31 33 33 34 34 34 28 33 On-chip debug Supported Supported Multiplier/divider Multiplier/divider: 16 bits × 16 bits = 32 bits, 32 bit Multiplier/divider: 16 bits × 16 bits = 32 bits, 32 bits ÷ 32 bits = 32 bits, remainder: 32 bits Low-voltage detector (LVI) 2.84/2.99/3.15/3.30/3.45/3.61/3.76/3.92/4.07/4.22 V ±0.1 V 1.91/2.07 (initial value)/2.22/2.38/2.53/2.68/2.84/2.99/3.1 (selectable by software), low-voltage detection for an external input pin (EXLVI) can be performed (selectable by software), low-voltage detection for an Power-on clear (POC) Power-on reset: 1.61 V ±0.09 V, power-down reset: 1.59 V ±0.09 V Power-on reset: 1.61 V ±0.09 V, power-Other peripheral functions Comparator: 2 channels, programmable gain amplifier: 1 channel Operational amplifier: 2 channels*6, key interrupt function Operational amplifier: 3 channels*6 Operating temperature -40 to +85° -40 to +85°C

*1. 2 KB when the self programming function is used.

*7. Under development

*2. The 40 MHz clock is only supplied to the timer array unit and the 20 MHz clock is supplied to the CPU and peripheral functions. *3. 3-phase sine-wave PWM output/full-bridge drive is disabled when LIN is used. Half-bridge drive is also restricted to 1 channel.

*6. Not available in the μPD78F151xA.

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.G3			KOR/LH	13		78K0R/LG3-M*7
1		1	28-pin			100-pin
µPD78F1505A µPD78F1515A	µPD78F1506A	µPD78F1516A	µPD78F1507A	µPD78F1508A	µPD78F1518A	UPD78F8070
128 K	64 K		96 K	12	8 K	128 K
7 K	4 K		6 K	7	К	7 K
			_			-
			_			-
			_			-
			-			-
V						1.8 to 3.6 V
V						2.7 to 3.6 V
2 μs (5 MHz: V _{DD} = 1.	.8 to 5.5 V)					$\begin{array}{l} 0.05 \ \mu S \ (20 \ \text{MHz: } V_{\text{DD}} = 2.7 \ \text{to} \ 3.6 \ \text{V}), \\ 0.2 \ \mu S \ (5 \ \text{MHz: } V_{\text{DD}} = 1.8 \ \text{to} \ 3.6 \ \text{V}) \end{array}$
lock: 2 to 20 MHz						Ceramic/crystal/external clock: 2 to 20 MHz
0 MHz ±2.4% (target)					1 MHz (TYP.), 8 MHz (TYP.)
8 kHz						Crystal: 32.768 kHz
or WDT)						30 kHz (TYP.)
			83			45
			76			39
			4			3
			1			1
			2			2
			12			12
surement/pulse width	measurement/	one-sho	ot pulse out	put/PWM o	utput	Interval timer/pulse interval measurement/ PWM output
nax.		10 0	channels n	nax.		3
			1			1
			1			1
			1			-
			-			-
			-			-
			2			1
			-			-
			-			-
			-			-
			-			-
			1			1
			-			1
			-			-
			1			1
ting, capacitive divis	ion, and exte	rnal res		sion.		
			54 (50)*4			40
			4 (8)*4			4
12			2 bits*5 × 1			Successive approximation: 10 bits \times 2, $\Delta\Sigma$: 24 bits \times 4
*6		1	2 bits × 2	^r 6		-
			2			2
			13			4
			33			32
d						
its ÷ 32 bits = 32 bit						1.01/0.07/0-1011
15/3.30/3.45/3.61/3.						1.91/2.07 (initial value)/ 2.22/2.38/2.53/2.68/2.84/ 2.99/3.15/3.45 ±0.1V
external input pin (E		perform	med			(selectable by software)
r-down reset: 1.59 V						Power calculation,
⁶ , key interrupt function	Operational a	mplifier:	3 channels*6,	key interrup	t function	Power calculation, power quality measurement, digital frequency conversion
°C						

 ^{*4.} Values in parentheses are the number of signal outputs when 8com signal is used.
 ns.
 *5. The A/D converter has 10-bit resolution in the μPD78F151xA.

Product specifications (8-bit 1/3)

CPU Core			70/00////	78K0S	701/00/1/1				701/0 // / 0		78K0	701/0 /// 50	701/0///70
Commercial name			78K0S/KU1+	78K0S/KY1+	78K0S/KA1+ 78			00	78K0/KC2		78K0/KD2	78K0/KE2	78K0/KF2
Pin count			10-pin	16-pin		30/32-pin	30/36 -pin	38-pin	44-pin	48-pin	52-pin	64-pin	80-pin
Product name			 μPD78F9200 μPD78F9201 μPD78F9500 μPD78F9500 μPD78F9501 μPD78F9501 	uPD78F9210 uPD78F9211 uPD78F9510 uPD78F9510 uPD78F9511 uPD78F9512	µРD78F9221 µРD78F9222	µРD78F9232 µРD78F9234	LIPD78F05014 LIPD78F05024 LIPD78F05024	LPD78F0511A LPD78F0512A LPD78F0513A	uPD78F0511A uPD78F0512A µPD78F0513A	LPD78F0512/ LPD78F0513/ LPD78F0514A	uPD78F0621/ µPD78F0522/ µPD78F0523/ µPD78F0524/ µPD78F0526/ µPD78F0526/ µPD78F0526/	LPD78F0531/ LPD78F0532/ LPD78F0533/ LPD78F0535/ LPD78F0535/ LPD78F0535/	LPD78F0544, LPD78F0545, LPD78F0546, LPD78F0546/
Flash memory (bytes)			1 K 2 K 4 K 1 K 2 K 4 K	1K 2K 4K 1K 2K 4k	X 2K 4K 8K 4	4K 8K 8	K 16 K 24 K 32 K	16 K 24 K 32 K	16 K 24 K 32 K 16	6 K 24 K 32 K 48 K 60 K		16 K 24 K 32 K 48 K 60 K 96 K 128 K	
	Bank		-	-	-	-	-	-	-	-	- 4 6	- 4 6	- 4 6
RAM (bytes)			128	128	128 256	256 51	12 768 1 K 1 K	768 1 K 1 K	768 1 K 1 K 76	68 1K 1K 2K 3K		768 1K 1K 2K 3K 5K 7K	2K 3K 5K 7K
Power supply	Normal operation m	ode		2.0 to 5.5 V							1.8 to 5.5 V		
voltage	Flash memory progr	ramming mode		2.7 to 5.5 V							2.7 to 5.5 V		
Minimum instruction exec	cution time			$_{\rm D}$ = 4.0 to 5.5 V)/0.33 µs (6 MHz: V _{DE}							o 5.5 V)/0.20 μ s (10 MHz: V _{DD} = 2.7 to 5.5 V	/)/	
				$v_{DD} = 2.7 \text{ to } 5.5 \text{ V}/1.0 \mu\text{s} (2 \text{ MHz: } \text{V}_{DD})$	-						5 MHz: $V_{DD} = 1.8$ to 5.5 V)		
Clock	Main clock	High-speed system clock	Ceramic	c*1/crystal*1/external clock: 1 to 10 l	MHz					Ceramic/crys	tal/external clock: 1 to 20 MHz		
		High-speed internal oscillator		8 MHz ±5%							8 MHz ±5%		
	Subclock		-	-	-	-	-				external clock: 32.768 kHz		
1/0	Low-speed internal	oscillator	. , ,	clock for watchdog timer and 8-bit			20			· · · · · · · · · · · · · · · · · · ·	or watchdog timer and 8-bit timer TMH1)		
I/O ports	Total		8	14	17	26	23	31	37	41	45	55	71
	CMOS I/O		1	13	15	24	21	29	33	36	40	50	66
	CMOS input		1	1	1	1	-	-	-	-	-	-	-
	CMOS output		-	-	1	1	2	- 4	- 4	1	1	1	1
Timor	N-ch open-drain	Number of the second		- 1	- 1	-	2	4		4	4		
Timer	16-bit timer (TM0)	Number of channels	1	event counter/PPG output/pulse wi	1 1 E	1	1	1	1		ا ounter/PPG output/pulse width measureme	1 2	2
		Function										ent/	
	10 644	Number of observate	squar	e-wave output/one-shot pulse outp			_			square-wave	output/one-shot pulse output		
	16-bit timer (TMx)	Number of channels		-	-	_	_	-	_		-		-
		Function	- 1	-	- 1	- 1	2	- 2	2	- 2	- 2		- 2
	8-bit timer (TMH)	Number of channels			1 1		2	2	2		_	-	2
		Function	Interval	timer/PWM output/square-wave ou						niterval timer/PWM output/	carrier generator output/square-wave outpu		
	8-bit timer (TM5)	Number of channels	_	-	-	-	2	2	2	2	2	2	2
		Function	-	-	-	-				Interval timer/external ever	nt counter/PWM output/square-wave output	t	
	8-bit timer (TM8)	Number of channels	_	-	1	1	_	_	_	_	-	_	_
		Function	_	_	Interval time		_	-	_	_	_	-	_
	Watchdog timer (WI		1	1	1	1	1	1	1	1	1	1	1
	Watch timer	,	_	-	-	-	-	1	1	1	1	1	1
	Real-time counter (F	RTC)	_	-	-	-	_	-	_	-	-	-	-
Serial interface	UART (supporting L	,	_	-	1	1	1	1	1	1	1	1	1
	UART		-	-	-	-	-	-	_	-	-	-	-
	UART/CSI		-	-	-	-	1	1	1	1	1	1	1
	CSI		-	-	-	-	_	-	-	-	-	- 1	1
	Automatic transmit/	receive 3-wire CSI	-	-	-	-	-	-	-	-	-	_	1
	I ² C		_	-	-	-	1	1	1	1	1	1	1
A/D converter	Successive approxi	mation	10 bits × 4 –	10 bits × 4 -	10 bits × 4 10	0 bits × 4	10 bits × 4	10 bits × 6	10 bits × 8	10 bits × 8	10 bits × 8	10 bits × 8	10 bits × 8
	ΔΣ		_	-	-	-	_	-	-	-	_	-	-
Interrupt	External		2	2	4	4	6	7	7	8	8	9	9
	Internal		5 3	5 4	9	9	14	16	16	16	16	16 19	20
Maximum number of	8 commons		_	-	-	-	_	-	-	-	_	-	-
segments displayed in LC	CD 4 commons		-	-	-	-	_	-	-	-	-	-	-
On-chip debug			Supported	Supported	Supported Su	Supported	Supported	d Supported	Supported	Supported	Supported	Supported	Supported
Multiplier/divider			-	-	- 8-	-bit × 8-bit	_	-	-	— 16-bit × 16-bit, 32-bit ÷ 16-bit	— 16-bit × 16-bit, 32-bit ÷ 16-bit	— 16-bit × 16-bit, 32-bit ÷ 16-bit	16-bit × 16-bit, 32-bit ÷ 16-bit
Low-voltage detector (LV	(1)		2.35/2.6 V ±0.1 V or 2	2.85/3.1/3.3 V ±0.15 V or 3.5/3.7/3.9	9/4.1/4.3 V ±0.2 V			. :	1.93/2.08/2.	.24/2.39/2.55/2.70/2.85/3.01/	3.16/3.32/3.47/3.62/3.78/3.93/4.09/4.24 V (· i ·	
Power on close (DOO)				(Selectable by software)						The detected voltage car	be input to pins. (Selectable by software)		
. ,				2.1 V ±0.1 V						Cleak autout	1.59 V ±0.15 V		
			- T 40 to 19	-		-	-	-	-	Clock output	Clock output	Clock output, buzzer output	Clock output, buzzer output
	d and 7050500			5°C (model with expanded tempera	lure range)					r _A = -40 to +85°C (mo	odel with expanded temperature range)		
Power-on clear (POC) Other Operating temperature . The µPD78F9500, 78F9501, and	and (VE0502 connot con	poot to a coramic or orvetal recor	nator										

*1. The μPD78F9500, 78F9501, and 78F9502 cannot connect to a ceramic or crystal resonator.
 *2. Only supported in the μPD78F0503DA, 78F0513DA, 78F0515DA, 78F0527DA, 78F0537DA, and 78F0547DA.

Product specifications (8-bit 2/3)

CPU Core				78			78K0		78k		78K0		78K0			
Commercial name			78K0/KY2-L	78K0/KA2-L	78K0/KB2-L	78K0/KC2 -L	78K0/KB2-A 78K0	0/KC2-A 78	3K0/KC2-C	78K0/KE2-C	μPD179F [·]	IXX	µPD78F0730	78K0/IY2	78K0/IA2	78K0/IB2
Pin count			16-pin	20/25/32-pin	30-pin	40/44/48 -pin	30-pin 36/	/48-pin	48-pin	64-pin	30-pin	38-pin	30-pin	16-pin	20-pin	30/32-pin
Product name			D78F0550 D78F0551 D78F0552 D78F0555 D78F0555	D78F0560 D78F0561 D78F0562 D78F0565 D78F0565 D78F0566	D78F0571 D78F0572 D78F0573 D78F0576 D78F0576 D78F0577 D78F0578	D78F0581 D78F0582 D78F0583 D78F0586 D78F0587 D78F0587 D78F0588	D78F0590 D78F0591 D78F0592	D78F0593 D78F0760	D78F0761 D78F0762	D78F0763 D78F0764 D78F0765	D179F110 D179F111 D179F112 D179F113 D179F114	D179F122 D179F123 D179F124	D78F0730	D78F0740 D78F0741 D78F0742 D78F0750 D78F0751 D78F0752	D78F0743 D78F0744 D78F0753 D78F0753	D78F0745 D78F0746 D78F0755 D78F0755
							<u> </u>	<u><u><u> </u></u></u>		트 트 트			느			
Flash memory (bytes)	Deals		4 K 8 K 16 K 4 K 8 K 16	K 4 K 8 K 16 K 4 K 8 K 16 K	8 K 16 K 32 K 8 K 16 K 32 K	8 K 16 K 32 K 8 K 16 K 32 F	K 16 K 32 K 16 I	K 32 K 32	K 48 K 60 K 3	32 K 48 K 60 K	4 K 8 K 16 K 24 K 32 K	16 K 24 K 32 K	16 K	4 K 8 K 16 K 4 K 8 K 16	K 8 K 16 K 8 K 16	K 8 K 16 K 8 K 16 F
PAM (bytee)	Bank		204 512 760 204 512 76		- 510 760 1 K 510 760 1 K	- 510 760 1 K 510 760 1 K	-	(1 K 1 I		1 1 2 2 2 2 2		760 1 1 1 1			-	-
RAM (bytes)		! -	304 512 700 304 512 70	1.8 to		512 100 1 K 512 100 1 K	1.8 to 5.5		1.8 to 5		1.8 to 3.6		4.0 to 5.5 V		2.7 to 5.5 V	0 512 700 512 700
Power supply voltage	Normal operation mo															
	Flash memory progr	amming mode		2.0 to			2.7 to 5.5		1.8 to 5		2.0 to 3.6 \		4.0 to 5.5 V 0.125µs		2.7 to 5.5 V	
Minimum instruction execu	tion time			0.2 μs (10 MHz: V _{DD} = 2.7 to 5.5 V)	//0.4 µs (5 MHz: V _{DD} = 1.8 to 5.5	5 V)	0.1 µs (20 MHz: V _{DD} = 2.7 1 0.4 µs (5 MHz: V _{DD} = 1.8 t	to 5.5 V)/	1 µs (20 MHz: V _p	_{DD} = 2.7 to 3.6 V)	0.5 μs (4 MHz: V _{DD} = 2 1 μs (2 MHz: V _{DD} = 1.		(16 MHz: V _{DD} = 4.0 to 5.5 V)	0.2 μs (10	0 MHz: V _{DD} = 2.7 to 5.5	V)
Clock	Main clock	High-speed system clock		Ceramic/crystal/extern	nal clock: 1 to 10 MHz		Ceramic/crystal/exter	rnal clock: Cor	ramic/crvstal/externa	al clock: 2 to 20 MHz	Ceramic/crystal/external cl		*5	Ceramic/crvst	al/external clock: 1 to	10 MHz
	Main block	High-speed internal oscillator		4 MHz ±2%, 8 MHz ±3%: Can b		9	1 to 20 MH 8 MHz ±5	-	8 MHz		4 MHz ±2%		16 MHz ±10%	4 MHz ±2%, 8 MHz ±3%		
	Subclock	righ opeen internal ecollator	_	-	–	Crystal/external clock: 32.768 kH	- C	rystal:	Crystal: 32.			<u>,</u>	_	_		_
				30 kHz ±10% (clock for watchd			32.	.768 kHz				r and 0 bit timer TMU(1)	*6			
1/2	Low-speed internal	oscillator	10			04/00/40					240 kHz (TYP.) (clock for watchdog time			30 kHz ±10% (clock for		
I/O ports	Total		12	16/21/25	24	34/38/42		8/40	41	55	26	34	19	12	16	25 (30-pin)/23 (32-pin)
	CMOS I/O		9	13/18/22	21	29/33/37	20 2	6/38	26	38	25	33	17	9	13	22 (30-pin)/20 (32-pin)
	CMOS input		3	3	3	5	-	-	4	4	1	1	-	3	3	3
	CMOS output		-	-	-	-	-	-	1	1	-	-	-	-	-	-
	N-ch open-drain		2	2	2	4	2	2	10	12	N-ch: 24, P-ch: 1*4	N-ch: 32, P-ch: 1*4	2	-	2	2
Timer	16-bit timer (TM0)	Number of channels	1	1	1	1	1	1	3	3	1		1	1	1	1
		Function		Interval timer/external ev	ent counter/PPG output/		Interval time	er/external eve	ent counter/PPG of	output/pulse	Interval timer/external event co	ounter/PPG output/	pulse width	Interval timer/external	event counter/	Interval timer/external event counter PPG output*9/pulse width
			p	oulse width measurement/square-v	wave output/one-shot pulse ou	tput	width measurem	nent/square-w	wave output/one-s	shot pulse output	measurement/square-wave of	output/one-shot pul	se output	pulse width meas	surement	measurement/square-wave output*9 one-shot pulse output*9
	16-bit timer (TMx)	Number of channels	_	_	_	_	_	_	_	_	_		_	2	2	2
		Function	_	-	_	_	-	_	_	_	-		_	PWM output/AD conversion	: start timing signal ou	utput/capture function
	8-bit timer (TMH)	Number of channels	1	1	2	2		2	2	2	2		1	1	1	1
			l Ini	tory of times /DW/M output / corrier of	_	:					_	D\A/\A output/		Interval timer/DW/A		rotor output/
		Function		terval timer/PWM output/carrier ge	enerator output/square-wave o	ulpul	Interval timer /PWN carrier generator	output/	nterval timer/PWN			PWM output/			VI output/carrier genera	ator output/
							square-wave or	utput ger	nerator output/squ		carrier generator outp	ut/square-wave		sq	uare-wave output	
	8-bit timer (TM5)	Number of channels	1	1	2	2	2	2	2	2	2		2	1	1	1
		Function	Interval timer/ex	kternal event counter	Interval timer/exte	rnal event counter/	Interval timer/exterr counter/PWM o		terval timer/extern	hal event counter/	Interval timer/exte	ernal event coun	ter/	Interval tim	ner/external event cou	nter
	Watchdog timer (WDT Watch timer Real-time counter (RT				PWM output/squ	are-wave output	square-wave o	utput F	PWM output/squa	are-wave output	PWM output/so	uare-wave outp	ut			
	8-bit timer (TM8)	Number of channels	-	-	-	-	-	-	-	-	-		-	-	-	-
		Function	-	-	-	-	-	-	-	-	-		-	-	-	-
	Watchdog timer (WD	DT)	1	1	1	1	1	1	1	1	1		1	1	1	1
	Watch timer		-	-	-	-	-	-	-	-	-		-	-	-	-
	Real-time counter (R	RTC)	-	-	-	1	-	1	1	1	-		-	-	-	-
Serial interface	UART (supporting LI	N)	1	1	1	1	1	1	1	1	-		-	-	1 ^{*10}	1*10
	UART		-	-	-	-	-	-	-	1	1		1	-	-	-
	UART/CSI		_	_	_	_	_	-	1	1	_		_	_	_	_
			-	-/1/1	1	2	1	1	1	1	-		1	_	_	1
		receive 3-wire CSI	_	_	_	_	_	_	-	_	_		_	_	_	_
			1	1	1	1	1	1	3	3	_		_	_	1	1
A/D converter	Successive approxir		10 bits × 4	10 bits × 6/10 bits × 7/10 bits × 11		10 bits × 10/10 bits × 11/ 10 bits × 11			10 bits × 8	10 bits × 8			_	10 bits × 5	10 bits × 6	10 bits × 9
A/D converter		nation												TO DILS X 5		
	$\Delta \Sigma$		-	-	-	-		-	-	_	-		-	-	-	-
Interrupt	External		2	4/5/5	8	9/10/13		3/11	8	8	8		4	7	7	9 (30-pin)/8 (32-pin)
	Internal		10	10/11/11	13	16	14 1	4/16	25	25	10		14	8	12	13
Maximum number of	8 commons		-	-	-	-	-	-	-	-	-		-	-	-	-
segments displayed in LCD	4 commons		-	-	-	-	-	-	-	-	-		_	-	-	-
On-chip debug			Supported	Supported	Supported	Supported	Supported Sup	ported S	Supported	Supported	Supported		Supported	Supported	Supported	Supported
Multiplier/divider			-	-	-	-	16-bit × 16-bit 16-b	it × 16-bit 16	6-bit × 16-bit	16-bit × 16-bit	-		-	8-bit × 8-bit,	8-bit × 8-bit,	8-bit × 8-bit,
	iplier/divider						32-bit ÷ 16-bit 32-b	it ÷ 16-bit 32	2-bit ÷ 16-bit	32-bit ÷ 16-bit				16-bit × 16-bit	16-bit × 16-bit	16-bit × 16-bit
Low-voltage detector (LVI)			2.07 V ±0.07 V or 1.	91/2.22/2.38/2.53/2.68/2.84/2.99/	3.15/3.30/3.45/3.61/3.76/3.92/	4.07/4.22 V (default) ±0.1 V	*1		2.85/3.01/3.16/3.32		2.08 V +0.07 V -0.08 V, 1.93 to 3	47 V ±0.1 V.	*7	1.91/2.84/2.99/3.15/3.30/3.45	5/3.61/3.76/3.92/4.07/	4.22 V (default) ±0.1 V
				(Selectable I	by software)			The	e detected voltage a (Selectable by		The detected voltage can be (Selectable by soft	input to pins.		(Sele	ectable by software)	
Power-on clear (POC)	r-on clear (POC)			Power-on reset: 1.61 V ±0.09 V, p		V	1.59 V ±0.1	5 V	1.59 V ±	0.15 V	1.8 V ±0.1 V		1.59 V ±0.15 \	Power-on reset: 1.61 V ±0	0.09 V, power-down re	eset: 1.59 V ±0.09 V
Other			Operational	Operational	Operational	Clock output Clock output (48-pin on)	nly), Operational	*0 CE	EC, clock output,	*3	-		*8	Operational	Operational	al Operational
			amplifier: 1 ch	$T_{a} = -40$	to +85°C	(48-pin only) operat ional amplifier: 2 c	$T_A = -40 \text{ to } +$		ote control receiver $T_{A} = -40 \text{ to}$	-		10 to +85°C	-	amplifier: 1 ch	$= -40 \text{ to } +105^{\circ}\text{C}$	ch amplifier: 1 ch
		7/3 62/3 78/2 02/4 00/4 04 14/-	efault) +0.1.V	*4. N-ch open-drain output and P-c		alv used as CMOS I/O	1.11		-speed function		· · · · ·			14		
				*5. Ceramic/crystal/external clock:		., 300 ao 0100 i/0.			-	ported in 32-pin	products.					
Derating temperature 1.93/2.08/2.24/2.39/2.55/2.70/2.85/ The detected voltage can be input t Clock output, operational amplifier: CEC, clock output, buzzer output, b				*6. 240 kHz ±10% (clock for watch						can be perform						
3. UEU, Clock output, buzzer ou	uput, remote control rece	liver		*7. 4.24 V ±0.1 V or 4.09 V ±0.1 V (s	selectable by software)		Rem	arks The st	pecifications of i	products still une	der development or in planning	are subject to cha	inge without	DOUCE.		

50 *3. CEC, clock output, buzzer output, remote control receiver

*7. 4.24 V \pm 0.1 V or 4.09 V \pm 0.1 V (selectable by software)

Remarks The specifications of products still under development or in planning are subject to change without notice.

Product specifications (8-bit 3/3)

CPU Core			78K0	78K0							7	8K0										78ł	K0
Commercial name				µPD78F071x	78	3K0/LC	3		78K0/L[D3			78K0/LE3						78K0/LF3	}		78K0/LE3-M*17	
'in count				30-pin 64-pir		48-pin			52-pin				64-pin						80-pin			64-pin	
oduct name			024 025			10 33	411 412 413	420 421 422		631 432	441	444	51 52 53	455 461	462 463 464	465 471	472	474 475 481	82 83 84	485 491 492	493 494 495	052 053	054
			µРD78F8(µРD78F8(µРD78F0711 µРD78F0712 µРD78F0714	µРD78F0400 µРD78F0401 µРD78F0402	µPD78F04 µPD78F04	μΡD78F0411 μΡD78F0412 μΡD78F0413		µPD78F0423 µPD78F0430	µPD78F00	µPD78F0. µPD78F0.	µРD78F0	µРD78F04 µРD78F04 µРD78F04 µРD78F04	µPD78F04 µPD78F04	µРD78F0 µРD78F0	µPD78F04 µPD78F04	8 8	µРD78F04 µРD78F04 µРD78F04	µРD78F04 µРD78F04 µРD78F04	µРD78F0, µРD78F0, µРD78F0,	, µРD78F04 µРD78F04	µPD78F8(µPD78F8(µPD78F8(
ash memory (bytes)			8 K 32 K	8 K 16 K 32 K	8 K 16 K 24 K	32 K 8 K	16 K 24 K 32	K 8 K 16 K 24 I	K 32 K 8 K	16 K 24 K 32	2 K 16 K 24 K 32	K 48 K 60 K 1	6 K 24 K 32 K 48 K	60 K 16 K	24 K 32 K 48 K	60 K 16 K	24 K 32 K	48 K 60 K 16	K 24 K 32 K 48 I	60 K 16 K 24	K 32 K 48 K 60	K 16 K 32 K	48 k
	Bank		_			_			_				_						_			-	
M (bytes)			512 1 K	768 768 1 K	512 768 1 K	1 K 512	768 1 K 1 F	K 512 768 1 K	(1 K 512	768 1 K 1	K 768 1 K 1	K 2K 2K	68 1 K 1 K 2 K	2 K 768	1K 1K 2K	2 K 768	1K 1K	2 K 2 K 76	3 1 K 1 K 2 K	2 K 768 1 k	K 1 K 2 K 2 F	768 1 K	21
wer supply	Normal operation m	ode	1.8 to 5.5 V*2	4.0 to 5.5 V									1.8 to 5.5 V									1.8 to	3.6
Itage	Flash memory progr	amming mode	2.7 to 5.5 V*2	4.0 to 5.5 V									2.7 to 5.5 V									2.7 to	_
nimum instruction execu	tion time		*3	0.10 µs (20 MHz:									0 MHz: V _{DD} = 2.7 to									0.2 µ (10 MHz: V _{DD} =	JS 2.
				V _{DD} = 4.0 to 5.5 V)								0.40 µs (5 MHz: V _{DD} = 1.8 to	5.5 V)								0.4 µ (5 MHz: V _{DD} =	_
ock	Main clock	High-speed system clock	4	Ceramic/crystal/external clock: 5 to 20 MHz								Ceramic/crys	al/external clock: 2	to 10 MHz								Ceramic/external clock:	: 2
		High-speed internal oscillator	8 MHz ±5%	8 MHz ±5% —									8 MHz ±5%									8 MHz	
	Subclock		-	-									rystal: 32.768 kHz									Crystal: 32	2.7
	Low-speed internal	oscillator	*5	240 kHz						24	40 kHz ±10% (cl	ock for watchdo	g timer, 8-bit timer T	MH1, and L	CD controller/di	iver)							_
) ports	Total		23	15 48		30			34				46						62			32	
	CMOS I/O		21	11 40		26			30				42						58			29	
	CMOS input		-	4 8		4			4				4						4			2	
	CMOS output		-	-		-			-				_						-			1	
	N-ch open-drain		2	-		-			-				_						-			-	
ier	16-bit timer (TM0)	Number of channels	1	1		1			1				1						1			1	
		Function	PPG output/pu	/external event counter/ ulse width measurement/ tput/one-shot pulse output							Interval timer/		ounter/PPG output/ output/one-shot puls		measurement/							Interval external ever pulse width m	en
	16-bit timer (TMx)	Number of channels	-	-		-			-				-						-			-	
		Function	-	-		-			-				_						-			-	
	8-bit timer (TMH)	Number of channels	2	- 1		3			3				3						3			3	
		Function		r/PWM output/carrier put/square-wave output							Interval time	er/PWM output/	carrier generator out	put/square-v	wave output							Interval timer/F carrier genera square-way	ra
	8-bit timer (TM5)	Number of channels	2	2		3			3				3						3			3	
		Function		, /external event counter/ t/square-wave output							Interval time	r/external event	counter/PWM output	ut/square-wa	ave output*10							Interval timer/ex counter/PW square-way	٧N
	8-bit timer (TM8)	Number of channels	-	-		_			-				_						-			-	
		Function	-	-		-			-				-						-			-	
	Watchdog timer (WE	T)	1	1		1			1				1						1			1	
	Watch timer		-	-		-			-				-						-			-	
	Real-time counter (F	TC)	-	-		1			1				1						1			1	
ial interface	UART (supporting LI	N)	1	-		1			1				1						1			1	ſ
	UART		-	1		1			-				_						-			-	
	UART/CSI		1	-		-			1				1						1			1	
	CSI		-	- 1		-			-				_						-			-	
	Automatic transmit/	eceive 3-wire CSI	-	-		-			-				_						1			-	
	I ² C		1	-		_			-				_						-			-	
) converter	Successive approxir	nation	10 bits × 4	10 bits $\times 4^{10 \text{ bits}}_{\times 8}$	-		10 bits × 6	-		10 bits × 6		-	10 bits × 8		10 bits × 8		_		10 bits × 8	-	10 bits × 8	10 bits × 1	1
	$\Delta \Sigma$		-	-	-		-	-		-		-	_		16 bits × 3		_		-		16 bits × 3	24 bits × 3	2
errupt	External		6	5 8		5			5				6						7			4	l
	Internal		14	14 20	17		18	19		20	1	9	20		21		20		21		22	17	_
ximum number of	8 commons		-	-		144			160		2	24	224		160		288		288		224	-	ĺ
ments displayed in LCD	4 commons		-	-		88			96		1	28	128		96		160		160		128	96	
chip debug			-	Supported	5	Supported			Supported	ł			Supported						Supported			Supported	s
ltiplier/divider			-	16-bit × 16-bit		-			_				-						-			-	Ĩ
				32-bit ÷ 16-bit																			
w-voltage detector (LVI)			*6	4.3 V ±0.2 V						1.93/ 2.			1/3.16/3.32/3.47/3.6 be input to pins. (S			fault) ±0.1 V,						*1	1
wer-on clear (POC)			1.59 V ±0.15 V	3.5 V ±0.2 V									1.59 V ±0.15 V									1.59 V ±	έC
her			*7	*8 *9	Manchester cod	e generato	r, buzzer output	t		M	lanchester code	generator, buzz	er output, remote co	ntrol receive	r	Man	chester cod	le generator, b	uzzer output, rem	ote control rece	iver, clock output	*12	2
			$T_A =$	T _A = -40 to +85°C	1								with expanded temp									T _A = -40 to	10
perating temperature																							

*3. 0.1 μ s (20 MHz: V_{DD} = 2.7 to 5.5 V)/0.4 μ s (5 MHz: V_{DD} = 1.8 to 5.5 V) (When the constant-current driver is not used) 52 *4. Ceramic/crystal/external clock: 1 to 20 MHz

*11. 1.93/2.08/2.24/2.39/2.55/2.70/2.85/3.01/3.16/3.32 V ±0.1 V. The detected voltage can be input to pins. (Selectable by software)

*7. Constant-current driver for which stepping up or stepping down can be specified. *8. Timer for 10-bit inverter control, real-time output port, Hi-Z output controller

(default) ±0.1 V. The detected voltage can be input to pins. (Selectable by software)

*12. Power calculation, power quality measurement, digital frequency conversion, buzzer output, remote control transmitter

MEMO



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