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# M16C/62A Group Operation of Timer A (timer mode)

## 1.0 Abstract

In timer mode, choose functions from those listed in Table 1. Operations of the circled items are described below.

### Table 1. Choosed functions

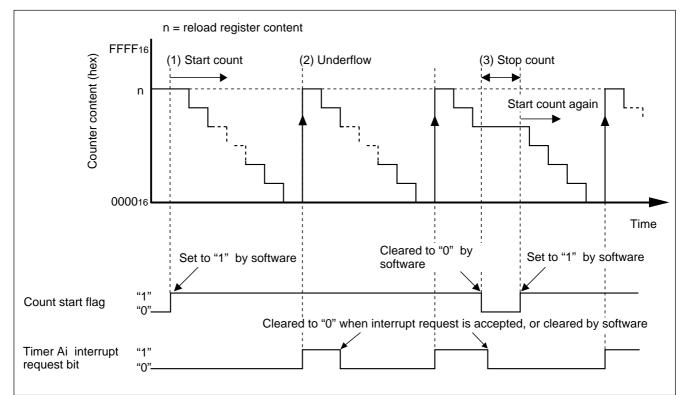
| Item                  | Set-up               |  |  |
|-----------------------|----------------------|--|--|
| Count source          | 0                    | Internal count source (f1 / f8 / f32 / fc32)                             |  |
| Pulse output function | n O No pulses output |  |  |
|                       |                      | Pulses output  |  |
| Gate function         |                      | No gate function   |  |
|                       |                      | Performs count only for the period in which the TAin pin is at "L" level |  |
|                       |                      | Performs count only for the period in which the TAin pin is at "H" level |  |

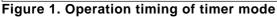
### 2.0 Introduction

Operation (1) Setting the count start flag to "1" causes the counter to perform a down count on the count source.

- (2) If an underflow occurs, the content of the reload register is reloaded, and the count continues. At this time, the timer Ai interrupt request bit goes to "1".
- (3) Setting the count start flag to "0" causes the counter to hold its value and to stop.

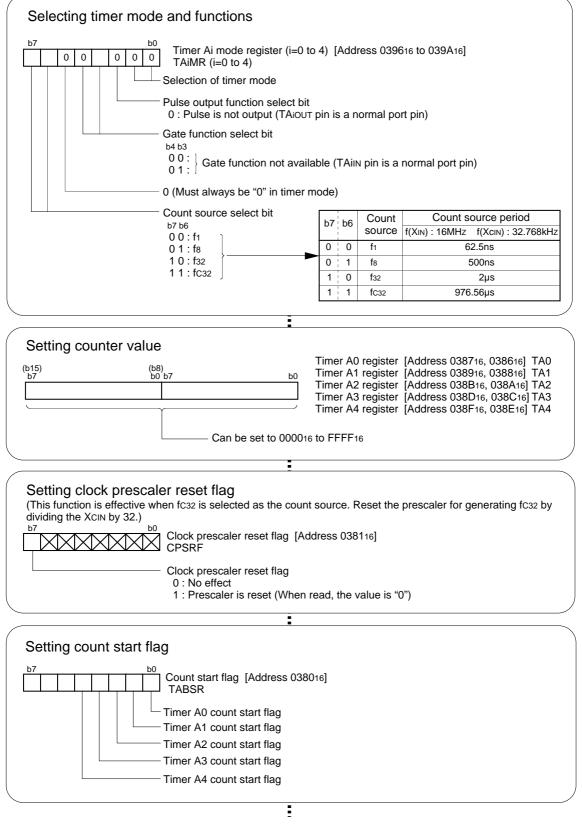
Figure 1 shows the operation timing







### 3.0 Set-up procedure



Start count



### 4.0 Programming Code

```
M16C/62A Program Collection
:
 FILE NAME : rjj05b0030_src.a30
 CPU : M16C/62A Group
:
 FUNCTION : Operation of Timer A
:
        (timer mode)
  HISTORY : 2003.05.16 Ver 1.00
;
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;
;
    Include
.LIST OFF
                   ;Stops outputting lines to the assembler list file
    .INCLUDE sfr62a.inc ;Reads the file that defined SFR
    .LIST
          ON
                  ;Starts outputting lines to the assembler list file
;
Symbol definition
;
ROM_TOP .EQU 0F8000H ;Start address of ROM
FIXED_VECT_TOP .EQU OFFFDCH ;Start address of fixed vector
Program area
:
Start up
.SECTION PROGRAM, CODE ;Declares section name and section type
          ROM_TOP
                  ;Declares start address
    .ORG
RESET:
    MOV.B #03H, prcr
                   ;Removes protect
                    ;Set processor mode registers 0 and 1
         #0000000B, pm0 ; Single-chip mode
    MOV.B
        #0000000B, pm1 ; No expansion, No wait
    MOV.B
                   ;Set system clock control registers 0 and 1
        #00001000B, cm0 ; Xcin-Xcout High
    MOV.B
    MOV.B #00100000B, cml ; Xin-Xout High, Main clock is No divison
    MOV.B
         #00H, prcr ;Protects all registers
;
TimerA (timer mode)
;
MOV.B #01000000B, talmr ;Selecting timer mode and functions
;
          |||||++----;Selection of timer mode
          |||||+-----;Pulse output function select bit (0:Pulse is not output)
;
          |||++----;Gate function select bit (00:Gate function not available)
;
          ||+----;Must always be "0" in timer mode
;
          ++----;Count source (01:f8)
;
         #2000-1, tal ;Setting counter value (1msec @16MHz, f8)
    MOV.W
        #0000000B, cpsrf ;Setting clock prescaler reset flag
    MOV.B
         +-----;Clock prescaler reset flag (0:No effect)
;
         #00000010B, tabsr ;Setting count start flag
    MOV.B
;
             +----;TimerA1 count start flag
;
```



## M16C/62A Group Operation of Timer A (timer mode)

MAIN:

|         | JMP                                | MAIN        |   |  |  |  |
|---------|------------------------------------|-------------|---|--|--|--|
| ;       |                                    |             |   |  |  |  |
| ;====== |                                    | =========== |   |  |  |  |
| ;       | Dummy interrupt processing program |             |   |  |  |  |
| ;====== |                                    |             |   |  |  |  |
| dummy:  |                                    |             |   |  |  |  |
|         | REIT                               |             |   |  |  |  |
| ;       |                                    |             |   |  |  |  |
| ;*****  | * * * * * * * * * * *              | ******      | ***************************************       |  |  |  |
| ;       | Setting of fixed vector            |             |   |  |  |  |
| ;*****  | * * * * * * * * * * *              | ******      | ***************************************       |  |  |  |
|         | .SECTION                           | F_VECT      | , ROMDATA                                     |  |  |  |
|         | .ORG                               | FIXED_      | VECT_TOP                                      |  |  |  |
| ;       |                                    |             |   |  |  |  |
|         | .LWORD                             | dummy       | ;Undefined instruction interrupt vector       |  |  |  |
|         | .LWORD                             | dummy       | ;Overflow (INTO instruction) interrupt vector |  |  |  |
|         | .LWORD                             | dummy       | ;BRK instruction interrupt vector             |  |  |  |
|         | .LWORD                             | dummy       | ;Address match interrupt vector               |  |  |  |
|         | .LWORD                             | dummy       | ;Single-step interrupt vector                 |  |  |  |
|         | .LWORD                             | dummy       | ;Watchdog timer interrupt vector              |  |  |  |
|         | .LWORD                             | dummy       | ;DBC interrupt vector                         |  |  |  |
|         | .LWORD                             | dummy       | ;NMI interrupt vector                         |  |  |  |
|         | .LWORD                             | RESET       | ;Sets reset vector                            |  |  |  |
| ;       |                                    |             |   |  |  |  |
|         |                                    |             |   |  |  |  |

.END



### 5.0 Reference

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### **Data Sheet**

M16C/62A group Rev. C.1 (Use the latest version on the Home page: http://www.renesas.com/)

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M16C/62A group Rev. 1.0 (Use the latest version on the Home page: http://www.renesas.com/)

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