

To our customers,

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April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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## 7545 Group

### Interrupt (INT0 Interrupt)

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#### **1. Abstract**

This document describes how to set up and use the interrupt (INT0 interrupt) on the 7545 Group.

#### **2. Introduction**

The application example described in this document is applied to the following:

MCU: 7545 Group

This sample program may include operations of unused bit functions for the convenience of the SFR bit layout. Set the values according to the operating conditions of the user system.

### 3. Contents

#### 3.1 INT0 Interrupt

Outline: Interrupts are generated by a waveform input to the INT0 pin

Specifications:

- The interrupt routine is executed by the falling edge of the INT0 pin.
- Multiple interrupts are disabled.
- Accumulator A, index register X, and index register Y are used in the interrupt routine.

Figure 3.1 shows the Connection Diagram, Figure 3.2 shows the Relevant Register Settings, and Figure 3.3 shows the Control Procedure.

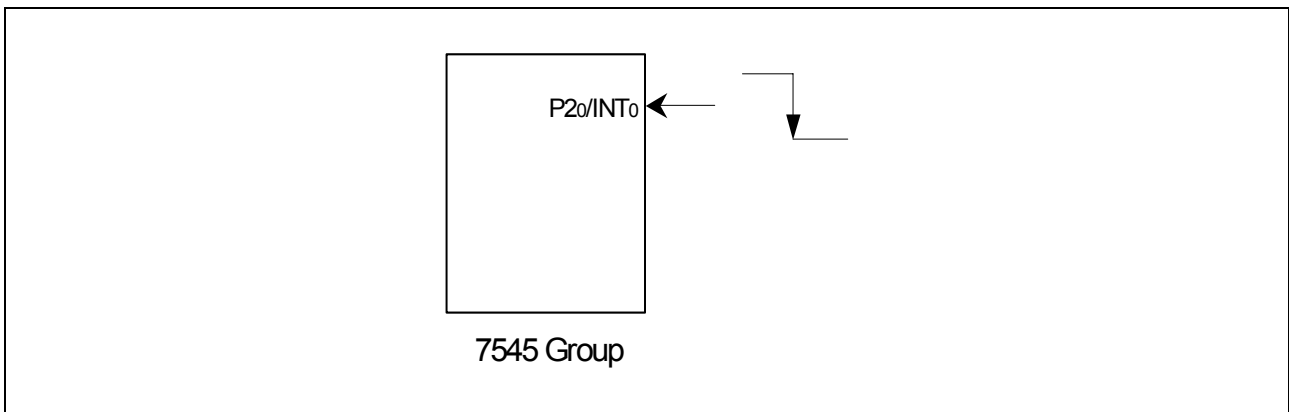


Figure 3.1 Connection Diagram

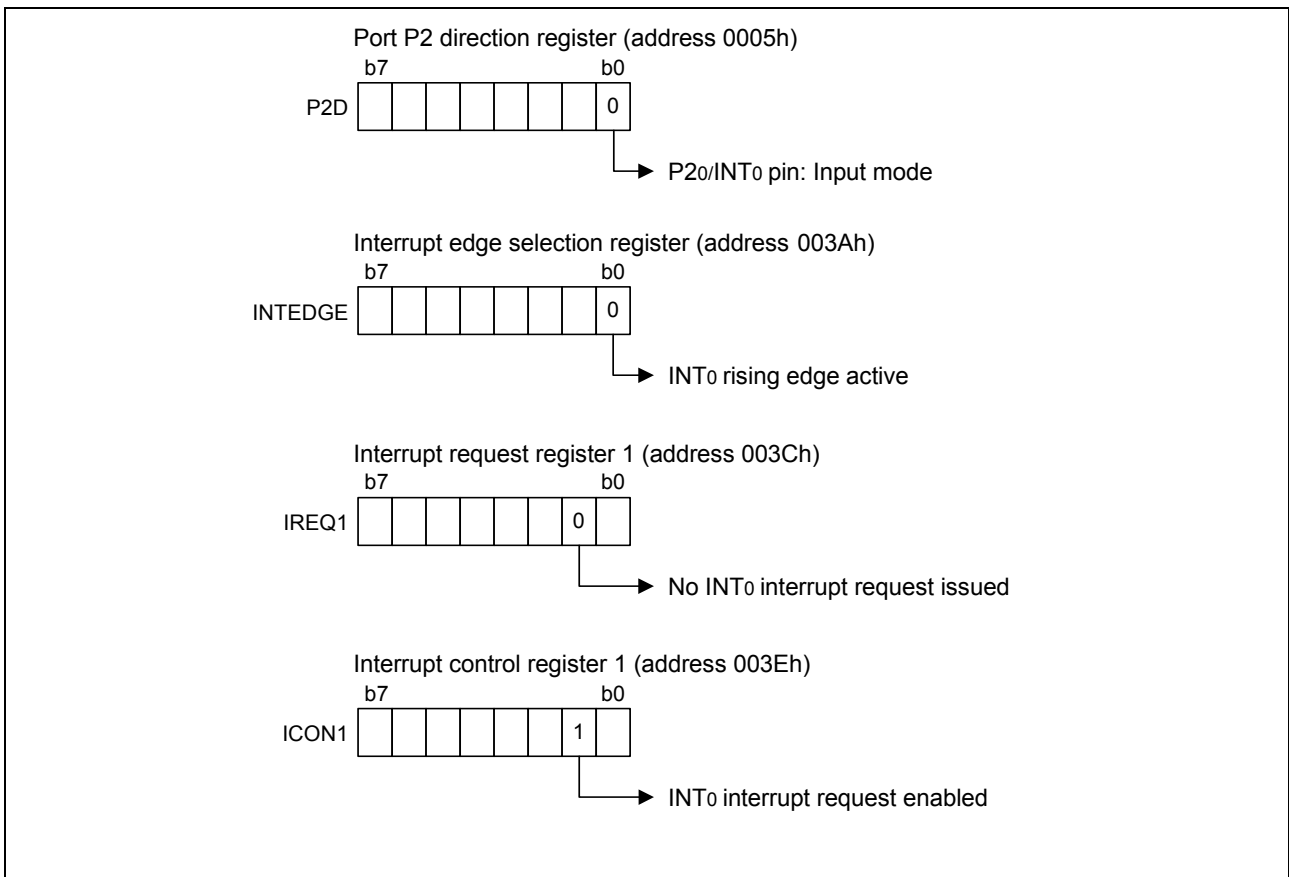


Figure 3.2 Relevant Register Settings

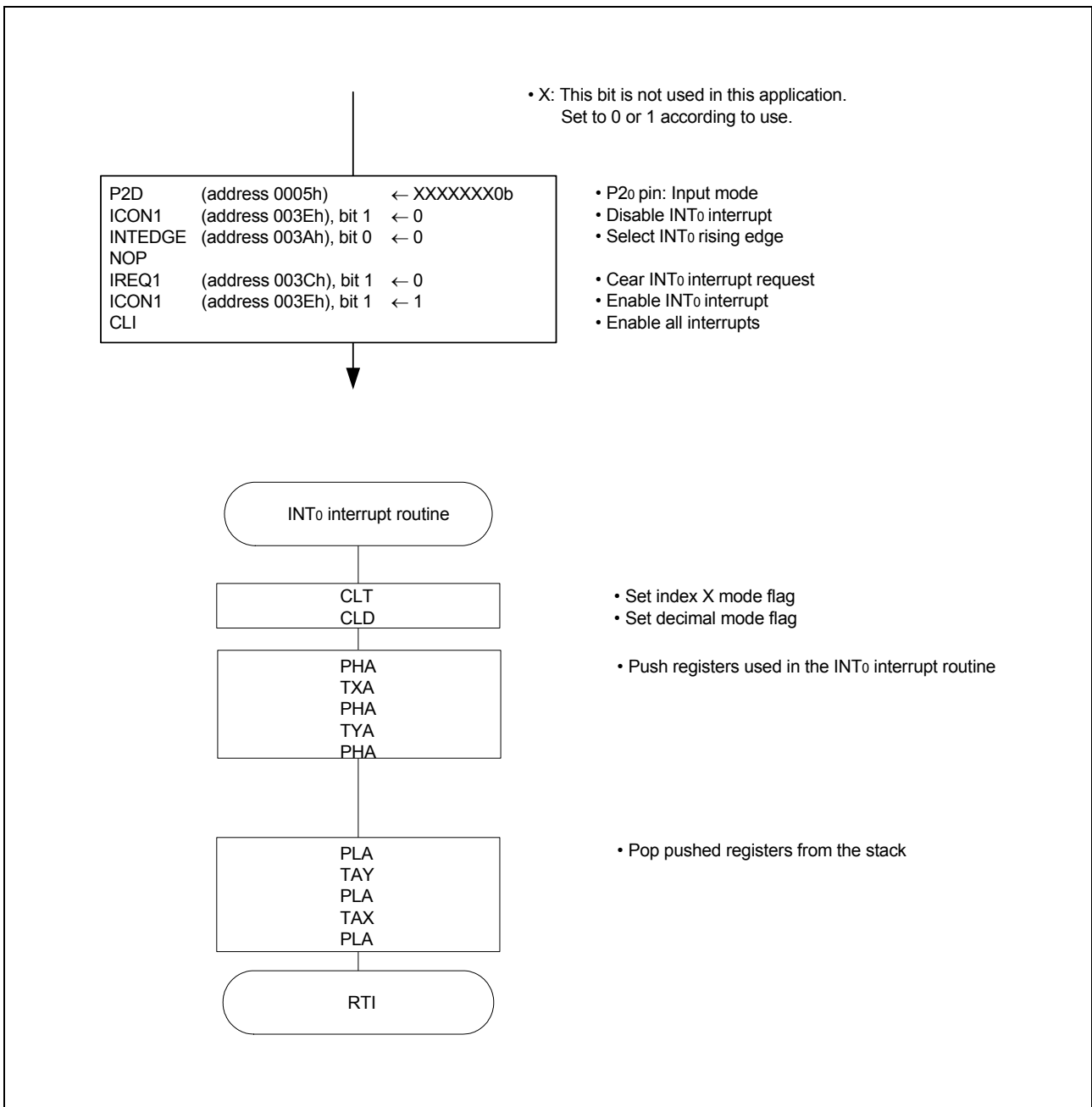


Figure 3.3 Control Procedure

#### **4. Sample Programming Code**

Download a sample program from the Renesas Technology website.

To download, click “Application Notes” in the left-hand side menu on the page of the 7545 Group.

#### **5. Reference Document**

Datasheet

7545 Group Datasheet

Download the latest version from the Renesas Technology website.

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REVISION HISTORY	7545 Group Interrupt (INT0 Interrupt) Application Note
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Rev.	Date	Description	
		Page	Summary
1.00	Aug 10, 2006	-	First Edition issued

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