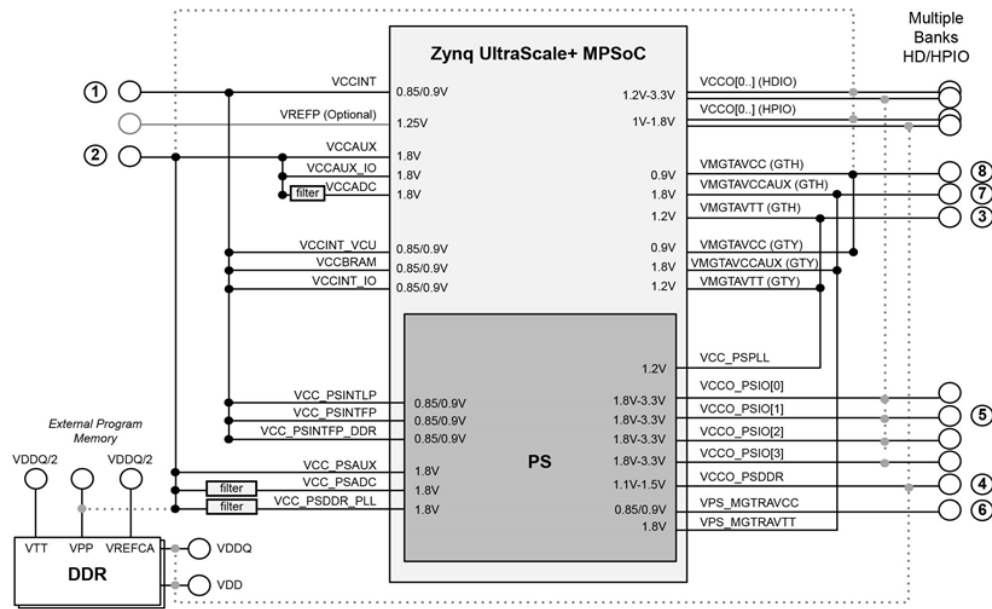
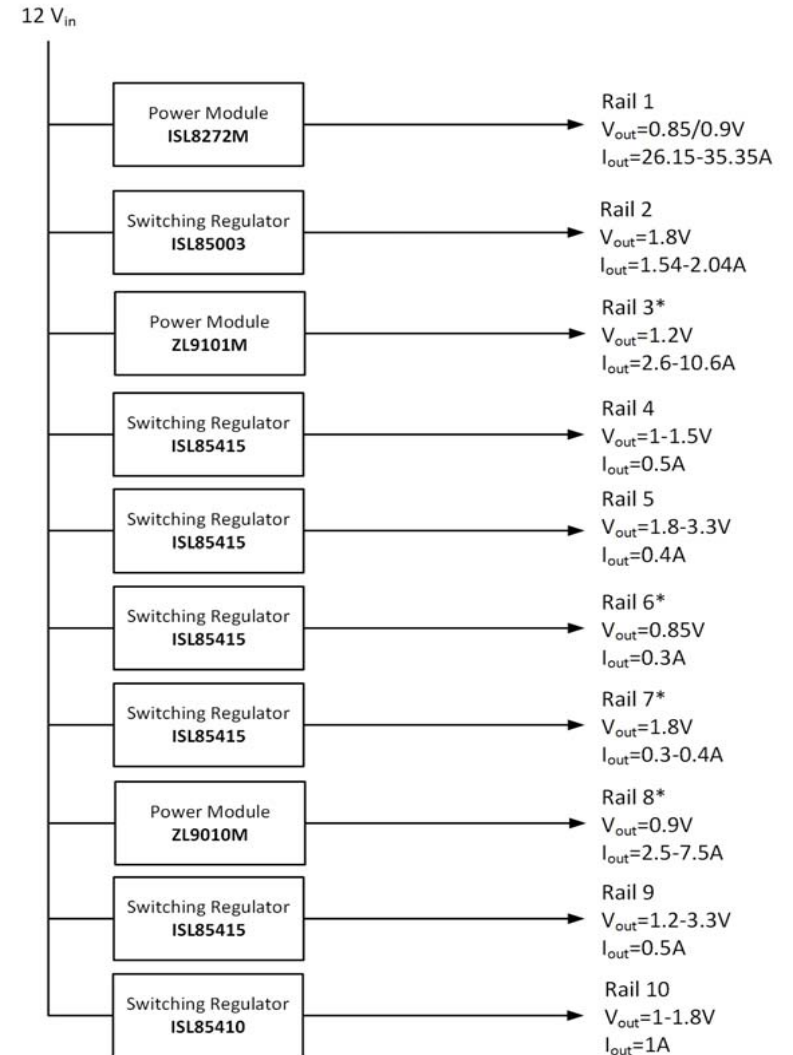


USECASE 1 #4



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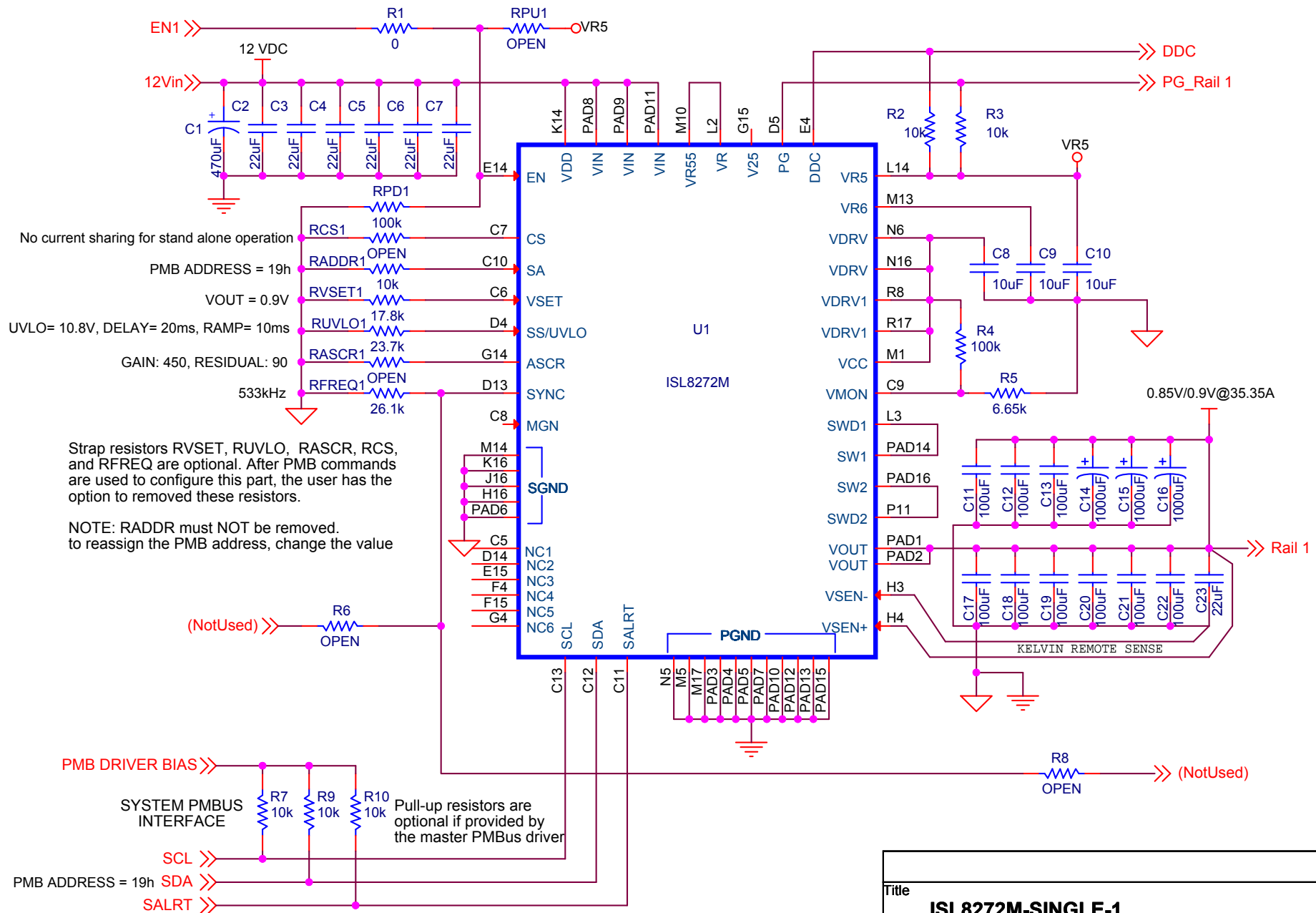
Rail	Voltage	Power Supply Rails
Rail 1	0.85/0.9V	VCCINT, VCCINT_VCU, VCCBRAM, VCCINT_IO, VCC_PSINTLP, VCC_PSINTFP, VCC_PSINTFP_DDR
Rail 2*	1.8V*	VCCAUX, VCCAUX_IO, VCCADC, VCC_PSAUX, VCC_PSDDR_PLL, VCC_PSADC
Rail 3*	1.2V*	VMGTAVTT(GTH), VMGTAVCCAUX(GTY), VCC_PSPLL
Rail 4	1-1.5V	VCCO_PSDDR, DDR_VDD2, DDR_VDDQ
Rail 5	1.8-3.3V	VCCO_PSIQ
Rail 6*	0.85/0.9V	VPS_MGTRAVCC
Rail 7*	1.8V*	VMGTAVCCAUX(GTH), VMGTAVCCAUX(GTY), VPS_MGTRAVTT
Rail 8*	0.9V*	VMGTAVCC(GTH), VMGTAVCC(GTY)
Rail 9	1.2-3.3V	HDIO VCCO
Rail 10	1.0-1.8V	HPIO VCCO



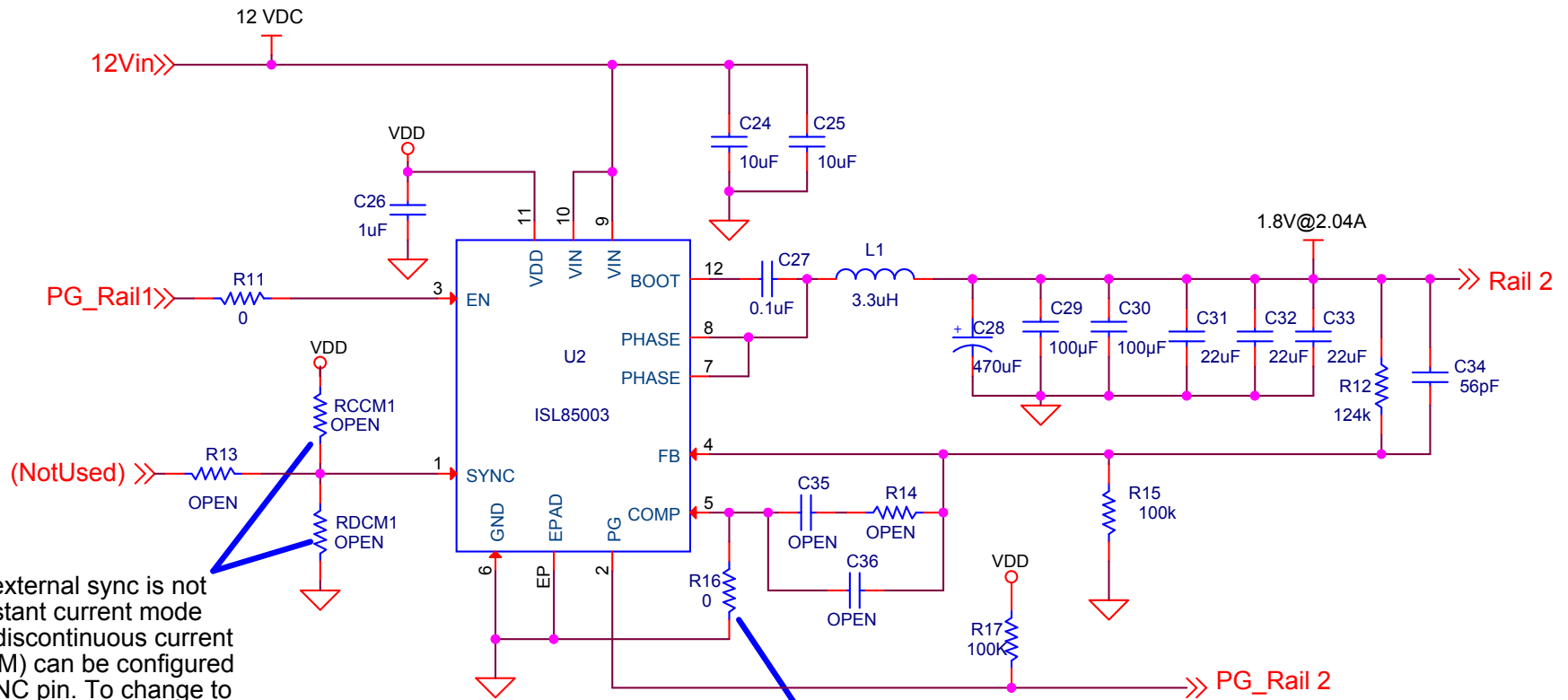
If an external ENABLE is not used, resistor RPU is a pullup for initial evaluation without PMB commands.

Remove RPU1 to make EN low allowing the use of PMB commands.

ISL8272M SINGLE OUTPUT (50A)



ISL85003 SINGLE OUTPUT (3A)



When an external sync is not used, constant current mode (CCM) or discontinuous current mode (DCM) can be configured on the SYNC pin. To change to DCM mode, remove RCCM, and change RDCM to 0 ohms.

If custom compensation is desired, remove the ground connection on the COMP pin to enable external compensation.

OPEN value components on the COMP pin are place holders if external compensation is used.

Programmable Soft Start is available in the ISL85003A (without synchronization)

Title		
ISL85003-SINGLE-1		
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A	<Doc>	<RevCo>
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ZL9101M Digital Module (12A)

12VDC
12Vin>>
PG_Rail 8*>>
R20 0
RPD2 OPEN
RPU2 OPEN
R18 10k
R19 10k
R21 3.9k
R22 1k
C38 0.1uF
U4
OUT
IN
ADJ
ISL80136
NC2
NC3
GND
EN
C39 10uF
C40 330uF
C41 22uF
C42 100pF
C43 4.7uF
C37 4.7uF
C44 22uF
C45 1000uF
C46 1000uF
C47 680uF
C48 680uF
C49 100uF
C50 100uF
C51 100uF
C52 47uF
C53 47uF
C54 47uF
C55 47uF
C56 10uF
C57 10uF
C58 0.1uF
C59 0.1uF
VOUT = 1.2V
571kHz
RADDR2 11k
RVSET2 31.6k
RFREQ2 28.7k
R23 OPEN
R24 OPEN
SCL>>
SDA>>
PMB ADDRESS = 1Ah
PMB DRIVER BIAS>>
SYSTEM PMBUS INTERFACE
R25 10k
R26 10k
Pull-up resistors are optional if provided by the master PMBus driver
Hardware SALRT not available in the ZL9101M

If an external ENABLE is not used, RENPU is an EN pullup for evaluation without PMB commands.
Remove RENPU for EN = 0 to use PMB commands.

PMB ADDRESS = 1Ah

VOUT = 1.2V

571kHz

Strap resistors RVSET and RFREQ are optional. After PMB commands are used to configure this part, the user has the option to removed these resistors.

NOTE: RADDR must NOT be removed. To reassign the PMB address, change the value.

(NotUsed)>>

R23 OPEN

R24 OPEN

(NotUsed)>>

PMB DRIVER BIAS>>

SYSTEM PMBUS INTERFACE

SCL>>

SDA>>

PMB ADDRESS = 1Ah

Hardware SALRT not available in the ZL9101M

Title

ZL9101M-SINGLE-1

Size
A

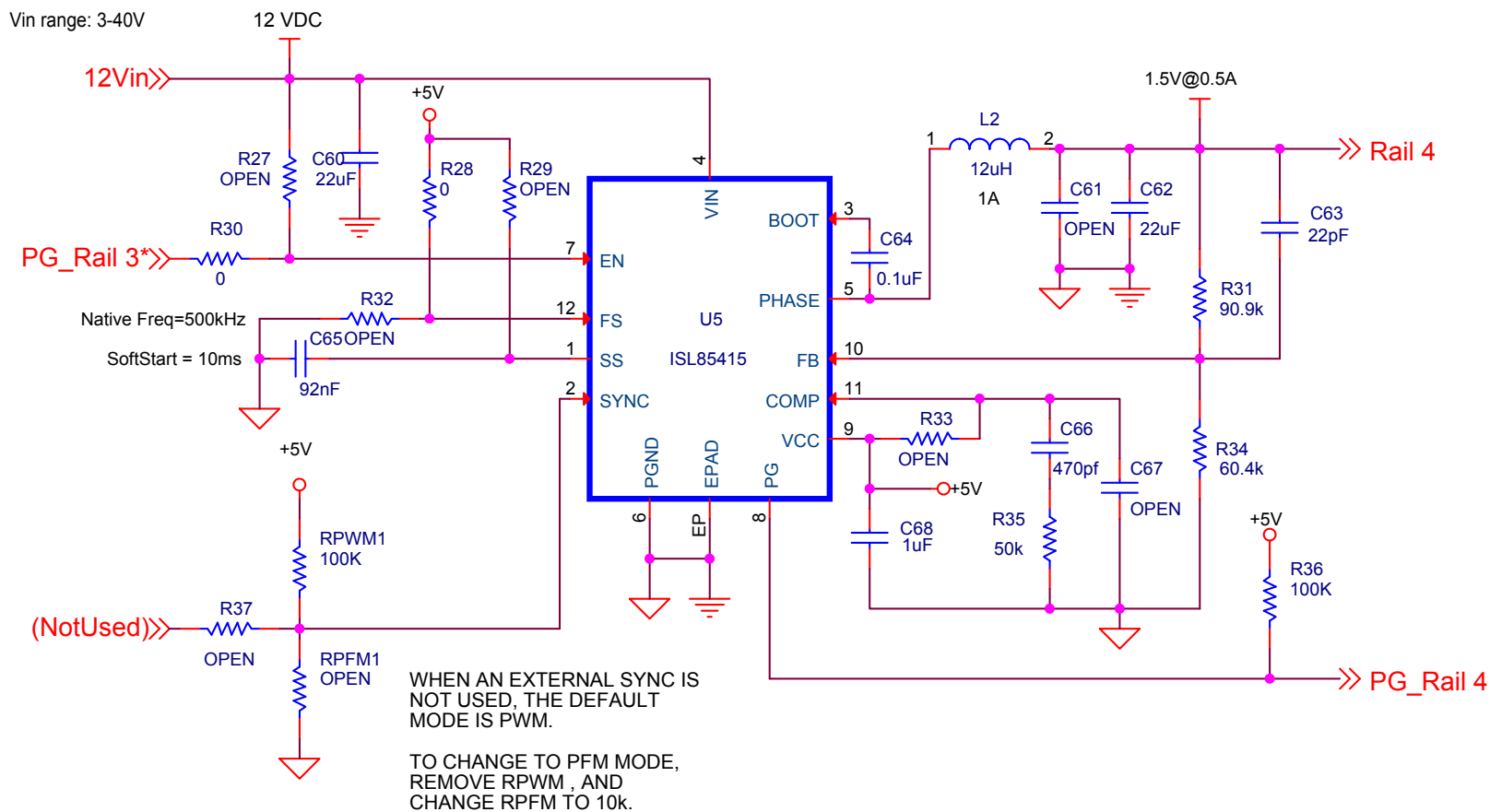
Document Number

Rev

Date:

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ISL85415 SINGLE OUTPUT (500mA)



Title
ISL85415-SINGLE-1

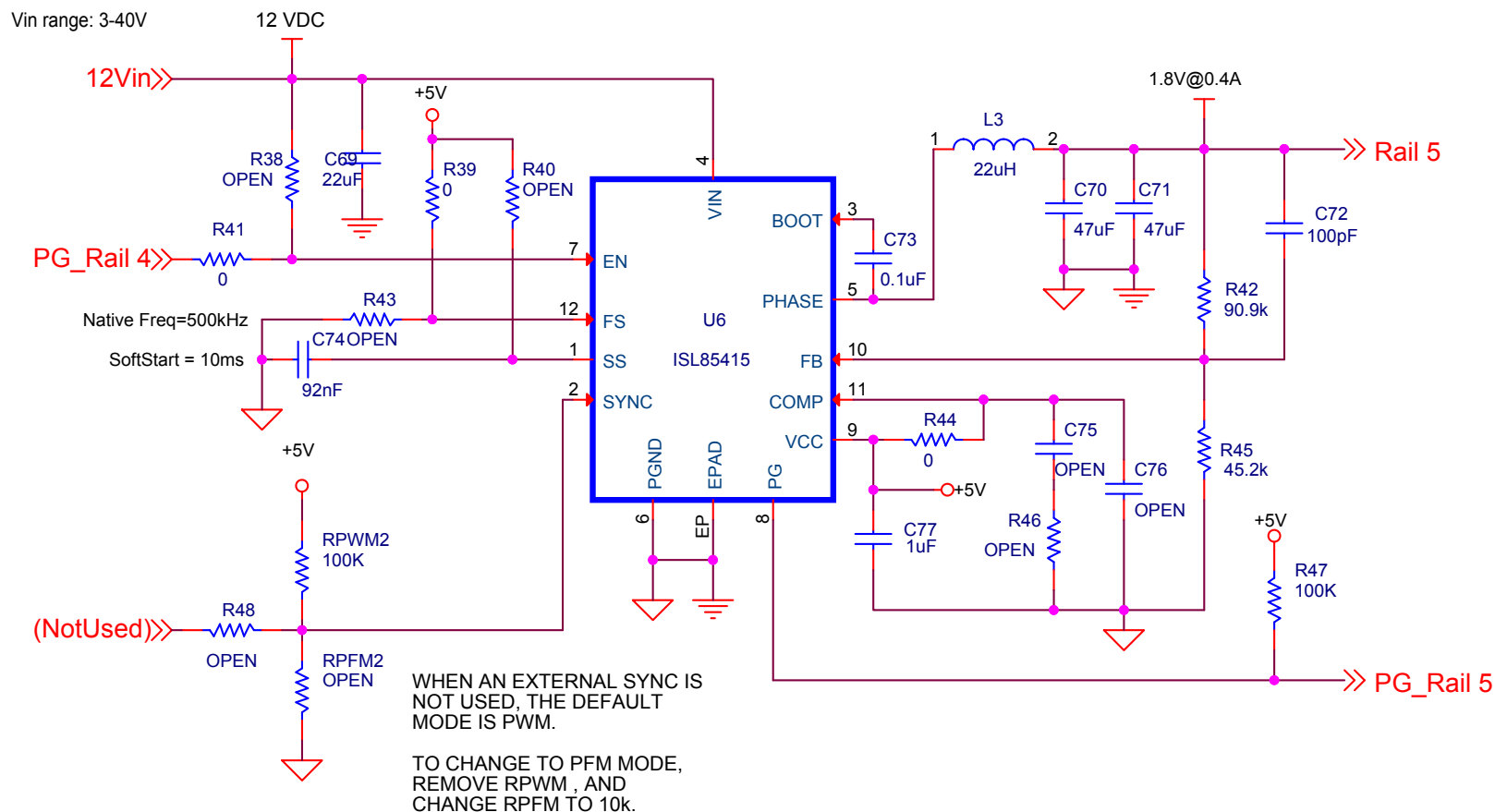
Size
A Document Number

Rev

Date: Thursday, March 09, 2017

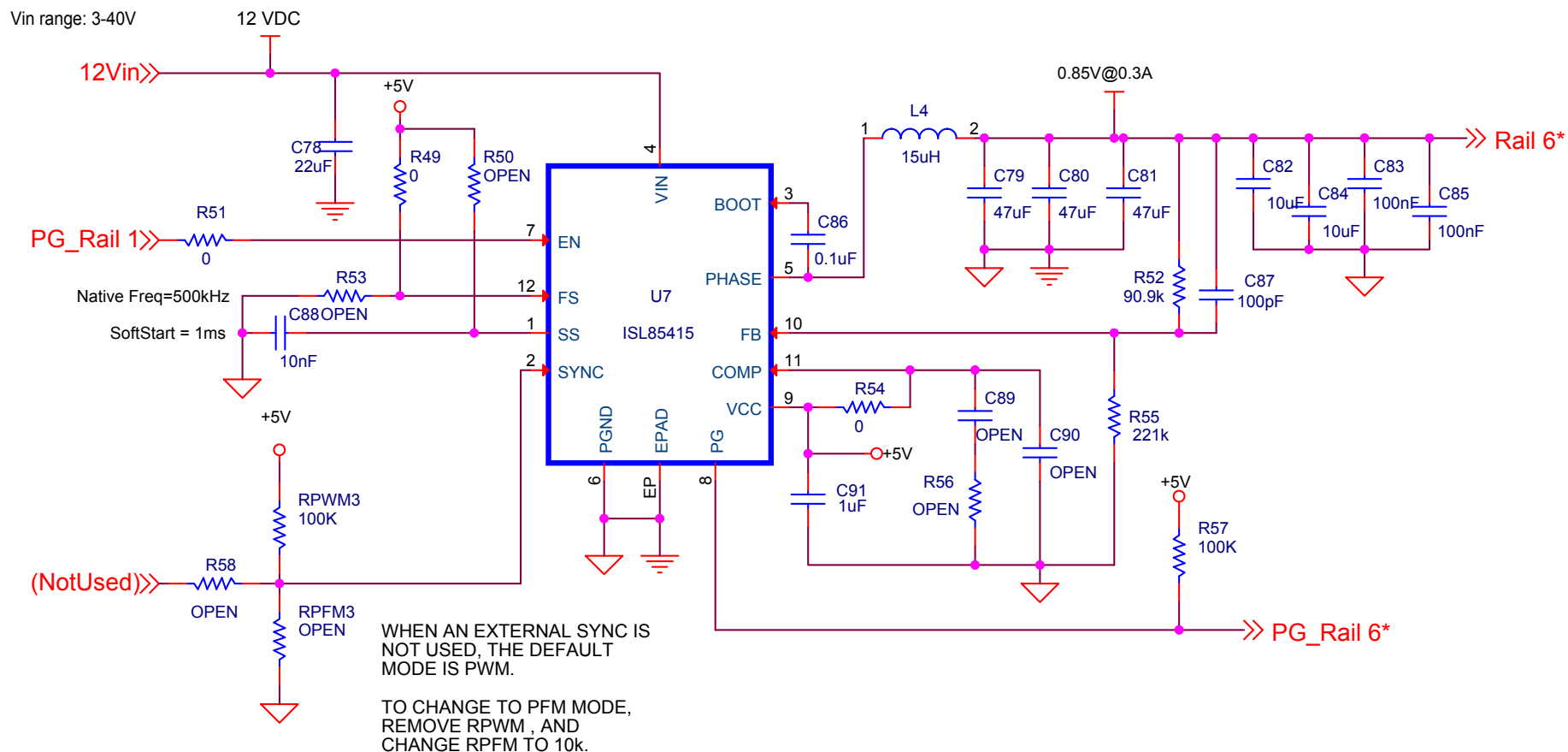
Sheet 5 of 11

ISL85415 SINGLE OUTPUT (500mA)



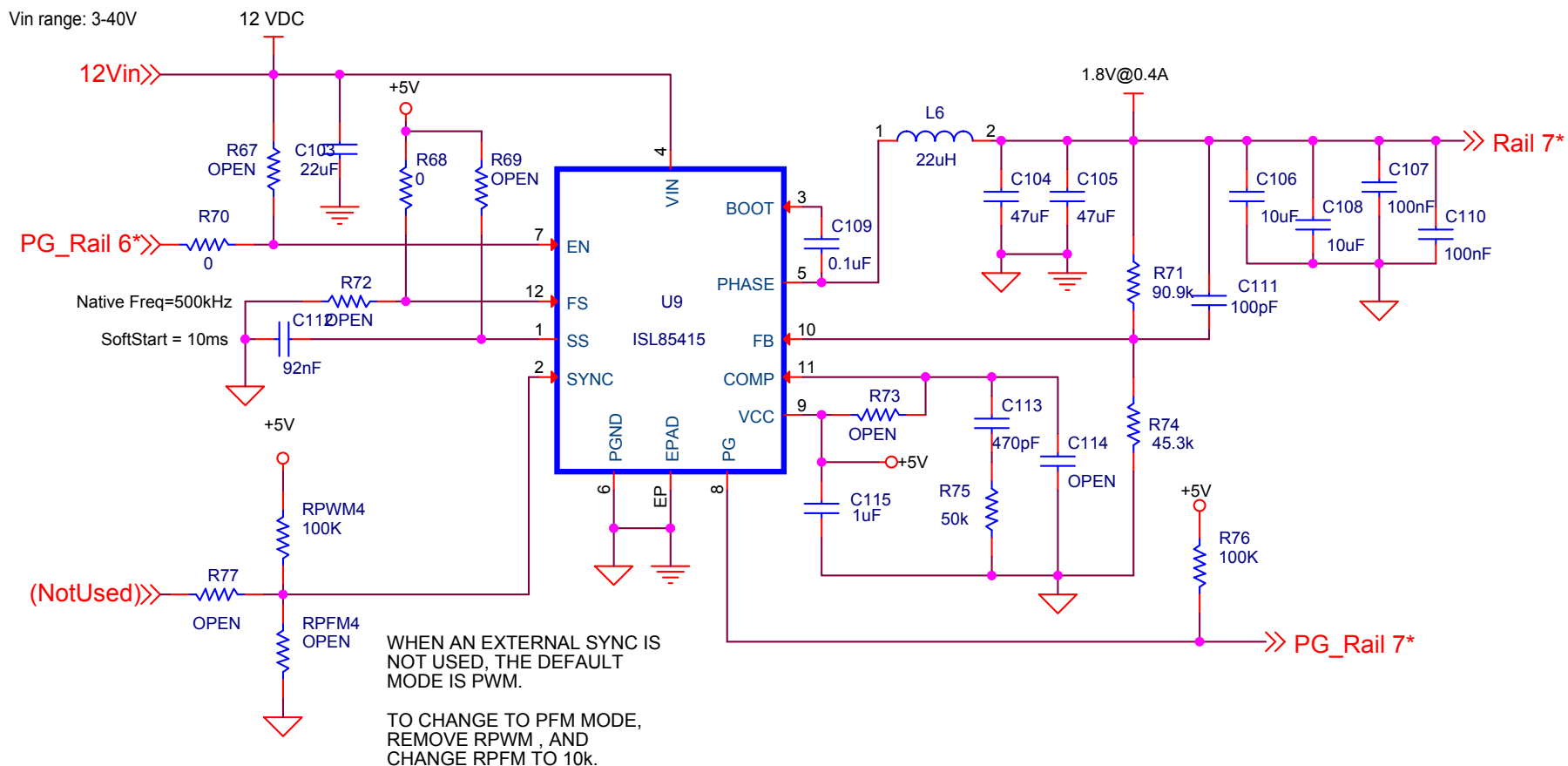
Title		
ISL85415-SINGLE-1		
Size	Document Number	Rev
A		
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ISL85415 SINGLE OUTPUT (500mA)



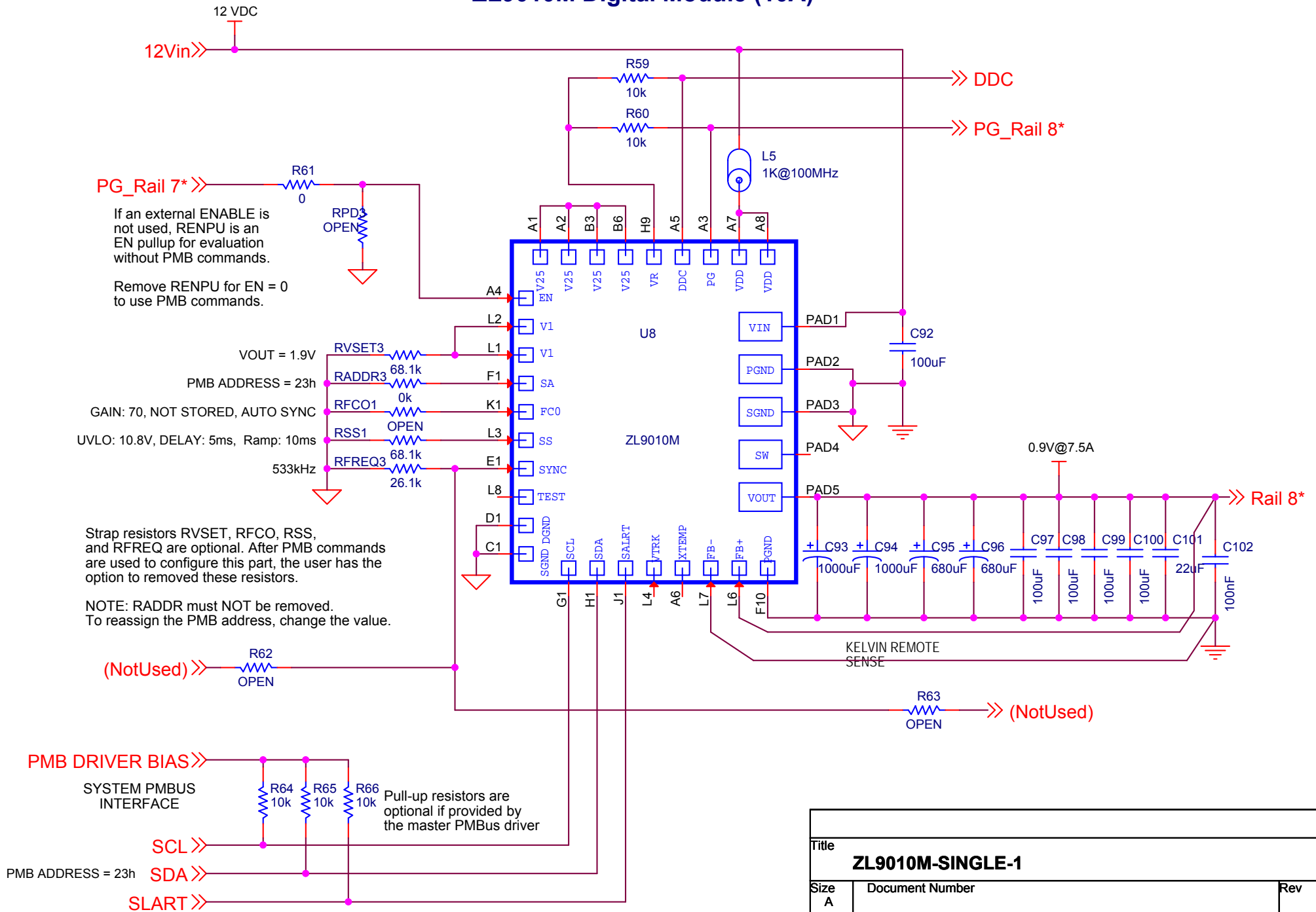
Title		
ISL85415-SINGLE-1		
Size A	Document Number	Rev
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ISL85415 SINGLE OUTPUT (500mA)



Title		
ISL85415-SINGLE-1		
Size A	Document Number	Rev
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ZL9010M Digital Module (10A)

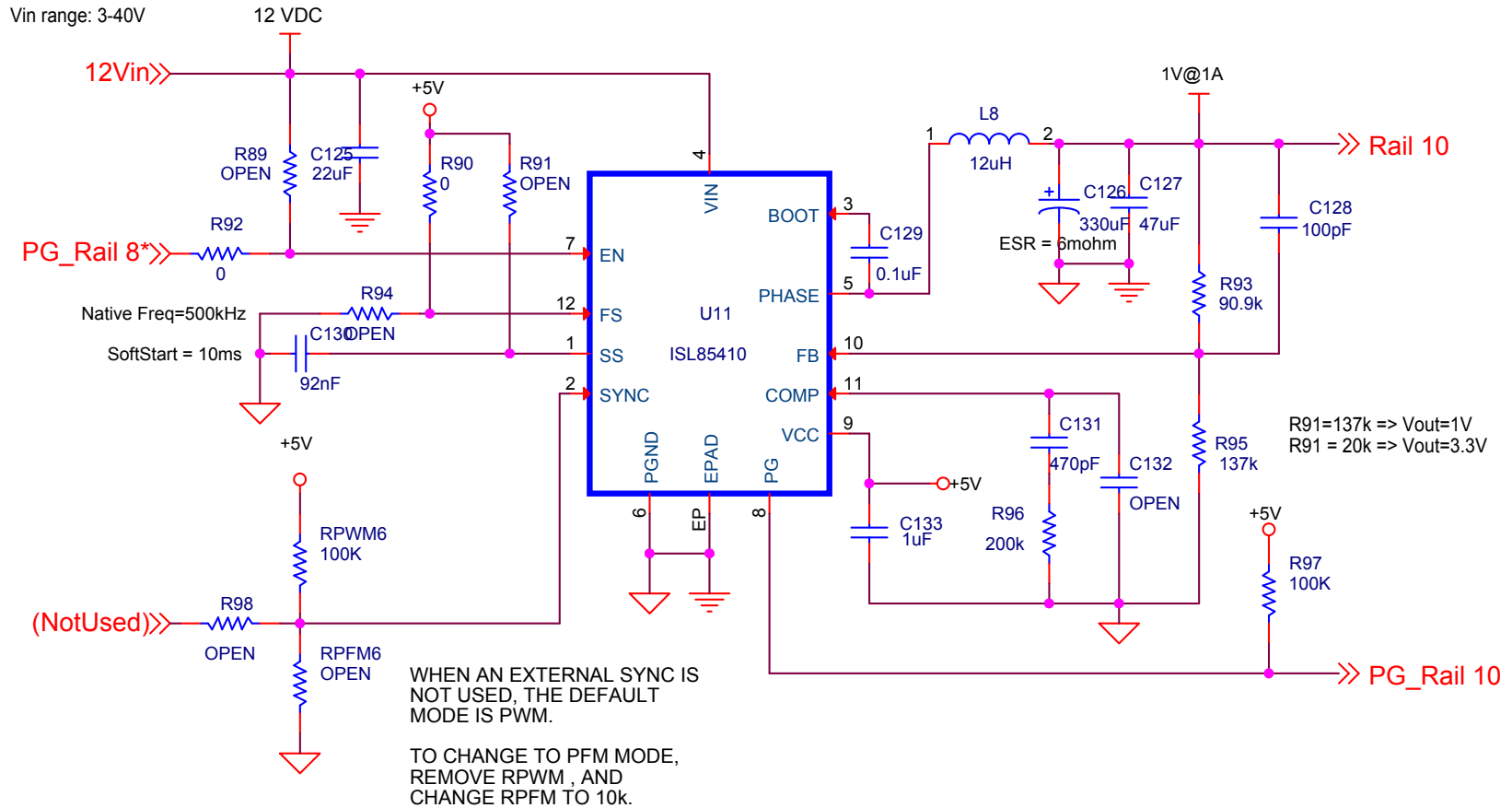


Title		
ZL9010M-SINGLE-1		
Size	Document Number	Rev
A		
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1

ISL85410 SINGLE OUTPUT (1A)



Title		
ISL85410-SINGLE-1		
Size A	Document Number	Rev
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