RENESAS Tool News

The Simulator Debugger for the R32C MCU Series Revised to V.1.01 Release 00

We have revised the simulator debugger for the R32C MCU series from V.1.00 Release 00 to V.1.01 Release 00. This product is included in the C compiler package for the R32C series. For details of the simulator debugger, go to:

   http://www.renesas.com/r32c_sim

The above URL is one of our global site (in English).

1. Descriptions of Revision

1.1 Functions Introduced and Improved

(1) The number of instruction cycles used in cycle-count measurement
   has been modified. For details of the instruction cycles to be
   used, see "R32C/100 Series Software Manual Rev.1.00."
   This manual is also accessible on the Web page at:

http://documentation.renesas.com/eng/products/mpumcu/rej09b0267_r32c100sm.pdf

(2) The MR window can be used which displays the status of real-time OS
   M3T-MR100/4.

(3) The disconnect function is usable.

(4) If a longer instruction is rewritten into a shorter instruction
   in line assembling, the differential can be filled with NOP
   instructions.

(5) When instructions are disassembled, their respective instruction
   format specifiers can be displayed in the Disassembly, Trace,
   and other windows.

(6) Values can be assigned to the members of bit fields.

(7) Data size can be specified when displaying enumeration variables.

For the other functions improved, see the Version Reports chapter
in "R32C Simulator Debugger V.1.01.00 Release Notes."
This Release Note is also accessible on and after July 21 from here.
It is one of our global site (in English).

1.2 Compatibility with Windows Vista(R)

The simulator debugger has been made compatible with the 32-bit edition of Windows Vista(R). On this OS, you can run the simulator debugger with your user rights.
Note that the simulator debugger still remains incompatible with the 64-bit edition of Windows Vista(R).

1.3 High-performance Embedded Workshop Updated

The High-performance Embedded Workshop included in the simulator debugger has been updated from V.4.03.00 to V.4.05.01. For details of the revision from V.4.03.00 to V.4.05.01, made in four steps, see the following RENESAS TOOL NEWS items:
- Document No. 071216/tn5, published on December 16, 2007, at:
  http://tool-support.renesas.com/eng/toolnews/071216/tn5.htm
- Document No. 080118/tn1, published on January 18, 2008, at:
  http://tool-support.renesas.com/eng/toolnews/080118/tn1.htm
- Document No. 081125/tn1, published on November 25, 2008, at:
  http://tool-support.renesas.com/eng/toolnews/081125/tn1.htm
- Document No. 090201/tn3, published on February 1, 2009, at:
  http://tool-support.renesas.com/eng/toolnews/090201/tn3.htm

The updated simulator debugger can be used with High-performance Embedded Workshop V.4.06.00 or later.

1.4 Problems Fixed

The following known problems have been fixed:
(1) With using direct addressing for the Bank 1 registers
   For details see RENESAS TOOL NEWS Document No. 071001/tn4 at:
      http://tool-support.renesas.com/eng/toolnews/071001/tn4.htm
(2) With displaying the values of int-type variables of 32 bits long
   For details see RENESAS TOOL NEWS Document No. 081101/tn4 4 at:
      http://tool-support.renesas.com/eng/toolnews/081101/tn4.htm

2. How to Update Your Product

Online update is available free of charge.
Use AutoUpdate Utility to update yours. This service is available on and after July 22. For details of AutoUpdate Utility, go to:
   http://www.renesas.com/tool_update
The above URL is one of our global site (in English).

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