# [Released on the Web]

R20TS0075EJ0100 Rev.1.00 Oct. 01, 2016

# Parameter Files for the PG-FP5 Flash Memory Programmer

#### **Outline**

We are releasing new parameter files and revising several of the existing ones for the PG-FP5 flash memory programmer.

These parameter files are text files which contain the information required to use the PG-FP5 for writing to certain target MCUs. Refer to the URL below for an overview of the PG-FP5.

https://www.renesas.com/pg fp5

#### 1. New Parameter Files

Parameter file for the RL78/G11 group: PR5-R5F105 V1.00

Supported MCUs: R5F1056A, R5F1057A, and R5F1058A

Parameter file for the RL78/L1A group: PR5-R5F11M V1.00

Supported MCUs: R5F11MMD, R5F11MME, R5F11MMF, R5F11MPE, R5F11MPF, and R5F11MPG

#### 2. Parameter Files to be Revised

## 2.1 Addition of Supported MCUs

➤ Parameter file PR5-RH850C1X for the RH850/C1x series will be revised from V1.00 to V1.01.

Added MCU: R7F701271 (RH850/C1M group)

Parameter file PR5-RH850D1X for the RH850/D1x series will be revised from V1.01 to V1.02.

Added MCUs: R7F701402, R7F701403, R7F701422, and R7F701423 (RH850/D1L group)
R7F701404, R7F701405, R7F701407, R7F701408, R7F701410, R7F701411, R7F701412,
R7F701428, R7F701430, R7F701431, and R7F701432 (RH850/D1M group)

➢ Parameter file PR5-RH850E1X for the RH850/E1x series will be revised from V1.01 to V1.02.

Added MCUs: R7F701215 and R7F701216 (RH850/E1M-S2 group)

➤ Parameter file PR5-RH850F1X for the RH850/F1x series will be revised from V1.05 to V1.06.

Added MCUs: R7F701502, R7F701506, R7F701507, R7F701513, R7F701522, R7F701525, R7F701527, R7F701529, and R7F701531 (RH850/F1H group) R7F701581, R7F701583, and R7F701587 (RH850/F1K group)

R7F701544, R7F701545, R7F701549, R7F701552, R7F701564, and R7F701572 (RH850/F1M group)

Parameter file PR5-RH850P1X for the RH850/P1x series will be revised from V1.02 to V1.03.

Added MCUs: R7F701371 and R7F701372 (RH850/P1H-C group) R7F701304 (RH850/P1M group) R7F701373 and R7F701374 (RH850/P1M-C group)

Parameter file PR5-R5F113 for the RL78/F15 group will be revised from V1.00 to V1.01.

Added MCUs: R5F113GK, R5F113LK, R5F113MK, R5F113PG, R5F113PH, R5F113PJ, and R5F113PK

➤ Parameter file PR5-R5F10N for the RL78/I1C group will be revised from V1.00 to V1.01.

Added MCUs: R5F10NLE, R5F10NME, R5F10NMJ, and R5F10NPG

## 2.2 Modification to Parameter Files for the RX Family

- Parameter file PR5-R5F5230 for the RX230 group will be revised from V1.00 to V1.01.
  - Applicable MCUs: R5F52305 and R5F52306
  - Details of the Correction: A phenomenon which potentially led to errors in the case of FINE communications for ID code protection leading to complete erasure has been corrected.
- Parameter file PR5-R5F5231 for the RX231 group will be revised from V1.01 to V1.02.
  - Applicable MCUs: R5F52315A, R5F52315C, R5F52316A, R5F52316C, R5F52317A, R5F52317B, R5F52318A, and R5F52318B
  - Details of the Correction: A phenomenon which potentially led to errors in the case of FINE communications for ID code protection leading to complete erasure has been corrected.
- Parameter file PR5-R5F523T for the RX23T group will be revised from V1.00 to V1.01.
  - Applicable MCUs: R5F523T3 and R5F523T5
  - Details of the Correction: A phenomenon which potentially led to errors in the case of FINE communications for ID code protection leading to complete erasure has been corrected.
- Parameter file PR5-R5F524T for the RX24T group will be revised from V1.00 to V1.01.
  - Applicable MCUs: R5F524T8 and R5F524TA
  - Details of the Correction: Times for erasure through FINE communications have been shortened.

### 3. Obtaining the Product

To obtain parameter files, download those you need from the download site below.

The above information will be available from October 5.

https://www.renesas.com/pg fp5 download

Refer to chapter 3 of the PG-FP5 User's Manual: Common for details of how to install the file.

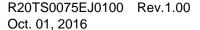
https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut2922

## 4. Required Firmware, FPGA Data, and Programming GUI

Obtain the release notes for the parameter files from the documentation page and consult them to confirm that you have the firmware, FPGA data, and programming GUI versions required to use the parameter files.

https://www.renesas.com/pg\_fp5\_documentation

Parameter files for PG-FP5 release note (This information will be available from October 5.)





## **Revision History**

		Description	
Rev.	Date	Page	Summary
1.00	Oct. 01, 2016	-	First edition issued

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061 Japan Renesas Electronics Corporation

### ■Inquiry

http://www.renesas.com/contact/

Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.

The past news contents have been based on information at the time of publication.

Now changed or invalid information may be included. The URLs in the Tool News also may be subject to change or become invalid without prior notice.

All trademarks and registered trademarks are the property of their respective owners.

© 2016. Renesas Electronics Corporation. All rights reserved.