[Released on the Web]

Parameter File for the PG-FP5 Flash Memory Programmer

Outline

We will release a new parameter file for use with the PG-FP5 flash memory programmer.

- Parameter file for the RL78/I1C group

This parameter file is a text file which contains the information required to use the PG-FP5 for writing to certain target MCUs. Refer to the URL below for an overview of the PG-FP5.

https://www.renesas.com/pg_fp5

1. New Parameter File

➢ Parameter file for RL78/I1C: PR5-R5F10N V1.00

Supported MCUs: R5F10NLG, R5F10NMG, and R5F10NPJ

2. Obtaining the File

To obtain the parameter file, download it from the download site below.

(This file will be released on September 5.)

https://www.renesas.com/pg_fp5_download

Refer to chapter 3 of the PG-FP5 User's Manual: Common for details of how to install the file.
https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut2922

3. Required Firmware, FPGA Data, and Programming GUI

Obtain the release notes for the parameter file from the documentation page and consult them to confirm that you have the firmware, FPGA data, and programming GUI versions required to use the parameter file.

https://www.renesas.com/pg_fp5_documentation

Parameter files for PG-FP5 release note (This document will be available from September 5.)

Revision History

Rev.	Date	Description	
		Page	**Summary**
1.00	Sep. 01, 2016	-	First edition issued

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