

RENESAS TOOL NEWS on December 1, 2012: 121201/tn1

Peripheral Driver Generator Revised to V.2.06

We have revised Peripheral Driver Generator from V.2.05 to V.2.06. This utility program aids you to create on-chip peripheral I/O drivers. Peripheral Driver Generator V.2 is specific to the RX family of MCUs.

For an overview of Peripheral Driver Generator, see:

https://www.renesas.com/pdg

The above URL is one of our global sites.

1. Description of Revision

1.1 Supported MCUs Increased

The revised product supports the RX63T and RX62G groups of MCUs. The following type names and peripheral I/O modules of these MCUs are supported respectively:

(1) RX63T group

Type names:

- R5F563T6EDFM
- R5F563T5EDFM
- R5F563T4EDFM
- R5F563T6EDFL
- R5F563T5EDFL
- R5F563T4EDFL

Peripheral I/O modules:

- Voltage detection circuit (LVDA)
- Clock generator circuit
- Clock frequency accuracy measurement circuit (CAC)
- Low power consumption
- Register write protection function
- Interrupt control unit (ICUb) and exception handlers
- Buses
- DMA controller (DMACA)
- Data transfer controller (DTCa)
- I/O ports
- Multi-function pin controller (MPC)

- Multifunction timer pulse unit 3 (MTU3)
- Port output enable 3 (POE3)
- General PWM timer (GPT)
- Compare match timer (CMT)
- Watchdog timer (WDTA)
- Independent watchdog timer (IWDTa)
- Serial communication interface (SCIc and SCId)
- I2C bus interface (RIIC)
- Serial peripheral interface (RSPI)
- CRC calculator (CRC)
- 12-bit A/D converter (S12ADB)
- Data operation circuit (DOC)

(2) RX62G group

Type names:

- R5F562GAADFH
- R5F562GAADFP
- R5F562G7ADFH
- R5F562G7ADFP
- R5F562GADDFH
- R5F562GADDFP
- R5F562G7DDFH
- R5F562G7DDFP

Peripheral I/O modules:

- Voltage detection circuit (LVD)
- Clock generator circuit
- Low power consumption
- Interrupt control unit (ICU) and exception handlers
- Buses
- Data transfer controller (DTC)
- I/O ports
- Multifunction timer pulse unit 3 (MTU3)
- Port output enable 3 (POE3)
- General PWM timer (GPTa)
- Compare match timer (CMT)
- Watchdog timer (WDT)
- Independent watchdog timer (IWDT)
- Serial communication interface (SCIb)
- CRC calculator (CRC)
- I2C bus interface (RIIC)
- Serial peripheral interface (RSPI)
- LIN module (LIN)
- 12-bit A/D converter (S12ADA) (Comparator excluded.)
- 10-bit A/D converter (ADA)

1.2 Problems Fixed

We have been fixed the following two problems:

(1) With using Clock frequency accuracy measurement circuit (CAC) in MCUs of RX210 group

For details of the problem, see RENESAS TOOL NEWS Document No. 121116/tn1 at:

https://www.renesas.com/search/keyword-search.html#genre=document&q=121116tn1 This page will be opened on December 5, 2012.

(2) The problem that the value after the decimal point can not be processed correctly in Peripheral Driver Generator V2.05 when a decimal point is set as a comma on host PC.

1.3 Restriction Raised

The following restriction has been raised:

- On setting SCI0 in the RX210 group MCUs encapsulated in 80-pin package.

For details of the restriction, see Section 2.12 in the Peripheral Driver Generator V.2.05 Release Note from HERE.

2. Host System Requirements

Computer: IBM PC/AT or compatible

OS: Windows 7, Windows XP, or Windows Vista

All other necessary software environments:

- .NET Framework 3.5 SP1
- Microsoft Visual C++ 2008 SP1 runtime library

3. Precaution

Some known problems reside in Peripheral Driver Generator V.2.06.

For details of these problems, read through the release note and

RENESAS TOOL NEW Document No. 120601/tn6 through /tn10, 120916/tn1

through /tn3, and 121201/tn4. (NOTE)

You can see these news at:

https://www.renesas.com/search/keyword-

search.html#genre=document&documenttype=531&toollayer=300616

NOTE: Document No.121201/tn4 will be published on the above site in mid-December.

4. How to Update Your Product

Update yours in either of the following ways:

- (1) Use Auto Update Utility. This service will be available on and after December 10.
- (2) Download the installer of the revised product from: https://www.renesas.com/pdg_download

Then execute it. The installer will be published on the Web site on December 5.

The above URL is one of our global sites.

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