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The Device File Updater Specific to the C Compiler Package for the M16C Series and R8C Family of MCUs (M3T-NC30WA) Revised to V.1.06

We have revised the device file updater specific to the C compiler package for the M16C series and R8C family (M3T-NC30WA) from V.1.05 to V.1.06. For an overview of device file updaters, see:

<https://www.renesas.com/dfu>

The above URL is one of our global sites.

1. Supporting C Compiler and Its Versions

The C compiler package for the M16C series and R8C family (M3T-NC30WA) V.5.42 Release 00 and later

Note, however, that to install Device File Updater V.1.06 in your PC, you must install the V.5.45 Release 00 or later product of the above compiler package in advance.

2. Descriptions of Revision

The following CPUs have been added to the CPU Type list:

- M16C/5L, M16C/56, M16C/5M, and M16C/57 (M16C/50 series)

And the sfr files of the following CPUs have been updated:

- M16C/56D and M16C/5LD (M16C/50 series)

The following SFR files have been updated:

- The `_TB2SC` register name modifier has been deleted from the identifier of bits `PWCON`, `IVPCR1`, `TB0EN`, `TB1EN`, and `TB2SEL` (b0 to b4 in the `TB2SC` register).
- The `_ADTRGCON` register name modifier has been deleted from the identifier of bits `HDTRG0` and `HDTRG1` (b2 and b3 in the `ADTRGCON` register).

- The `_ADCON2` register name modifier has been deleted from the identifier of bits `ADGSEL0`, `ADGSEL1`, and `CKS2` (b1, b2, and b4 in the `ADCON2` register).
- The `_ADCON0` register name modifier has been deleted from the identifier of bits `CH0`, `CH1`, `CH2`, `MD0`, `MD1`, `TRG`, `ADST`, and `CKS0` (b0 to b7 in the `ADCON0` register).
- The `_ADCON1` register name modifier has been deleted from the identifier of bits `SCAN0`, `SCAN1`, `SCAN1`, and `ADSTBY` (b0, b1, b4, and b5 in the `ADCON1` register).
- The `_COCLKR` register name modifier has been added to the `CCLKS` bit (b0 in the `COCLKR` register). The new identifier is `CCLKS_COCLKR`.

3. How to Obtain the Revised Product

Download the device file updater V.1.06 specific to the C compiler package for the M16C series, and R8C family (M3T-NC30WA) from

https://www.renesas.com/dfu_download

and then install it. This Web site will be opened from October 20.

The above URL is one of our global sites.

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