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## The Beta Software of the Cycle-Accurate Simulator Debugger for the M16C/60, /30, /20, /10, /Tiny, and R8C/Tiny MCU Series Released

The beta software of the M16C R8C cycle-accurate simulator debugger has been released. The distribution of this beta software aims to be subjected to users' evaluation in the accuracy of cycle measurement, ease of operation, and so on so that the specifications of the commercial product can be refined.

We will much appreciate your advice sent by e-mail to us at <http://www.renesas.com/inquiry>

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### 1. Outline

The M16C R8C cycle-accurate simulator debugger is a simulator debugger in which the accuracy of cycle count measurement has much been improved than in the existing M16C R8C simulator debugger.

### 2. Extended Capabilities

The software enables you to measure cycle counts in consideration of bus width, instruction-queue buffers, and the number of wait states.

The extended capabilities newly introduced are as follows:

- (1) To make settings of bus width and the number of access cycles to memory.
- (2) To simulate operations of instruction-queue buffers and busses.
- (3) To improve the accuracy of cycle counts in Bus mode in the Trace window.

### 3. How to Get and Install the Beta Software

Updated on May 20, 2009:

We have terminated the provision of the beta software.

We have released the M16C R8C Simulator Debugger V.1.04 Release 00 which the precision of cycle-count measurement has been improved.

For the detail of this product and how to get, see our web page at

<http://tool-support.renesas.com/eng/toolnews/090516/tn5.htm>  
(available on and after May 20, 2009 )

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