

RENESAS TOOL NEWS [July 1, 2003: RSO-M34519T2-CPE-030701D]

Notes on Using Compact Emulator M34519T2-CPE

Please take note of the following problems in using the M34519T2-CPE compact emulator for the 4518, 4519, 4583, and 4584 groups in the 4500 series MCUs:

- On the states of pull-up transistors after executing the POF instruction
- On the port output structure after executing the POF instruction
- On an error at communicating with the M3T-PD45M debugger

1. Products Concerned

If the version of the M34500A1.C0 firmware file in the M3T-PD45M included with an M34519T2-CPE product is any one of those 1.00.00 through 1.00.04, the product is concerned with this problem.

The version of the firmware file can be obtained in the following steps:

- (1) Launch the M3T-PD45M.
- (2) Select the Help -> About menu.
- (3) Obtain the version of the firmware in the Version dialog box.

2. **Descriptions**

- 2.1 Problem on the States of Pull-Up Transistors after Executing the POF Instruction When the POF instruction is executed using such a program-executing command as RUN or STEP, all the pull-up transistors of ports 0 and 1 go to the OFF state regardless of the setting of pull-up control registers PU0 and PU1. However, the contents of the PU0 and PU1 registers are kept as before executing the POF instruction.

 Note that in the actual MCU, the states of the pull-up transistors before executing the POF instruction are also kept as well as the contents of the PU0 and PU1 registers.
- 2.2 Problem on the Port Output Structure after Executing the POF Instruction
 When the POF instruction is executed using such a program-executing command as
 RUN or STEP, the output types of ports 0 and 1 are fixed to N-channel open drain

regardless of the setting of port output structure control register FR0 and FR1. However, the contents of the FR0 and FR1 registers are kept as before executing the POF instruction.

Note that in the actual MCU, the output types of the ports before executing the POF instruction are also kept as well as the contents of the FRO and FR1 registers.

2.3 Problem on an Error at Communicating with the M3T-PD45M Debugger
Depending on the combination of the host PC and OS, the M34519T2-CPE cannot
properly communicate with the M3T-PD45M debugger, causing the following error
messages to appear in the debugger:

ERROR1429 "INTERNAL ERROR: Parameter is illegal." ERROR1483 "Check sum error"

3. Schedule of Fixing the Problems

We have fixed all the above problems in the latest Version 1.00.05 of the M34500A1.C0 firmware file and released the M3T-PD45M V.1.10 Release 1 debugger that includes this problem-fixed firmware file.

For details, see RENESAS TOOL NEWS "Debugger M3T-PD45M Upgraded to V.1.10 Release 1" issued on July 1, 2003.

So please download the latest debugger from this Web site.

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