

Notes on Using In-Circuit Emulator QB-RL78G13 (IECUBE for RL78/G12 and /G13 Groups of MCUs)

When you use the QB-RL78G13 in-circuit emulator (the IECUBE for the RL78/G12 and /G13 groups of MCUs), the following restrictions are imposed:

- On supplying the negative reference voltage to the A/D converter (No. 1)
- On setting the I/O types of the P60/SCLA0 pin (No. 2)

Here, No. X at each end of the above items is a consecutive number for indexing the restrictions in QB-RL78G13. Consecutive numbers used in tool news items are the same as restriction numbers in the release note of the QB-RL78G13.

1. Product Concerned

QB-RL78G13 with control code B

To check to see your product's control code, follow these steps:

- (1) Start the self-diagnostic tool of the IECUBE to display the IEQBUTL dialog box.
- (2) Click START in the SELF-TEST tab of the dialog box.
- (3) In IECUBE Information of the SELF-TEST tab, look for the letter before "F/W" in such an expression as is shown below.

Example: IECUBE RL78.78K0R: **** B F/W: **. **

In this example, the control code is B.

2. Restrictions

2.1 On Supplying the Negative Reference Voltage to the A/D Converter (No. 1)

Do not supply the negative reference voltage through the AVREFM pin to the A/D converter. Otherwise, the problem described below arises.

Problem:

The AVREFM pin is pulled up to the potential of the AVREFP pin through a 600-ohm resistor.

So, be sure to supply the negative reference voltage through the VSS pin.

Schedule of raising the restriction:

To raise Restriction No, 1, we are upgrading your emulator. We will receive your application for upgrade from November 22, 2012.

If the date is fixed, we inform you of it on RENESAS TOOL NEWS.

NOTICE: The above date is subject to change without notice.

For faster action, contact your local Renesas Electronics marketing office or distributor.

2.2 On Setting the I/O types of the P60/SCLA0 Pin (No. 2)

When emulating the R5F100PL, R5F101PL, R5F100PK, or R5F101PK, do not set the I/O types of the P60/SCLA0 and P61/SCLA0 pins in the different ones each other.

If the I/O types of both pins are different, the P60/SCLA0 pin functions incorrectly. So be sure to set the P60/SCLA0 pin, if used, in the same I/O type as the P61/SDAA0 pin.

Incorrect example:

When the P60/SCLA0 pin is used in the Input type, the P61/SDAA0 pin is set to the Output type.

Raising restriction:

In CubeSuite+ V1.03.00, Restriction No. 2 has already been raised. so use this. For details of CubeSuite+ V1.03.00 and how to obtain it, see RENESAS TOOL NEWS Document No. 121101/tn2. This news can be seen on the Web page at

<https://www.renesas.com/search/keyword-search.html#genre=document&q=121101tn2>

This page will be opened on this November 6.

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