To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

RENESAS

RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU		Document No.	TN-SH7-A682A/E	Rev.	1.00
Title	Usage notice on the ADST bit of the A/D Converter		Information Category	Technical Notification		
Applicable Product	SH7720 Group SH7721 Group	Lot No. All	Reference Document	SH7720/SH7721 hardware manual Rev.3.00 (REJ09B0033-0300)		

There are usage notice on the ADCSR.ADST bit of the A/D converter.

(1) When the ADCSR.ADST bit is cleared to stop the A/D conversion in the scan mode or the multi mode,

   please restart the next A/D conversion after waiting of necessary time for the A/D conversion of one channel,

   or initialize the ADC and clear the ADF bit by placing the ADC in the module standby state.

   And restart A/D conversion after releasing the ADC from the module standby state.

(2) The A/D conversion cannot be stopped by clearing the ADCSR.ADST bit at a single mode on the way.

   When the A/D conversion ends, the ADCSR.ADST bit is cleared automatically.

(3) Except for above-mentioned (1) or (2), it is necessary to wait longer than one cycle of the clock selected

   in the ADCSR.CKS1-0 bit, to change the ADCSR.ADST bit, or initialize the ADC and clear the ADF bit

   by placing the ADC in the module standby state.

   And restart A/D conversion after releasing the ADC from the module standby state.

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