RENESAS TECHNICAL UPDATE

Product Category	MPU/MCU		Document No.	TN-SH7-A843A/E	Rev.	1.00
Title	Usage notes about 16-Bit Timer Pulse Unit (TPU)		Information Category	Technical Notification		
Applicable Product	SH7705 Group SH7720 Group SH7721 Group SH7730 Group	Lot No.	Reference Document	SH7705 Hardware Manual Rev.2.00 (REJ09B0082-0200) SH7720, SH7721 Hardware Manual Rev.4.00 (R01UH0083EJ0400) SH7730 Hardware Manual Rev.3.00 (REJ09B0359-0300)		
		All				

We would like to inform you of the following usage notes about the 16-Bit Timer Pulse Unit (TPU).

Buffer operation can be used only when the value of time prescaler bits (TPSC2-0) of a timer control register is B'000.

If the value of a time prescaler bits (TPSC2-0) of a timer control register is except B'000, the output waveform of buffer operation may not become correct.

In addition, when not using buffer operation, there is no restriction of time prescaler bits (TPSC2-0).