To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MAEC TECHNICAL NEWS　　No. M32R-05-0211

M32R/ECU series

　Note for Wait Function during Internal Flash Programming and Erase Operation

Classification	Concerned Products
Corrections and supplementary explanation of document √ Notes Knowhow Others	M32R/ECU Series

The following describes a note about the M32R/ECU series of microcomputers. If a low-level signal happens to be applied to the WAIT# input pin of the microcomputer when entering flash E/W mode, the wait function is enabled.

To avoid this problem, choose P71 for the P71/WAIT# dual pin functions using the P7 Operation Mode Register (P7MOD) to disable the wait function, or add a circuit external to the chip to disable the wait function when accessing the internal flash area.

If WAIT# is chosen for the P71/WAIT# dual pin functions using the P7 Operation Mode Register (P7MOD) and the signal level applied to the WAIT# pin from the outside happens to be low, the wait state is entered when accessing the flash area and the microcomputer thereby stops. Similarly, if WAIT# is chosen for the P71/WAIT# dual pin functions using the P7 Operation Mode Register (P7MOD) while port input is disabled , the WAIT# pin internally is in the same state as a low signal is applied to it, so that the wait state is entered.

Note: • The suffix "#" in the pin or signal name WAIT# means that the pin or signal is active low.