

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A026A/E	Rev.	1.00
Title	RZ/G Series User's Manual:Hardware Correction (Area Map)		Information Category	Technical Notification		
Applicable Product	RZ/G Series RZ/G1H, M, N, and E	Lot No.	Reference Document	RZ/G Series User's Manual: Hardware Rev.1.00 (R01UH0543EJ0100)		
		All lots				

This technical update describes document correction for RZ/G Series.

[Summary]

RZ/G Series User's Manual: Hardware Correction (manual errata)
Correct incorrect address notation.

[Products]

RZ/G1H, M, N, and E

[Note]

There is no specification change (document correction only).

[Correction]

(Cancellation line parts (abcd) are deleted.)

1. Section 2. Area Map

Table 2.1 Address Space (Over 4-Gbyte Space)

Current (from):

Address	Space	Description	RZ/G Series Products			
			RZ/G1H	RZ/G1M	RZ/G1N	RZ/G1E
H'00_00000000 to H'00_FFFFFFFF	Legacy 4-Gbyte space	LBSC, DBSC3, etc.	√	√	√	√
H'01_00000000 to H'01_FFFFFFFF	DDR0/DBSC3 (DDR0)	DBSC3	√	√	√	√
H'02_00000000 to H'02_FFFFFFFF	DDR1*	DBSC3 [H, M]	√	√	—	—
	Reserved	—	—	—	√	√
H'03_00000000 to H'03_FFFFFFFF (H'03_400000000 to H'03_BFFFFFFF)	DBSC3 (DDR0) shadow	DBSC3 (up to 2 Gbytes)	√	√	√	√
H'04_00000000 to H'04_FFFFFFFF	DDR0 shadow	DBSC3 [H, M, N, E]	√	√	√	√
...

Correction (to):

Address	Space	Description	RZ/G Series Products			
			RZ/G1H	RZ/G1M	RZ/G1N	RZ/G1E
H'00_00000000 to H'00_FFFFFFFF	Legacy 4-Gbyte space	LBSC, DBSC3, etc.	√	√	√	√
H'01_00000000 to H'01_FFFFFFFF	DDR0/DBSC3 (DDR0)	DBSC3	√	√	√	√
H'02_00000000 to H'02_FFFFFFFF	DDR1*	DBSC3 [H, M]	√	√	—	—
	Reserved	—	—	—	√	√
H'03_00000000 to H'03_FFFFFFFF (H'03_400000000 to H'03_BFFFFFFF)	DBSC3 (DDR0) shadow	DBSC3 (up to 2 Gbytes)	√	√	√	√
H'04_00000000 to H'04_FFFFFFFF	DDR0 shadow	DBSC3 [H, M, N, E]	√	√	√	√
...

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