

# RENESAS TECHNICAL UPDATE

TOYOSU FORESIA, 3-2-24, Toyosu, Koto-ku, Tokyo 135-0061, Japan  
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RX*-A156A/E	Rev.	1.00
Title	RX64M Group Modified interrupt status register 0 (INTSTS0) in USB 2.0 FS Host/Function Module (USBb).		Information Category	Technical Notification		
Applicable Product	RX64M Group	Lot No.	Reference Document	RX64M Group User's Manual: Hardware (R01UH0377EJ100)		
		All				

This document describes a modification in section 38.2.13 for note.5 of the interrupt status register 0 (INSTS0).

#### 【Before】

Note 5. The status of the RESM, DVST, and CTRT bits are changed only when the **host controller** is selected. Set the corresponding interrupt enable bits to 0 (disabled) when the **function controller** is selected.

#### 【After】

Note 5. The status of the RESM, DVST, and CTRT flags are changed only when the **function controller** is selected. Set the corresponding interrupt enable bits to 0 (disabled) when the **host controller** is selected.

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