

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RA*-A0028A/E	Rev.	1.00
Title	RA4M2 Group, RA4M3 Group, RA6M4 Group, RA6M5 Group, correction of SRAM Parity Error Operation After Detection Register (PARIOAD)		Information Category	Technical Notification		
Applicable Product	RA4M2 Group RA4M3 Group RA6M4 Group RA6M5 Group	Lot No.	Reference Document	RA4M2 Group User's Manual Hardware Rev.1.10 RA4M3 Group User's Manual Hardware Rev.1.20 RA6M4 Group User's Manual Hardware Rev.1.10 RA6M5 Group User's Manual Hardware Rev.1.10		
		All				

The description of SRAM Parity Error Operation After Detection Register (PARIOAD) is corrected.

[before]

PARIOAD : SRAM Parity Error Operation After Detection Register

Bit	Symbol	Function	R/W
0	OAD	Operation After Detection 0: Reset 1: Non-maskable interrupt	R/W
7:1	—	These bits are read as 0. The write value should be 0.	R/W

[after]

PARIOAD : SRAM Parity Error Operation After Detection Register

Bit	Symbol	Function	R/W
0	OAD	Operation After Detection 0: Non-maskable interrupt 1: Reset	R/W
7:1	—	These bits are read as 0. The write value should be 0.	R/W