RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-R8C-A024A/E	Rev.	1.00
Title	R8C/3x Series Descriptions Complemented in the User's Manual: Hardware		Information Category	Technical Notification		
Applicable Products	R8C/3x Series	Lot No. __	Reference Document			

Descriptions in the User's Manual: Hardware have been complemented for the above applicable products.

The functions described below do not exist in some MCUs. Refer to the applicable User's Manual: Hardware for details.

1. CPU clock when exiting stop mode

   The clock divided by 8 specified by bits CM36 and CM37 in the CM3 register is used as the CPU clock when the MCU exits stop mode by a peripheral function interrupt. For more details, refer to the CM3 and CM0 register diagrams in the User's Manual: Hardware.

2. Pin states in stop mode

   The following shows pin states in stop mode.

Pin	Stop Mode
I/O port	Retain the status immediately before entering stop mode
When selecting XIN, XOUT functions (CM13 = 1)	High impedance
When selecting XCIN, XCOUT functions (CM04 = 1)	Comply with the setting of the port direction register and port register