

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

RENESAS TECHNICAL UPDATE

Classification of Production	MPU&MCU		No	TN-SH7-488A/E	Rev	1
THEME	A notice on the A/D Converter		Classification of Information	1. Spec change 2. Supplement of Documents ③. Limitation of Use 4. Change of Mask 5. Change of Production Line		
PRODUCT NAME	HD6417705	Lot No.	Reference Documents	SH7705 Group Hardware Manual Rev.2.00 REJ09B0082-02000	term of validity	
		ALL			Eternity	

The A/D converter module has the following usage notice.

- (1) When the A/D converter is used in the scan mode or during A/D conversion in the multi mode (A/D end flag ADF=1 in the A/D control/status register ADCSR), A/D start bit ADST is cleared to stop the A/D conversion and it is set again to restart it. It needs minimum time period between the ADST clear and the ADST set. The minimum time period is the A/D conversion time of an A/D channel.
- (2) In the single mode, the ADST cannot be cleared to stop the A/D conversion. When the A/D conversion ends, the ADST is cleared automatically.
- (3) When the ADST is modified (set or clear), it needs minimum time period between two consecutive modifications. The minimum time period is a clock cycle period, which is selected by clock select bits CKS1,0 in the ADCSR.