To our customers,

## Old Company Name in Catalogs and Other Documents

On April $1^{\text {st }}, 2010$, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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## RENESAS TECHNICAL UPDATE

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| Title | Notice about the flag bit of DMAC |  | Information Category | Technical Notification |  |  |
| Applicable Product | SH7280 Group <br> SH7211 Group <br> SH7080 Group <br> SH7243 Group | Lot No. ALL | Reference Document | SH7280 Group Hardw <br> Rev.1.00 (REJ09B0 SH7211 Group Hardw Rev. 2.00 (REJ09B0 SH7080 Group Hardw Rev.3.00 (REJ09B0 | Manual <br> -0100) <br> Manual <br> -0200) <br> Manual <br> 1-0300) |  |

## [Notice]

About the NMIF bit (NMIF Flag) of DMA Operation Register (DMAOR) in DMAC,
just when a flag is set to 1 , if the flag is read, the read data will be 0 , but the internal state will be the same as reading 1 .

In that case, if the flag is written 0 , the flag will be cleared as 0 , because the internal state is the same as reading 1 .

## [Workaround]

In the case of using a flag of DMAC, to protect unintended bit clear to 0 , please write it as following.

1) In the case of intended bit clear, please write 0 after reading 1 to the flag.
2) In the other cases, please write 1 to the flag.

If the flag is not used, it is no problem to write 0 to flag (in the case of intended bit clear, write 0 after reading 1 to the flag).

