

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

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Renesas Electronics Corporation

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Send any inquiries to <http://www.renesas.com/inquiry>.

RENESAS TECHNICAL UPDATE

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Product Category	MPU&MCU		Document No.	TN-SH7-A731A/E	Rev.	1.00
Title	Notes about disabling the Video receiver block operation of Video Display Controller 3		Information Category	Technical Notification		
Applicable Product	SH7264 Series SH7262 Series	Lot No.	Reference Document	SH7262 Group, SH7264 Group Hardware Manual Rev.2.00 (RJJ09B0445-0200)		
		ALL				

We would like to inform you of the notice about disabling the Video receiver block operation of Video Display Controller 3 included in the above-mentioned applicable product.

[Notice]

When disabling the Video receiving block operation, following the procedure given below.

- 1) Unselect the DV_CLK function of PF8 Mode in the Control Register (PFCR2) of General Purpose I/O Ports. (select the PF8 function)
- 2) Clear the VIDEO_MAIN_EXE bit in VIDEO_MODE register to 0.
- 3) Halt the clock to Video Display Controller 3 by the Standby Control Register (STBCR7) of Power-Down Modes.
- 4) Select the DV_CLK function of PF8 Mode in the Control Register (PFCR2) of General Purpose I/O Ports.
- 5) Supply the clock to Video Display Controller 3 by the Standby Control Register (STBCR7) of Power-Down Modes.