

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-SH7-A870A/E	Rev.	1.00
Title	Manual correction about the SC Port Control Register(SCPCR) of Pin Function Controller (PFC) of SH7727		Information Category	Technical Notification	
Applicable Product	SH7727 Group	Lot No.	Reference Document	SH7727 Group Hardware Manual (REJ09B0254-0600 Rev.6.00)	
		All			

It corrects about the SCP2 Mode 1, 0 (SCP2MD1, SCP2MD0) of SC Port Control Register (SCPCR) of the Pin Function Controller (PFC) of the SH7730 hardware manual.

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Bits 5, 4—SCP2 Mode 1, 0 (SCP2MD1, SCP2MD0): These bits select the pin functions and the input pullup MOS control.

Bit 5	Bit 4	Pin Function
SCP2MD1	SCP2MD0	
0	0	Transmit data output 1 (TxD_SIO) Receive data input 1 (RxD_SIO) (Initial value)
0	1	General output (SCPT[2] output pin) Receive data input 1 (RxD_SIO)
1	0	SCPT[2] input pin pullup (input pin) Transmit data output 1 (TxD_SIO)
1	1	General input (SCPT[2] input pin) Transmit data output 1 (TxD_SIO)

Note: There is no combination of simultaneous I/O of SCPT[2] because one bit (SCP2DT) is accessed using two pins of TxD_SIO and RxD_SIO.

When the port input is set (bit SCPnMD1 is set to 1) and when the TE bit in SCSCR is set to 1, the TxD_SIO pin is in the output state. When the TE bit is cleared to 0, the TxD_SIO pin is in the high-impedance state.

[Correction]

Bits 5, 4—SCP2 Mode 1, 0 (SCP2MD1, SCP2MD0): These bits select the pin functions and the input pullup MOS control.

Bit 5	Bit 4	Pin Function
SCP2MD1	SCP2MD0	
0	0	Transmit data output 1 (TxD_SIO) Receive data input 1 (RxD_SIO) (Initial value)
0	1	General output (SCPT[2] output pin) Receive data input 1 (RxD_SIO)
1	0	SCPT[2] input pin pullup (input pin) High-impedance (TxD_SIO)
1	1	General input (SCPT[2] input pin) High-impedance (TxD_SIO)

Note: There is no combination of simultaneous I/O of SCPT[2] because one bit (SCP2DT) is accessed using two pins of TxD_SIO and RxD_SIO.

When the port input is set (bit SCPnMD1 is set to 1), the TxD_SIO pin is in the high-impedance state. The TxD_SIO pin cannot be in the output state.