To our customers,

## Old Company Name in Catalogs and Other Documents

On April $1^{\text {st }}$, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

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## RENESAS TECHNICAL UPDATE

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RenesasTechnology Corp.

| Product Category | MPU\&MCU |  | Document No. | M16C-113-0312 | Rev. | 1.00 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Title | M16C/62N Group, M3062GF8NFP/GP Precautions when Rewriting Flash Memory Using CPU Rewrite Mode |  | Information Category | Usage Limitation |  |  |
| Applicable Product | M16C/62N Group M3062GF8NFP/GP | Lot No. | Reference Document |  |  |  |
|  |  | --- |  |  |  |  |

## 1. Precautionary Note

The microcomputer automatically enters read status register mode when a program command (40h) is executed in CPU rewrite mode. If a read status register followed by program command is performed continuously, and with the address to read the status register set to the same address every time, the data in this address may change.

This problem does not occur in standard serial I/O mode.

## 2. Countermeasure

When reading the status register immediately after a program command, use the same address as the program address set in the second bus cycle of the program command.
3. Affected Products

This precaution applies to the products listed at Table 1.

Table 1. Affected Products

|  | Affected Products |
| :---: | :--- |
| M16C/62N Group | M30624FGNFP/GP/HP, M30625FGNGP, M30620FCNFP/GP, M30621FCNGP |
| - | M3062GF8NFP/GP |

