# Old Company Name in Catalogs and Other Documents

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## M16C/61, M16C/62 Group

### **Precautions for UART2**

#### 1. Related devices

M16C/61 Group, M16C/62 Group

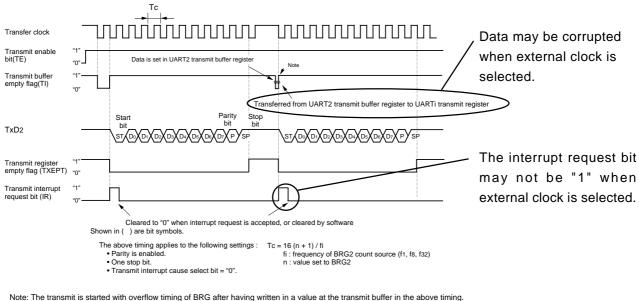
#### 2. Precautions

When using UART2 in clock asynchronous serial I/O (UART) mode choose internal clock. If UART2 in clock asynchronous mode is used with external clock, then one of the following may occur;

- 2.1 The interrupt may not be issued at the end of data transmission when the hardware transfers the data from the transmit buffer register to the transmit register.
- 2.2 Data may be corrupted when the hardware transfers the data from the transmit buffer register to the transmit register.

This precaution only applies to the UART2 asynchronous serial I/O mode and does not apply to UART0 or UART1. It does not apply to any UART when used in the synchronous clocked serial I/O mode.

Example of transmit with UART mode, Transfer data 8 bits long.



Note: The transmit is started with overflow timing of BRG after having written in a value at the transmit buffer in the above timing.