RENESAS TECHNICAL UPDATE

Product Category	MPU/MCU		Document No.	TN-SY*-A027A/E	Rev.	1.00
Title	Correction to the description for the Security MPU Program Counter Start Address Register (SECMPUPCSn)		Information Category	Technical Notification		
Applicable Product	Renesas Synergy™ S5 Series S5D5, S3 Series S3A3 and S3A6	Lot No.	Reference Document	S5D5 User's Manual: Rev.1.10, S3A3 User's Manual: Rev.1.00, S3A6 User's Manual: Rev.1.00		
		All lots				

Description for the Security MPU Program Counter Start Address Register (SECMPUPCSn) in the MPU chapter of S5D5, S3A3, and S3A6 MCU Group User's Manuals is updated.

● S5D5 and S3A3

1. Security MPU Program Counter Start Address Register (SECMPUPCSn) (n = 0, 1)

   [Before]

   A time interval of greater than 12 bytes is required between the last instruction of the non-secure program and the first instruction of the non-secure program.

   [After]

   An address space of greater than 12 bytes is required between the last instruction of a non-secure program and the first instruction of a secure program.

● S3A6

1. Security MPU Program Counter Start Address Register (SECMPUPCSn) (n = 0, 1)

   [Before]

   The secure program must set a time interval of more than 12 bytes between the last instruction of a non-secure program and the first instruction of a secure program.

   [After]

   An address space of greater than 12 bytes is required between the last instruction of a non-secure program and the first instruction of a secure program.