

RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-R8C-A034A/E	Rev.	1.00
Title	Errata to notes of the port register		Information Category	Technical Notification		
Applicable Product	R8C/3x Series	Lot No.	Reference Document			

This document describes corrections to the port Pi registers included in the following User's Manual: Hardware or hardware manual.

1. Applicable manuals and ports for corrections A

Applicable Products	Applicable Manual and Page	Applicable Ports
R8C/32A Group	REJ09B0458-0020 (Rev.0.20), Page 66 of 583	P3_0 to P3_2, P3_6, P4_3, and P4_4
R8C/32C Group	REJ09B0573-0100 (Rev.1.00), Page 66 of 573	
R8C/32M Group	R01UH0133EJ0100 (Rev.1.00), Page 66 of 592	
R8C/33A Group	REJ09B0455-0020 (Rev.0.20), Page 69 of 598	P2_3 to P2_7, P3_0, P3_2, P3_6, P4_3, and P4_4
R8C/33C Group	REJ09B0570-0100 (Rev.1.00), Page 69 of 589	
R8C/33M Group	R01UH0132EJ0100 (Rev.1.00), Page 68 of 605	
R8C/35A Group	REJ09B0407-0040 (Rev.0.40), Page 75 of 736	P5_0 to P5_2, and P5_5
R8C/35C Group	REJ09B0567-0100 (Rev.1.00), Page 75 of 725	
R8C/35M Group	R01UH0130EJ0100 (Rev.1.00), Page 75 of 744	
R8C/36A Group	REJ09B0480-0020 (Rev.0.20), Page 78 of 795	P5_5 and P8_7
R8C/36C Group	R01UH0095EJ0110 (Rev.1.10), Page 80 of 790	
R8C/36M Group	R01UH0259EJ0100 (Rev.1.00), Page 80 of 806	
R8C/3GA Group	REJ09B0472-0020 (Rev.0.20), Page 67 of 580	P0_0, P0_3 to P0_5, P3_0 to P3_2, P3_6, P4_3, and P4_4
R8C/3GC Group	R01UH0109EJ0100 (Rev.1.00), Page 69 of 577	
R8C/3GM Group	R01UH0284EJ0100 (Rev.1.00), Page 67 of 591	
R8C/3JA Group	REJ09B0508-0100 (Rev.1.00), Page 71 of 723	P0_0, P0_5, P2_6, P2_7, P3_0, P3_2, P3_6, P6_0 to P6_5, and P6_7
R8C/3JC Group	REJ09B0602-0100 (Rev.1.00), Page 71 of 715	
R8C/3JM Group	R01UH0285EJ0100 (Rev.1.00), Page 71 of 734	
R8C/33T Group	R01UH0156EJ0110 (Rev.1.10), Page 69 of 531	P2_3 to P2_7
R8C/32D Group	REJ09B0528-0100 (Rev.1.00), Page 62 of 447	P3_0 to P3_2, P3_6, P4_3, and P4_4
R8C/33D Group	REJ09B0539-0100 (Rev.1.00), Page 65 of 461	P2_3 to P2_7, P3_0, P3_2, P3_6, P4_3, and P4_4
R8C/3GD Group	REJ09B0518-0100 (Rev.1.00), Page 62 of 446	P0_0, P0_3 to P0_5, P3_0 to P3_2, P3_6, P4_3, and P4_4

Applicable Products	Applicable Manual and Page	Applicable Ports
R8C/36E Group, R8C/36F Group, R8C/36G Group, R8C/36H Group	REJ09B0448-0110 (Rev.1.10), Page 94 of 785	P5_5 and P8_7
R8C/36W Group, R8C/36X Group, R8C/36Y Group, R8C/36Z Group	R01UH0064EJ0100 (Rev.1.00), Page 94 of 788	
R8C/32G Group, R8C/32H Group	R01UH0139EJ0100 (Rev.1.00), Page 70 of 642	P3_0, P3_1, P3_6, P4_3, and P4_4

2. Corrections A (Example of the R8C/33C group)

7.4.2 Port Pi Register (Pi) (i = 0 to 4)

Address 00E0h(P0), 00E1h(P1), 00E4h(P2 ⁽¹⁾), 00E5h(P3 ⁽²⁾), 00E8h(P4 ⁽³⁾)

Bit	b7	b6	b5	b4	b3	b2	b1	b0
Symbol	Pi_7	Pi_6	Pi_5	Pi_4	Pi_3	Pi_2	Pi_1	Pi_0
After Reset	X	X	X	X	X	X	X	X

Bit	Symbol	Bit Name	Function	R/W
b0	Pi_0	Port Pi_0 bit	0: "L" level 1: "H" level	R/W
b1	Pi_1	Port Pi_1 bit		R/W
b2	Pi_2	Port Pi_2 bit		R/W
b3	Pi_3	Port Pi_3 bit		R/W
b4	Pi_4	Port Pi_4 bit		R/W
b5	Pi_5	Port Pi_5 bit		R/W
b6	Pi_6	Port Pi_6 bit		R/W
b7	Pi_7	Port Pi_7 bit		R/W

Notes:

1. Bits P2_3 to P2_7 in the P2 register are reserved bits. If it is necessary to set bits P2_3 to P2_7, set to 0. When read, the content is 0.
2. Bits P3_0, P3_2, and P3_6 in the P3 register are reserved bits. If it is necessary to set bits P3_0, P3_2 and P3_6, set to 0. When read, the content is 0.
3. Bits P4_0 and P4_1 in the P4 register are unavailable on this MCU. If it is necessary to set bits P4_0 and P4_1 set to 0. When read, the content is 0. Bits P4_3 and P4_4 are reserved bits. If it is necessary to set bits P4_3 and P4_4, set to 0. When read, the content is 0.

When read, the content is undefined.

3. Applicable manuals and ports for corrections B

Applicable Products	Applicable Manual and Page	Applicable Ports
R8C/34P Group, R8C/34R Group	R01UH0257EJ0100 (Rev.1.00), Page 75 of 656	P3_6

4. Corrections B

7.4.2 Port Pi Register (Pi) (i = 0 to 4, 6)

Address 00E0h(P0), 00E1h(P1), 00E4h(P2), 00E5h(P3 ⁽¹⁾), 00E8h(P4 ⁽¹⁾), 00ECh(P6)

Bit	b7	b6	b5	b4	b3	b2	b1	b0
Symbol	Pi_7	Pi_6	Pi_5	Pi_4	Pi_3	Pi_2	Pi_1	Pi_0
After Reset	X	X	X	X	X	X	X	X

Bit	Symbol	Bit Name	Function	R/W
b0	Pi_0	Port Pi_0 bit	0: "L" level 1: "H" level	R/W
b1	Pi_1	Port Pi_1 bit		R/W
b2	Pi_2	Port Pi_2 bit		R/W
b3	Pi_3	Port Pi_3 bit		R/W
b4	Pi_4	Port Pi_4 bit		R/W
b5	Pi_5	Port Pi_5 bit		R/W
b6	Pi_6	Port Pi_6 bit		R/W
b7	Pi_7	Port Pi_7 bit		R/W

Note:

- Nothing is assigned to the P3_2 bit in the P3 register, and bits P4_0 and P4_1 in the P4 register. If necessary, set these bits to 0. When read, the content is 0.
The P3_6 bit in the P3 register is reserved. If necessary, set the P3_0 bit to 0. When read, the content is undefined.

