

Peripheral Driver Generator V.2.09

Release Note

R20UT3012EJ0100 Rev.1.00 May 16, 2014

1. Description of Revision

Refer to the RENESAS TOOL NEWS Document No.140516/tn3 at: http://tool-support.renesas.com/eng/toolnews/140516/tn3.htm

2. Precautions

Some known problems reside in Peripheral Driver Generator V.2.09. You can refer to the details of them that have not been fixed yet on the following Web page.

http://tool-support.renesas.com/eng/toolnews/p_tool_3.htm

The following problems also reside in Peripheral Driver Generator V.2.09.

2.1 A Note on Setting I/O Port (RX631)

(1) Description

This note concerns the setting of PC0 and PC1 in 64 pin package and the setting of PC0, PC1, PC2 and PC3 in 48 pin package of RX631.

When condition described in (2) is satisfied after calling R_PG_IO_PORT_Set_PC or R_PG_IO_PORT_Set_<*PCn>* to set up Port C, all settings in port switching register (PSRA or PSRB) are

changed to Port B.

<PCn>: I/O port concerned (PC0, PC1, PC2, or PC3)

(2) Conditions

This problem arises if either of the following conditions is satisfied:

- When calling R_PG_IO_PORT_Set_PB or R_PG_IO_PORT_Set_<*PBm>* to set up one or more ports which are multiplexed with port C.
 - <*PBm>*: I/O port in port B which is multiplexed with port C
- When calling R_PG_IO_PORT_Set_<*PCn>*. In this case, problem arises in concerned I/O ports except an I/O port configured by this function call.

(3) Workaround

- Port B must be set up before setting up Port C.
- Use R_PG_IO_PORT_Set_PC instead of R_PG_IO_PORT_Set_<*PCn>* to set up port C.

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