

Report No. MCR-22-0505-A September 27,2022

RENESAS SEMICONDUCTOR RELIABILITY REPORT

- GROUP : RL78/G11
- DEVICE : R5F105XXX
- APPLICATION : Consumer / Industry

Quality Assurance Div. Renesas Electronics Corporation



MCR-22-0505-A

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Table. Reliability test results (QFN)

Test Items	Reference	Test Conditions	Results Failure/Size	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	Ta=125 ℃, Vccmax, 1000 hrs	0/22	
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 ℃, 1000 hrs	0/22	
Temperature Humidity bias (THB) (*1)	JESD22-A101	Ta=85 ℃, RH=85 %, Vccmax, 1000 hrs	0/22	
Temperature Cycling (TC) (*1)	JESD22-A104	a=-65 °C to 150 °C , 300 cycles 0/22		
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3	
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time	0/3	Class: 2
Electrostatic discharge (ESD-CDM)	JESD22-C101	+/-500V,1time 0/3		Class: C2
Solderability (SD)	J-STD-002	245 °C, 5 s, Solder coverage ≥95 % 0/5		
Resistance to Soldering Heat (PC)	JESD22-A113, J-STD-020	MSL3(Moisture Sensitivity Level 3)	0/22	

*1) With preconditioning per JESD22-A113, MSL 3 •It is tested to confirm that all the samples are satisfied with an individual product specification.

Note :

Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .



Table. Reliability test results (SOP)

Test Items	Reference	Test Conditions	Results Failure/Size	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	Ta=125 ℃, Vccmax, 1000 hrs	0/22	
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 ℃, 1000 hrs	0/22	
Temperature Humidity bias (THB) (*1)	JESD22-A101	Ta=85 ℃, RH=85 %, Vccmax, 1000 hrs	0/22	
Temperature Cycling (TC) (*1)	JESD22-A104	Ta=-65 ℃ to 150 ℃ , 300 cycles	0/22	
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3	
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time		Class: 2
Electrostatic discharge (ESD-CDM)	JESD22-C101	+/-500V,1time	0/3	Class: C2
Solderability (SD)	J-STD-002	245 ℃, 5 s, Solder coverage ≥95 % 0/5		
Resistance to Soldering Heat (PC)	JESD22-A113, J-STD-020	MSL3(Moisture Sensitivity Level 3)	0/22	

*1) With preconditioning per JESD22-A113, MSL 3 •It is tested to confirm that all the samples are satisfied with an individual product specification.

Note :

Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .



Table. Reliability test results (LGA)

Test Items Refere		Test Conditions	Results Failure/Size	Comment
High Temperature Operating Life (HTOL)	JESD22-A108	Ta=125 ℃, Vccmax, 1000 hrs	0/22	
High Temperature Storage Life (HTSL)	JESD22-A103	Ta=150 ℃, 1000 hrs	0/22	
Temperature Humidity bias (THB) (*1)	JESD22-A101	Ta=85 ℃, RH=85 %, Vccmax, 1000 hrs	0/22	
Temperature Cycling (TC) (*1)	JESD22-A104	Ta=-55 ℃ to 125 ℃ , 500 cycles	0/22	
Latch-Up (LU)	JESD78	Pulse Current Injection, I=+/-150 mA	0/3	
Electrostatic discharge (ESD-HBM)	JS-001	1.5 kΩ, 100 pF, +/-2000 V, 1 time	0/3	Class: 2
Electrostatic discharge (ESD-CDM)	JESD22-C101	+/-500V,1time	0/3	Class: C2
Resistance to Soldering Heat (PC)	JESD22-A113, J-STD-020	MSL3(Moisture Sensitivity Level 3)	0/22	

*1) With preconditioning per JESD22-A113, MSL 3 •It is tested to confirm that all the samples are satisfied with an individual product specification.

Note :

Basically qualification tests were performed using a representative product with the same wafer process and the same package structure .



The failure rate of the device in an actual use condition can be estimated by the below procedure.

• Equation for the failure rate estimation (λ)

 $\lambda = \lambda b \times \pi T$ (FIT)

(1) Unique failure rate (λb)

λb= 3.8 FIT

Unique failure rate at Ta=55 $^{\circ}$ C using 60 $^{\circ}$ confidence level.

②Temperature term (π T)

 π T=exp{11600×Ea×(1/(273+55)-1/(273+Ta))}

Ea: Activation energy (eV)

Ta : Ambient temperature ($^{\circ}$ C)

π	π T simplified chart as Ea=0.7 eV												
	Ta (℃)	40	50	55	60	65	70	75	80	85	90	100	110
;	πΤ	0.31	0.68	1	1.45	2.08	2.95	4.15	5.77	7.96	10.88	19.82	34.99

•MTTF (Mean Time To Failure)

 $MTTF = 1/\lambda$



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Reference about Renesas package code

Package type		Package code *1
Lead type plastic package	QFP	PxQP
	SOP	PxSP
Non-lead type plastic package	QFN	PxQN
Grid array type plastic package	BGA	PxBG
	LGA	PxLG

*1. First four digit

Table. Product list

			<u> </u>				1
No	Group	Product part number	Package code	No	Group	Product part number	Package code
1	RL78/G11	R5F1051AASP	PLSP0010J*	51			
2	RL78/G11	R5F1051AGSP	PLSP0010J*	52			
3	RL78/G11	R5F1054AANA	PWQN0016K*	53			
4	RL78/G11	R5F1054AASP	PRSP0016J*	54		_	
5	RL78/G11	R5F1054AGNA	PWQN0016K*	55		_	
6	RL78/G11	R5F1054AGSP	PRSP0016J*	56			
7	RL78/G11	R5F1056AASM	PTSP0020J*	57			
8	RL78/G11	R5F1056AASP	PLSP0020J*	58			
9	RL78/G11	R5F1056AGSM	PTSP0020J*	59			
10	RL78/G11	R5F1056AGSP	PLSP0020J*	60			
11	RL78/G11	R5F1057AANA	PWQN0024K*	61			
12	RL78/G11	R5F1057AGNA	PWQN0024K*	62			
13	RL78/G11	R5F1058AALA	PWLG0025K*	63			
14	RL78/G11	R5F1058AGLA	PWLG0025K*	64			
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46				96	T		1
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48		l		98			1
49		l		99			1
50				100			