

PRODUCT/PROCESS CHANGE NOTICE (PCN)						
PCN #: SM-0202-02	Date: February 25, 2002	MEANS OF DISTIN	NGUISHING CHANC	GED DEVICES:		
Product Affected: 7132, 7133 ar	nd 7134 Families	☐ Product Mark				
	Salinas, California	☐ Back Mark				
Manufacturing Location Affected:	■ Date Code Top mark will have "4" following the  Other die revision. Refer to page 1 of attachment					
Date Effective: May 25, 2002		Other die r	evision. Refer to page	e i oi attachment		
Contact: George Snell		A 1	<b>-</b> v			
Title: Quality Assurance Mana Phone #: (831) 754-4556	ger	Attachment::	Yes	∐ No		
Fax #: (831) 754-4672		Samples:	Available upon rec	mest		
E-mail: george.snell@idt.com		Samples.	Available apon rec	juest.		
DESCRIPTION AND PURPOSE	OF CHANGE:					
<ul> <li>□ Die Technology</li> <li>□ Wafer Fabrication Process</li> <li>□ Assembly Process</li> <li>□ Equipment</li> <li>□ Material</li> <li>□ Testing</li> <li>■ Manufacturing Site</li> <li>□ Data Sheet</li> <li>□ Other</li> </ul>	Hillsboro, Ore IDT's Wafer F	egon (Fab 4) these que Fab facility in Hillsbo on Cmos 7 (.64µm) to	on from Salinas, Calif alified products will b ro, Oregon. These dev Cmos 8 (.60µm). Cm	e transferred to vices will be		
RELIABILITY/QUALIFICATIO	ON SUMMARY:					
Qualification testing will verify that	t there is no change to the prod	luct reliability. Qualit	fication details are ava	ailable upon		
request						
CUSTOMER ACKNOWLEDGM	MENT OF RECEIPT:					
IDT records indicate that you require to grant approval or request addition it will be assumed that this change IDT reserves the right to ship either on the earlier version has been dep	onal information. If IDT does r is acceptable. r version manufactured after th	not receive acknowled	lgement within 30 day	ys of this notice		
Customer:		Approval for	shipments prior to	effective date.		
Name/Date:	E-1	Mail Address:				
Title:	Pho	one#/Fax#:				
CUSTOMER COMMENTS:						
IDT ACKNOWLEDGMENT OF	RECEIPT:					
RECD RV		DATE:				
RECD. BY:	_	DAIL.		_		

# PRODUCT/PROCESS CHANGE NOTICE (PCN)

### ATTACHMENT - PCN #: SM-0202-02

**PCN Type:** Fab Site Change

Data Sheet Change None Expected

#### **Detail of Change**

Transfer existing qualified products from Salinas, California Wafer Fab Facility (Fab 2) to Hillsboro, Oregon Wafer Fab Facility (Fab 4).

	Current Wafer Fab			Transfer Wafer Fab				
	Manufacturing	Technology	Wafer	Die	Manufacturing	Technology	Wafer	Die
Part Name	Site		Size	Revision	Site		Size	Revision
7132S	Salinas, CA	Cmos 7	6 inch	S	Hillsboro, OR	Cmos 8	8 inch	S4
7132SF	Salinas, CA	Cmos 7	6 inch	SF	Hillsboro, OR	Cmos 8	8 inch	SF4
7133W	Salinas, CA	Cmos 7	6 inch	W	Hillsboro, OR	Cmos 8	8 inch	W4
7134W	Salinas, CA	Cmos 7	6 inch	W	Hillsboro, OR	Cmos 8	8 inch	W4

#### **Conversion schedule (Estimated)**

<b>Base Device</b>	Production Shipments and Sample availability			
7132S4	5/25/02			
7132SF4	5/25/02			
7133W4	6/30/02			
7134W4	5/25/02			

### PRODUCT/PROCESS CHANGE NOTICE (PCN)

#### ATTACHMENT - PCN #: SM-0202-02

Qualification Plan: QSM-0201-02

#### **Expected Completion Date**

Test Vehicle	4/1/2002	
7132\$	Required Sample / # Fails	LOT #1
Operating Life Test: Dynamic @+135°C, Vcc=4V for 750 hours	116/0	
High Temp. Storage Life Test (Unbiased, 1000 hours @+150°C)	77 / 0	
Bake & Ballshear Test @ 200°C / 4 ball bonds per device	5/0	
Temperature Cycling: ( -65°C to +150°C, 500 cycles)	45 / 0	
HAST: (Biased, 100 Hrs. @+130°C, +85%RH, 3 Atm.)	45 / 0	
Autoclave:(Unbiased, 2 Atm Saturated Steam, +121°C, 168 Hrs)	45 / 0	
ESD Human Body Model	9/0	
ESD Charged Device Model	6/0	
Latch up: ( Tested to 1.5X Vcc)	10/0	

Product release is based on qualification of initial lot.

Qualification Plan: QSM-0201-03

#### **Expected Completion Date**

**Test Vehicle** 4/1/2002 Required 7133W Sample / LOT#1 # Fails Operating Life Test: Dynamic 116/0 @+135°C, Vcc=4V for 750 hours High Temp. Storage Life Test 77 / 0 (Unbiased, 1000 hours @+150°C) Bake & Ballshear Test @ 200°C / 5/0 4 ball bonds per device Temperature Cycling: (-65°C to 45 / 0 +150°C, 500 cycles) HAST: (Biased, 100 Hrs. 45/0 @+130°C, +85%RH, 3 Atm.) Autoclave:(Unbiased, 2 Atm 45/0 Saturated Steam, +121°C, 168 Hrs) 9/0 ESD Human Body Model 6/0 ESD Charged Device Model 10/0 Latch up: (Tested to 1.5X Vcc)

Product release is based on qualification of initial lot.

# PRODUCT/PROCESS CHANGE NOTICE (PCN)

### ATTACHMENT - PCN #: SM-0202-02

Qualification Plan: QSM-0201-01

**Expected Completion Date** 

**Test Vehicle** 

4/1/2002

		., .,
	Required	
7134W	Sample /	LOT #1
	# Fails	
Operating Life Test: Dynamic	116/0	
@+135°C, Vcc=4V for 750 hours	11070	
High Temp. Storage Life Test	77 / 0	
(Unbiased, 1000 hours @+150°C)	7770	
Bake & Ballshear Test @ 200°C /	5/0	
4 ball bonds per device	370	
Temperature Cycling: ( -65°C to	45 / 0	
+150°C, 500 cycles)	4370	
HAST: (Biased, 100 Hrs.	45 / 0	
@+130°C, +85%RH, 3 Atm.)	4370	
Autoclave:(Unbiased, 2 Atm	45 / 0	
Saturated Steam, +121°C, 168 Hrs)	4370	
ESD Human Body Model	9/0	
ESD Charged Device Model	6/0	
Latch up: ( Tested to 1.5X Vcc)	10/0	

Product release is based on qualification of initial lot.

**Characterization Data:** 

Characterization will be completed as part of product qualification and data available upon request. Characterization will verify that there is no change to existing datasheet parameters.

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### ATTACHMENT - PCN #: SM-0202-02

IDT7132 Family of Parts							
	Die						
Part Number	Rev.	Interface	Vcc	Bus	Depth	Density	
IDT7130	S4	Async	5	x8	1K	8K	
IDT7132	S4	Async	5	x8	2K	16K	
IDT71321	S4	Async	5	x8	2K	16K	
IDT7140	S4	Async	5	x8	1K	8K	
IDT7142	S4	Async	5	x8	2K	16K	
IDT71421	S4	Async	5	x8	2K	16K	
IDT7130	SF4	Async	5	x8	1K	8K	
IDT7132	SF4	Async	5	x8	2K	16K	
IDT71321	SF4	Async	5	x8	2K	16K	
IDT7140	SF4	Async	5	x8	1K	8K	
IDT7142	SF4	Async	5	x8	2K	16K	
IDT71421	SF4	Async	5	x8	2K	16K	
IDT71V30	SF4	Async	3.3	x8	1K	8K	
IDT71V321	SF4	Async	3.3	x8	2K	16K	
		IDT7133	Family	of Parts			
IDT7133	W4	Async	5	x16	2K	32K	
IDT7143	W4	Async	5	x16	2K	32K	
IDT7134 Family of Parts							
IDT7134	W4	Async	5	x8	4K	32K	
IDT71342	W4	Async	5	x8	4K	32K	