µPD78F9221/78F9222

IDD VS VDD(25°C/8MHz[Internal-OSC]/AD=ON)
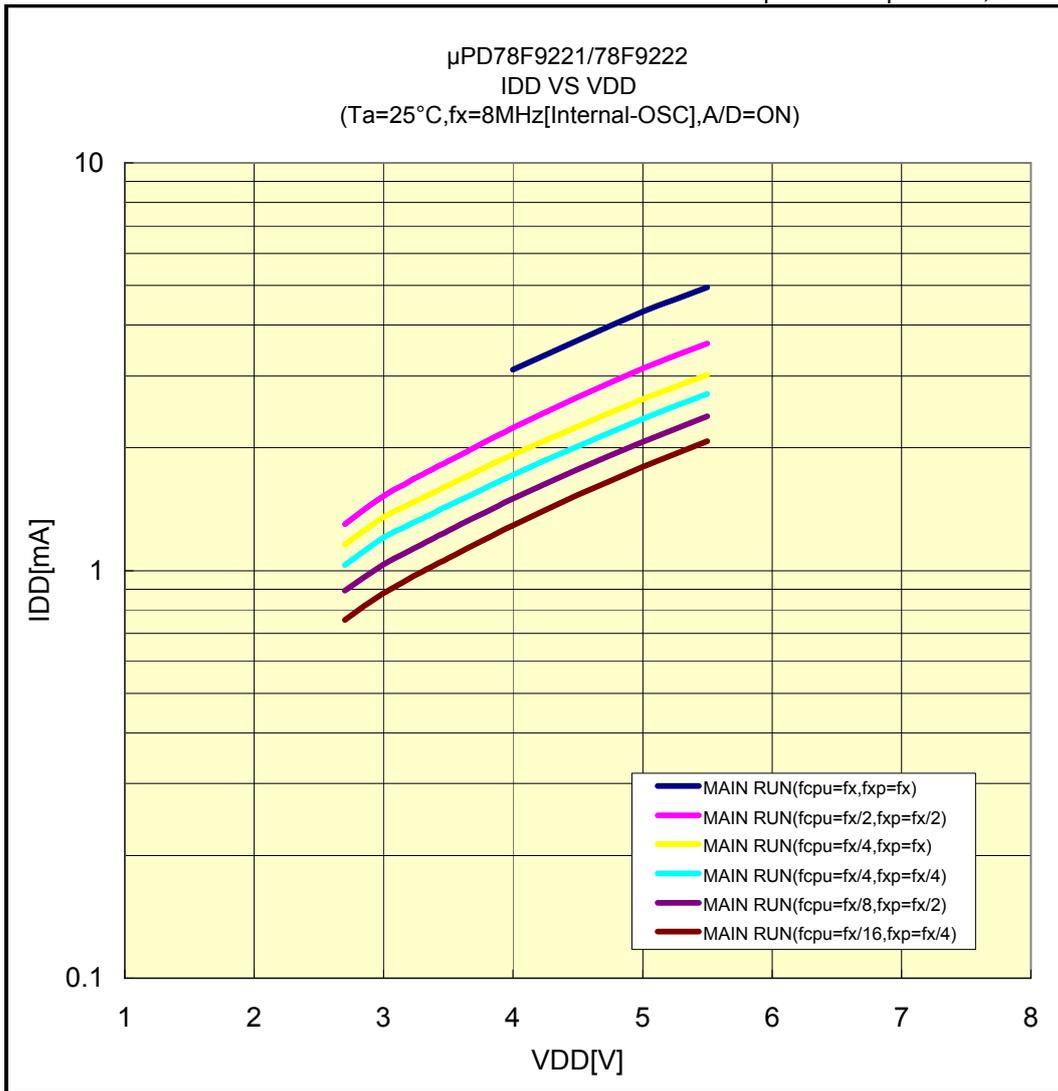
µPD78F9221/78F9222
IDD VS VDD
(Ta=25°C,fx=8MHz[Internal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(25°C/2MHz[X'tal-OSC]/AD=ON)
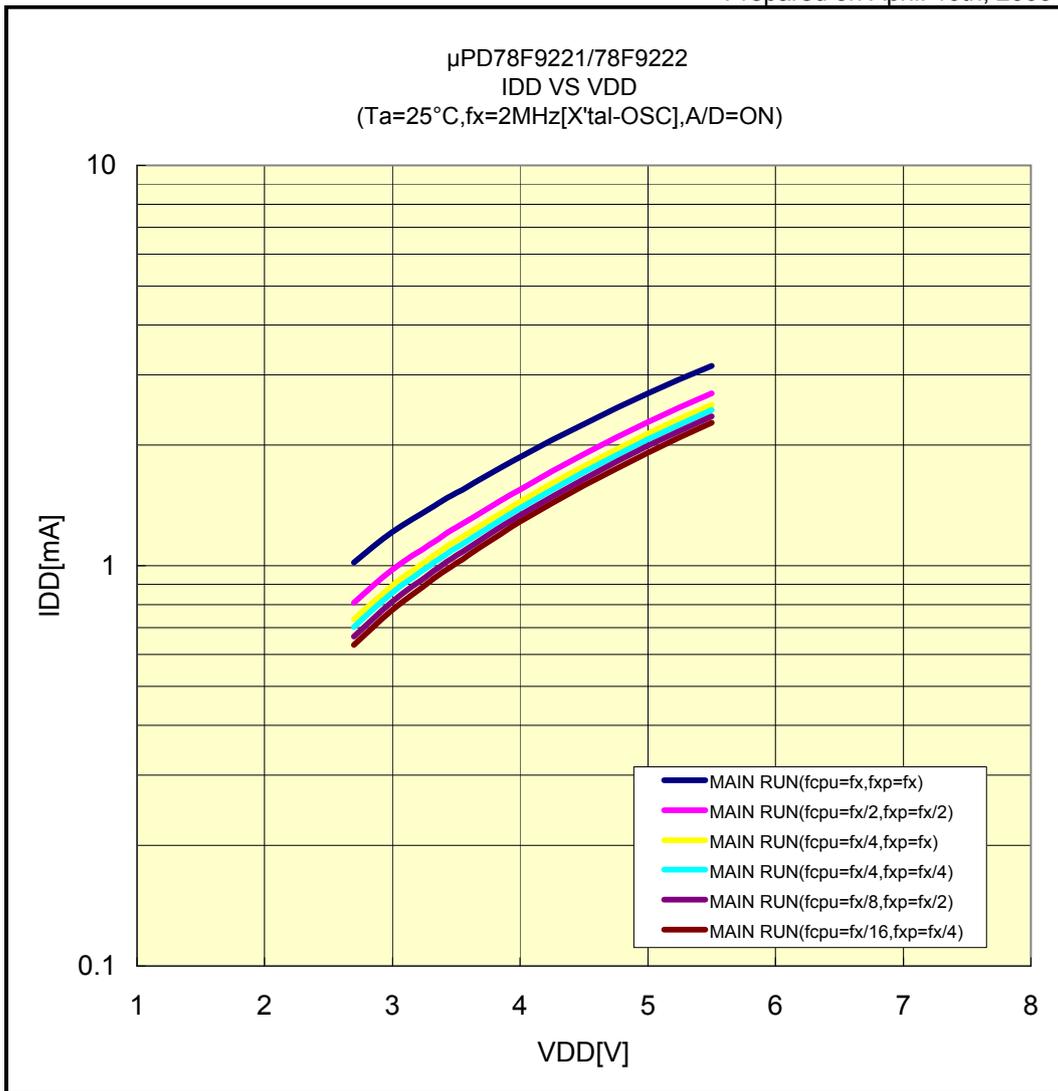
µPD78F9221/78F9222
IDD VS VDD
(Ta=25°C,fx=2MHz[X'tal-OSC],A/D=ON)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9221/78F9222

IDD VS VDD(25°C/4MHz[X'tal-OSC]/AD=ON)
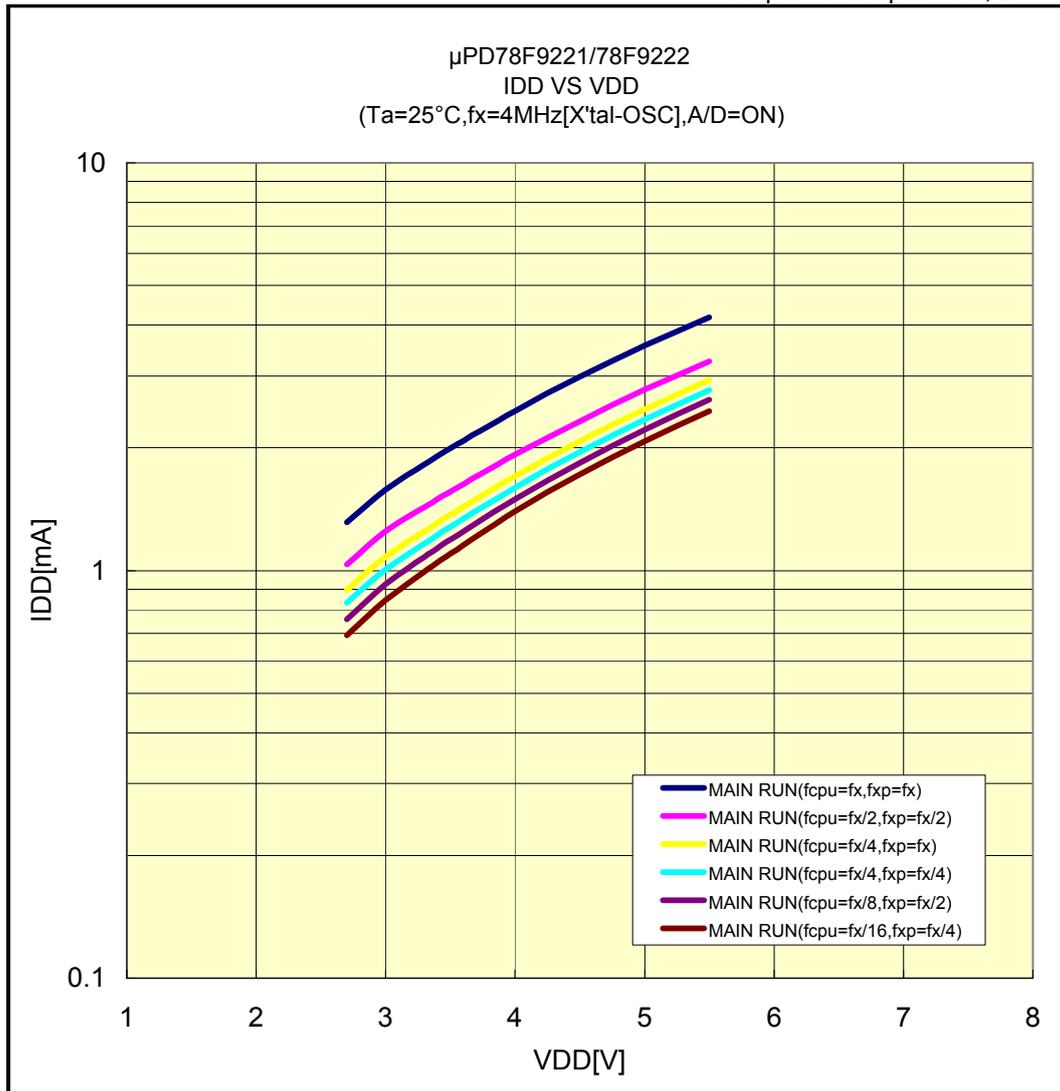
μPD78F9221/78F9222
IDD VS VDD
(Ta=25°C,fx=4MHz[X'tal-OSC],A/D=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(25°C/5MHz[X'tal-OSC]/AD=ON)

Prepared on April. 19th, 2006
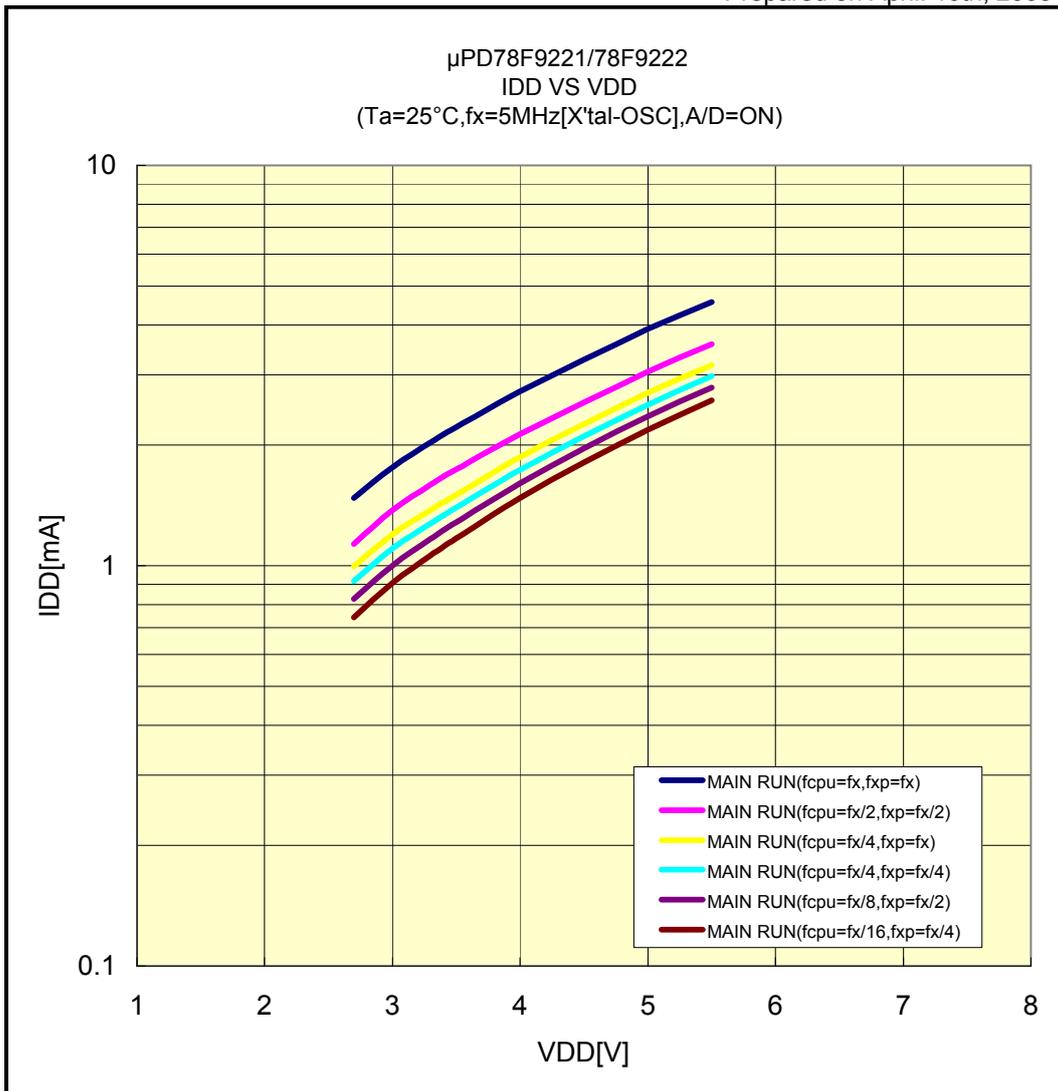
µPD78F9221/78F9222
IDD VS VDD
(Ta=25°C,fx=5MHz[X'tal-OSC],A/D=ON)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(25°C/6MHz[X'tal-OSC]/AD=ON)
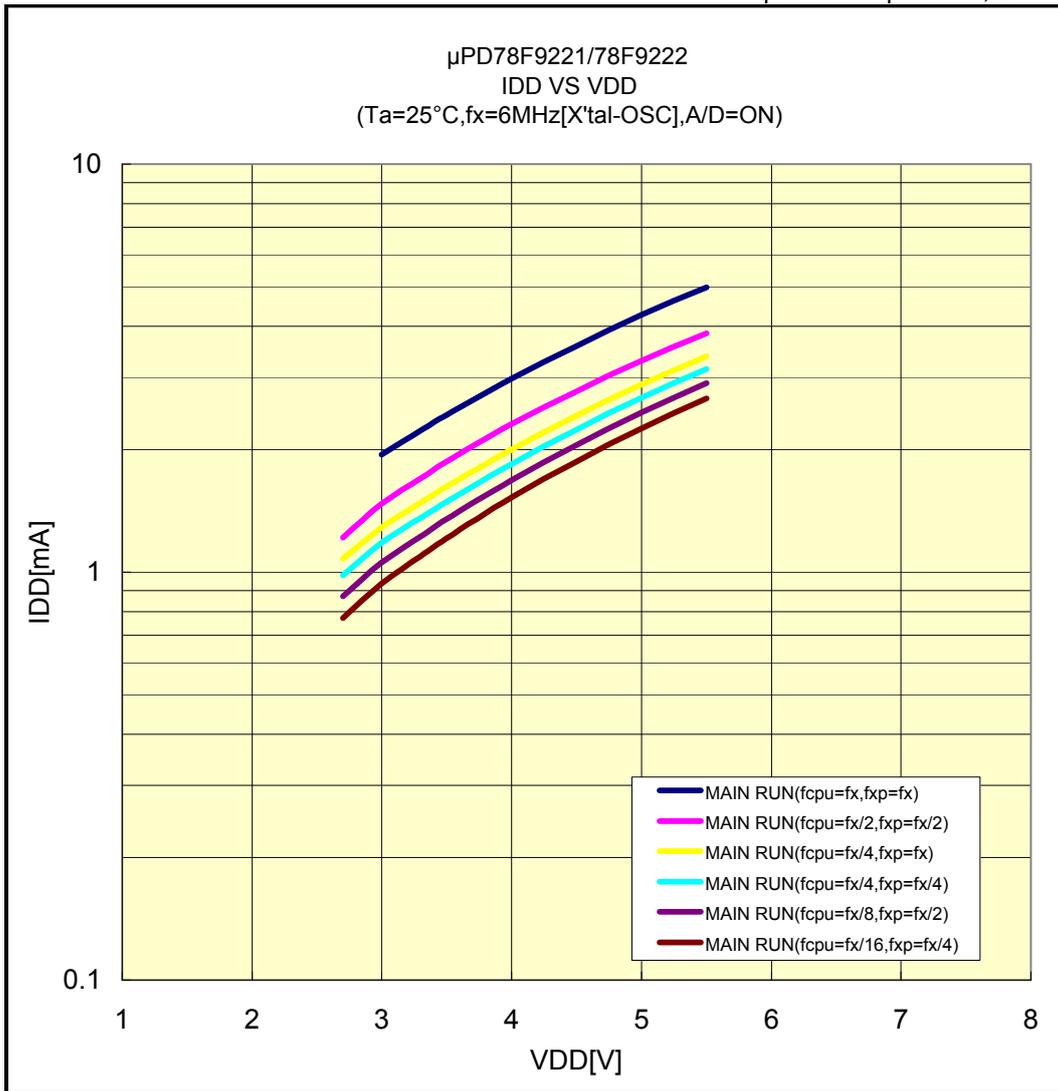
µPD78F9221/78F9222
IDD VS VDD
(Ta=25°C,fx=6MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(25°C/8MHz[X'tal-OSC]/AD=ON)

Prepared on April. 19th, 2006
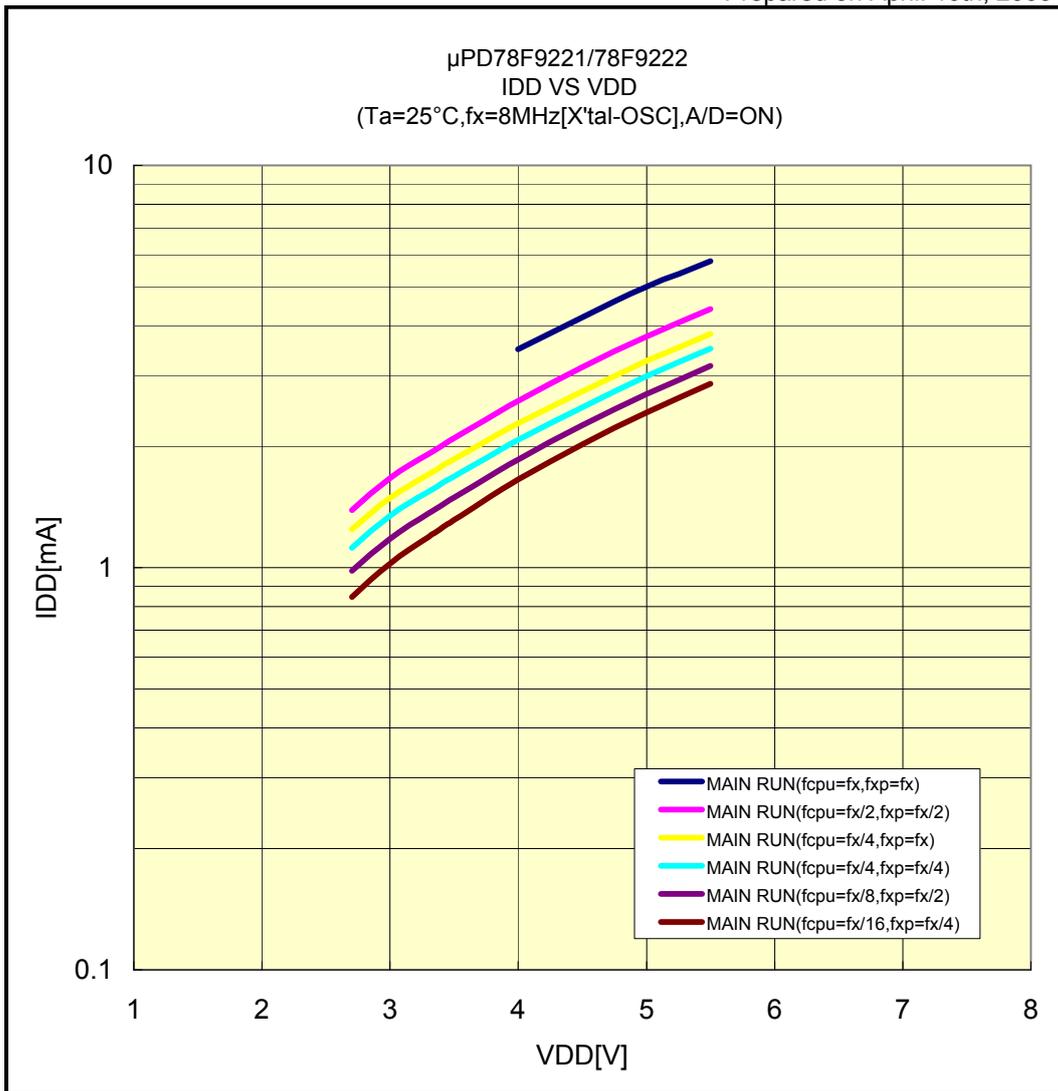
µPD78F9221/78F9222
IDD VS VDD
(Ta=25°C,fx=8MHz[X'tal-OSC],A/D=ON)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(25°C/10MHz[X'tal-OSC]/AD=ON)
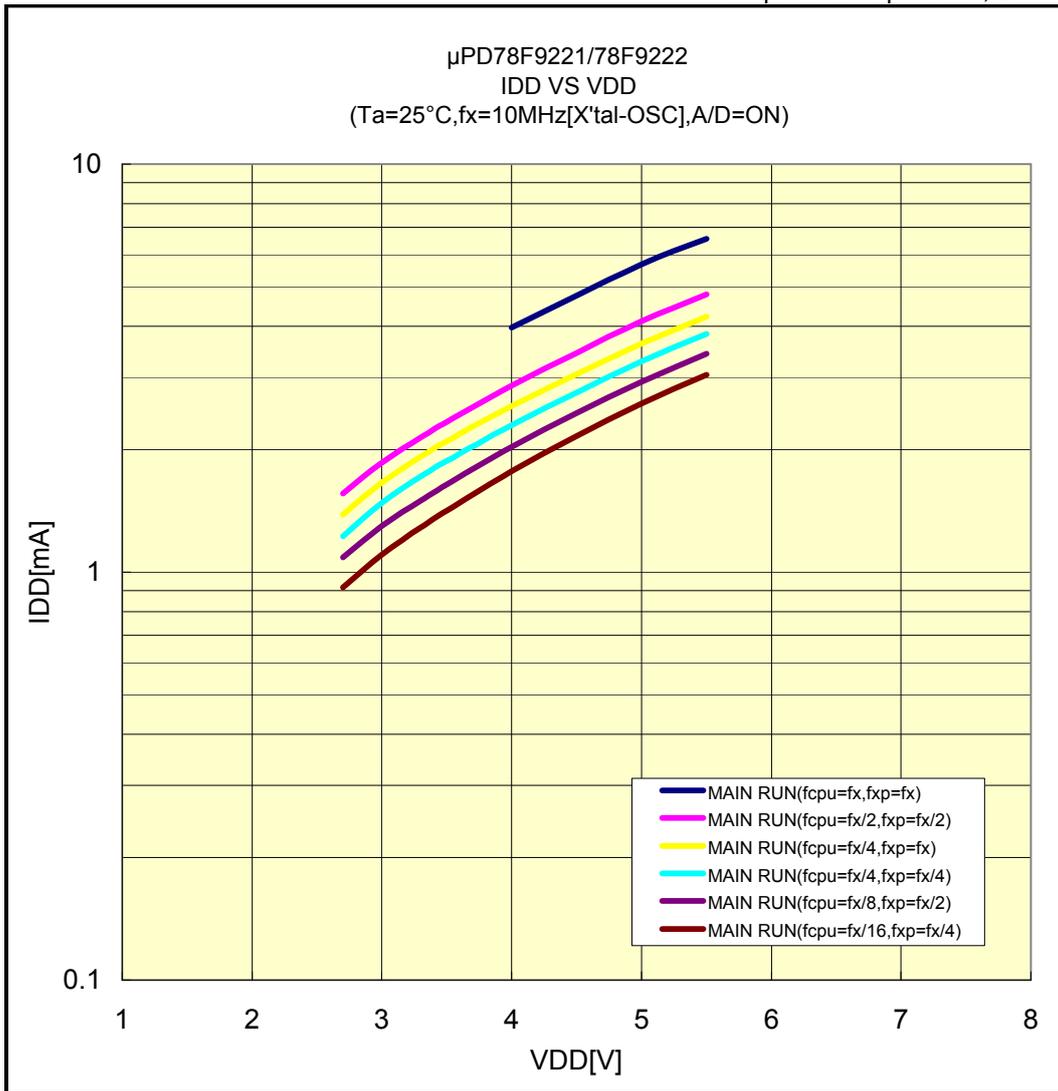
µPD78F9221/78F9222
IDD VS VDD
(Ta=25°C,fx=10MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

X-axis: VDD[V]
Y-axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9221/78F9222

IDD VS VDD(-40°C/8MHz[Internal-OSC]/AD=ON)
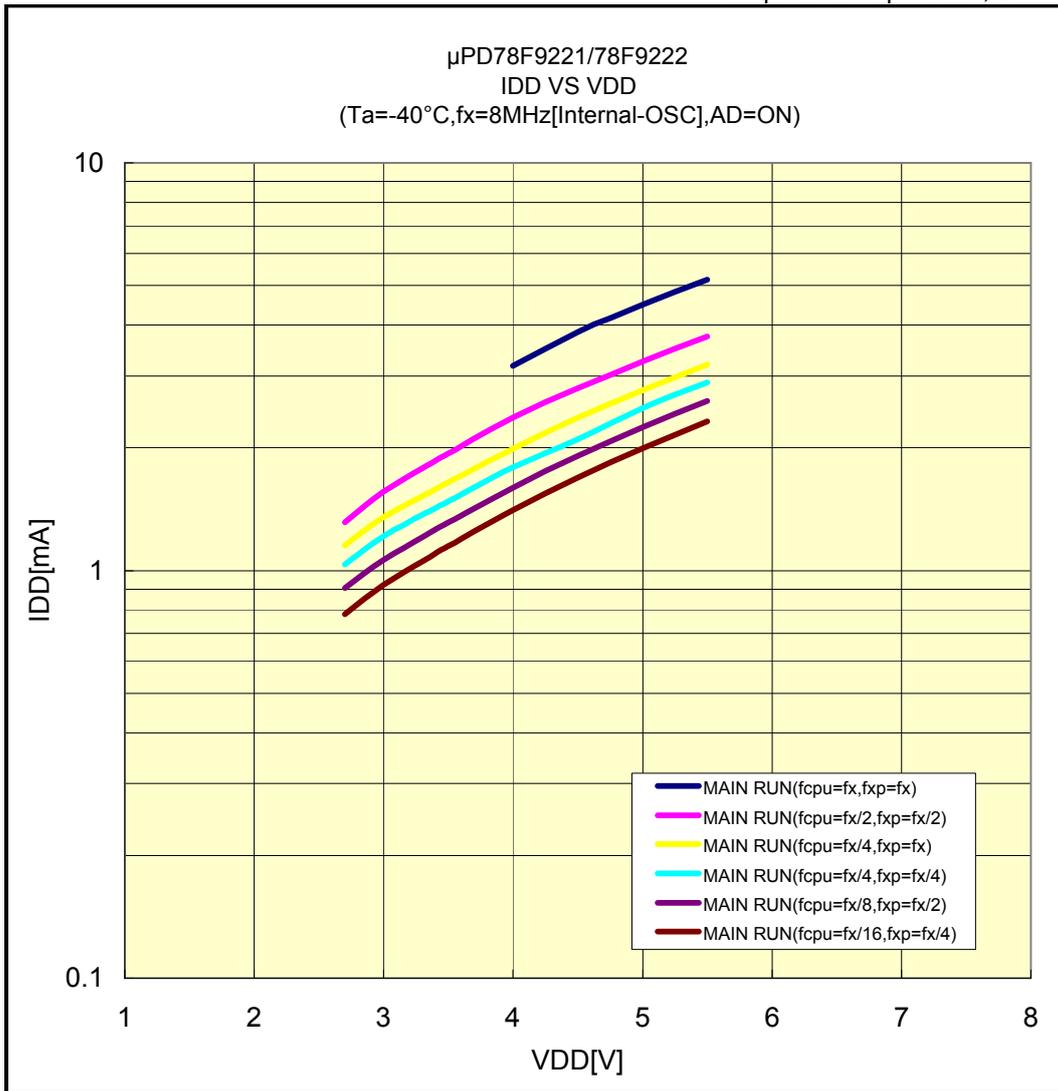
μPD78F9221/78F9222
IDD VS VDD
(Ta=-40°C,fx=8MHz[Internal-OSC],AD=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axes: IDD[mA] (vertical), VDD[V] (horizontal)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(-40°C/2MHz[X'tal-OSC]/AD=ON)

Prepared on April. 19th, 2006

µPD78F9221/78F9222
IDD VS VDD
(Ta=-40°C,fx=2MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

X axis: VDD[V], Y axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9221/78F9222

IDD VS VDD(-40°C/4MHz[X'tal-OSC]/AD=ON)

μPD78F9221/78F9222
IDD VS VDD
(Ta=-40°C,fx=4MHz[X'tal-OSC],AD=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(-40°C/4MHz[X'tal-OSC]/AD=ON)

µPD78F9221/78F9222
IDD VS VDD
(Ta=-40°C,fx=5MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(-40°C/6MHz[X'tal-OSC]/AD=ON)
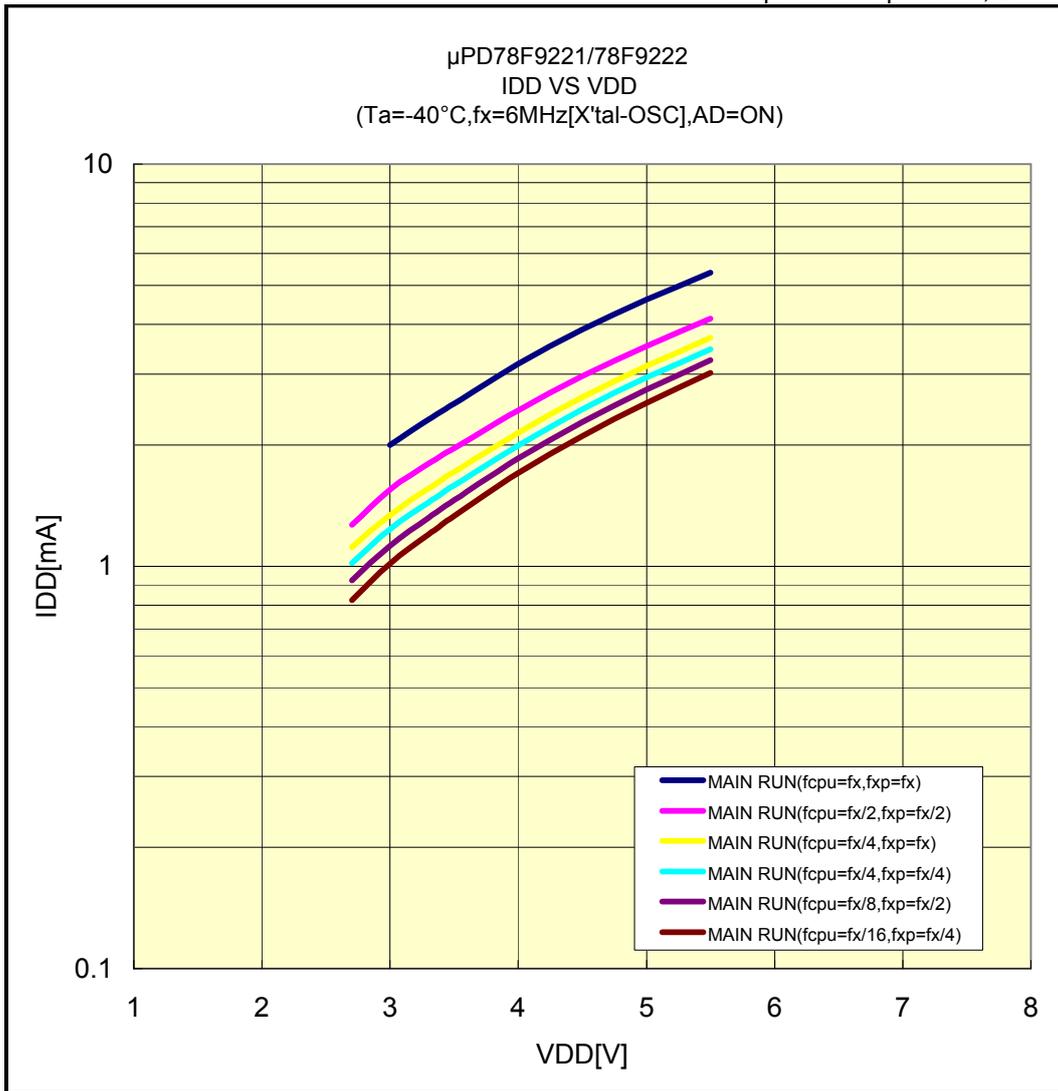
µPD78F9221/78F9222
IDD VS VDD
(Ta=-40°C,fx=6MHz[X'tal-OSC],AD=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

IDD[mA]

VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(-40°C/8MHz[X'tal-OSC]/AD=ON)
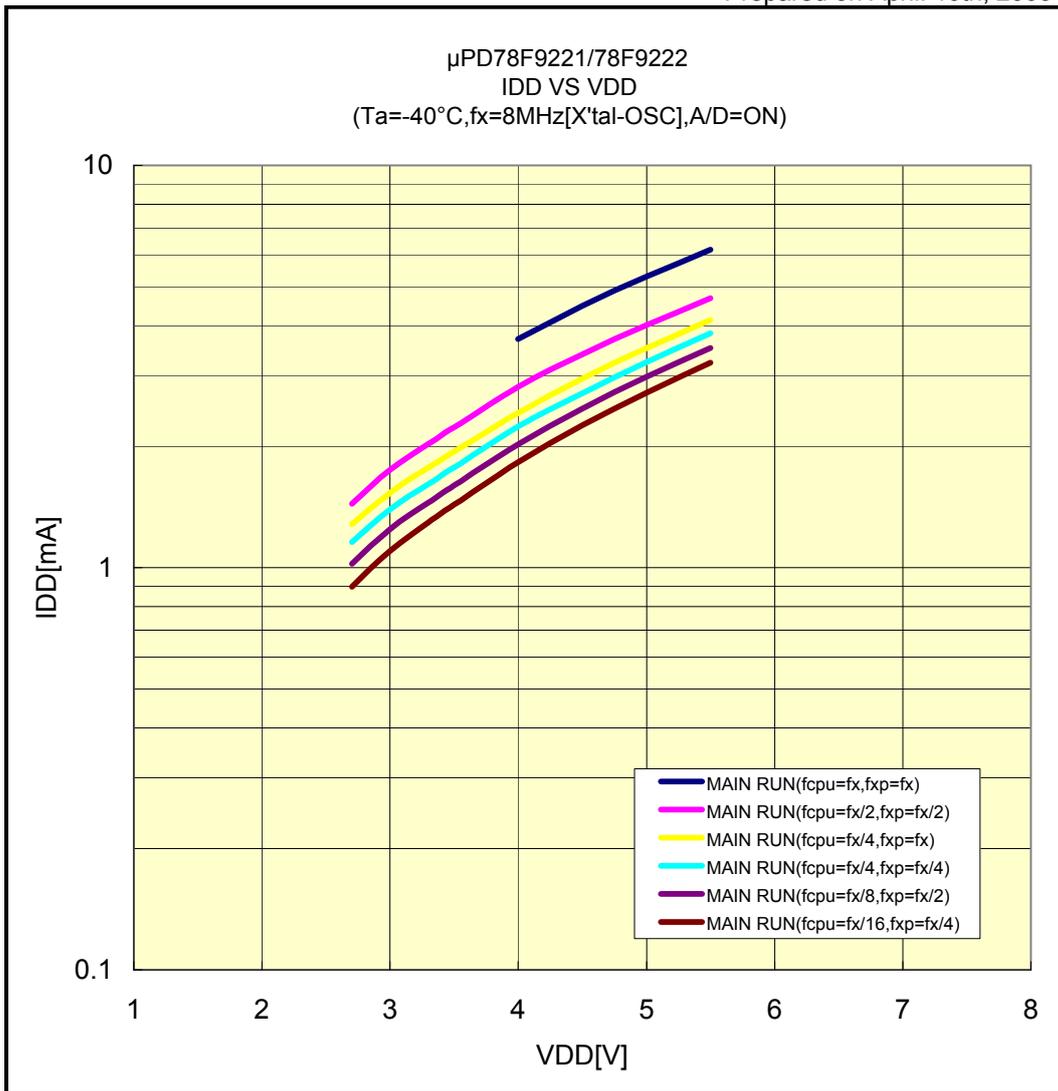
µPD78F9221/78F9222
IDD VS VDD
(Ta=-40°C,fx=8MHz[X'tal-OSC],A/D=ON)

- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(-40°C/10MHz[X'tal-OSC]/AD=ON)

Prepared on April. 19th, 2006
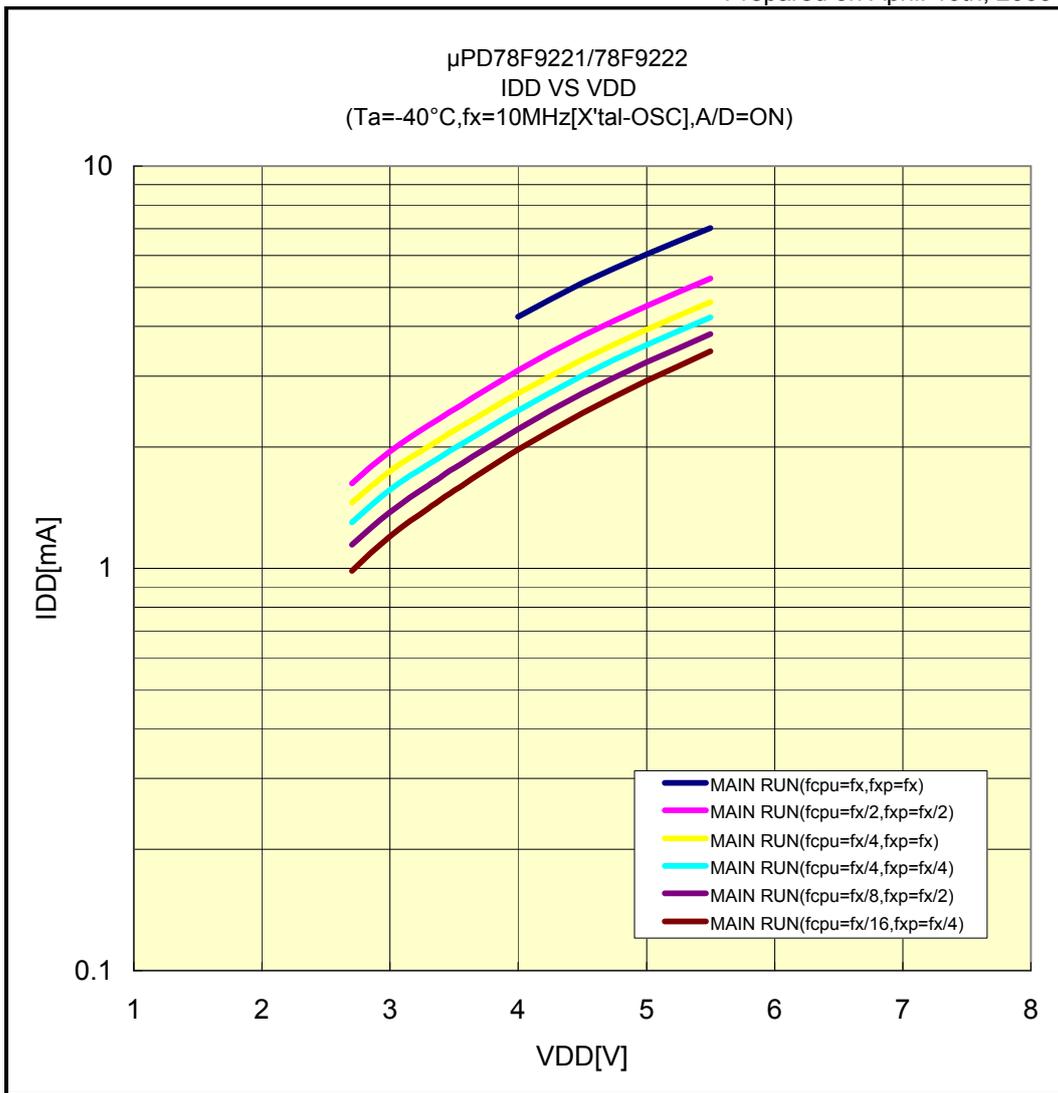
µPD78F9221/78F9222
IDD VS VDD
(Ta=-40°C,fx=10MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(85°C/8MHz[Internal-OSC]/AD=ON)
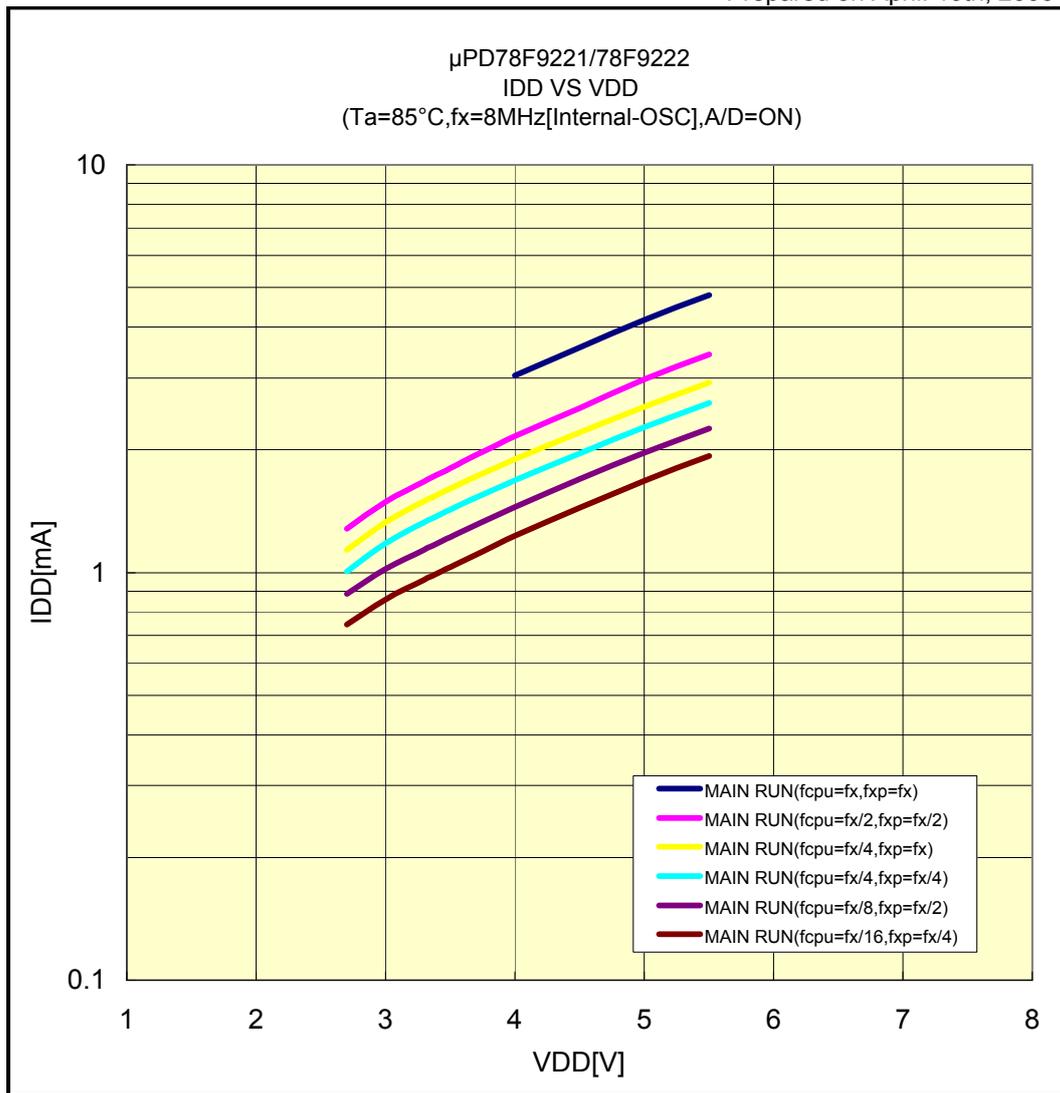
µPD78F9221/78F9222
IDD VS VDD
(Ta=85°C,fx=8MHz[Internal-OSC],A/D=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(85°C/2MHz[X'tal-OSC]/AD=ON)
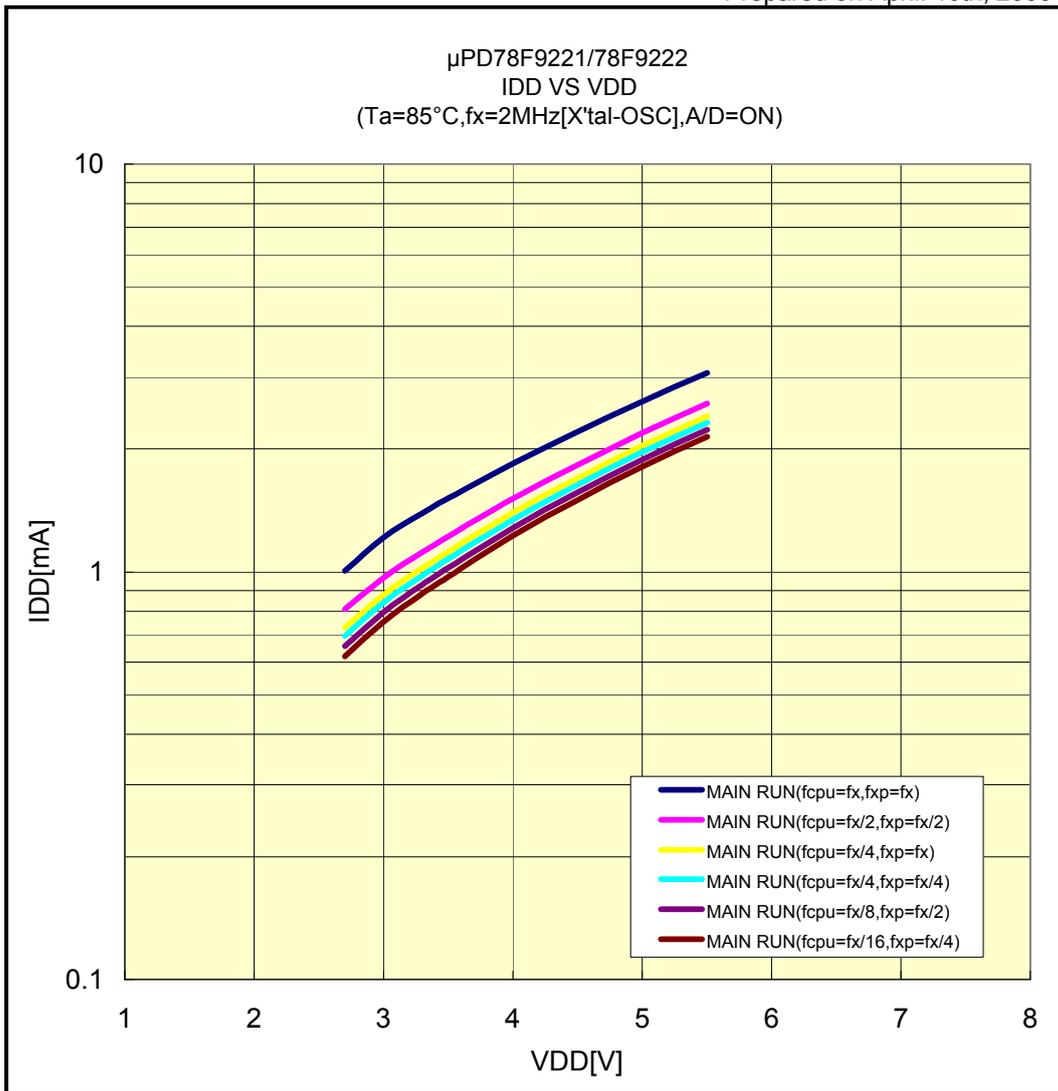
µPD78F9221/78F9222
IDD VS VDD
(Ta=85°C,fx=2MHz[X'tal-OSC],A/D=ON)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9221/78F9222

IDD VS VDD(85°C/4MHz[X'tal-OSC]/AD=ON)
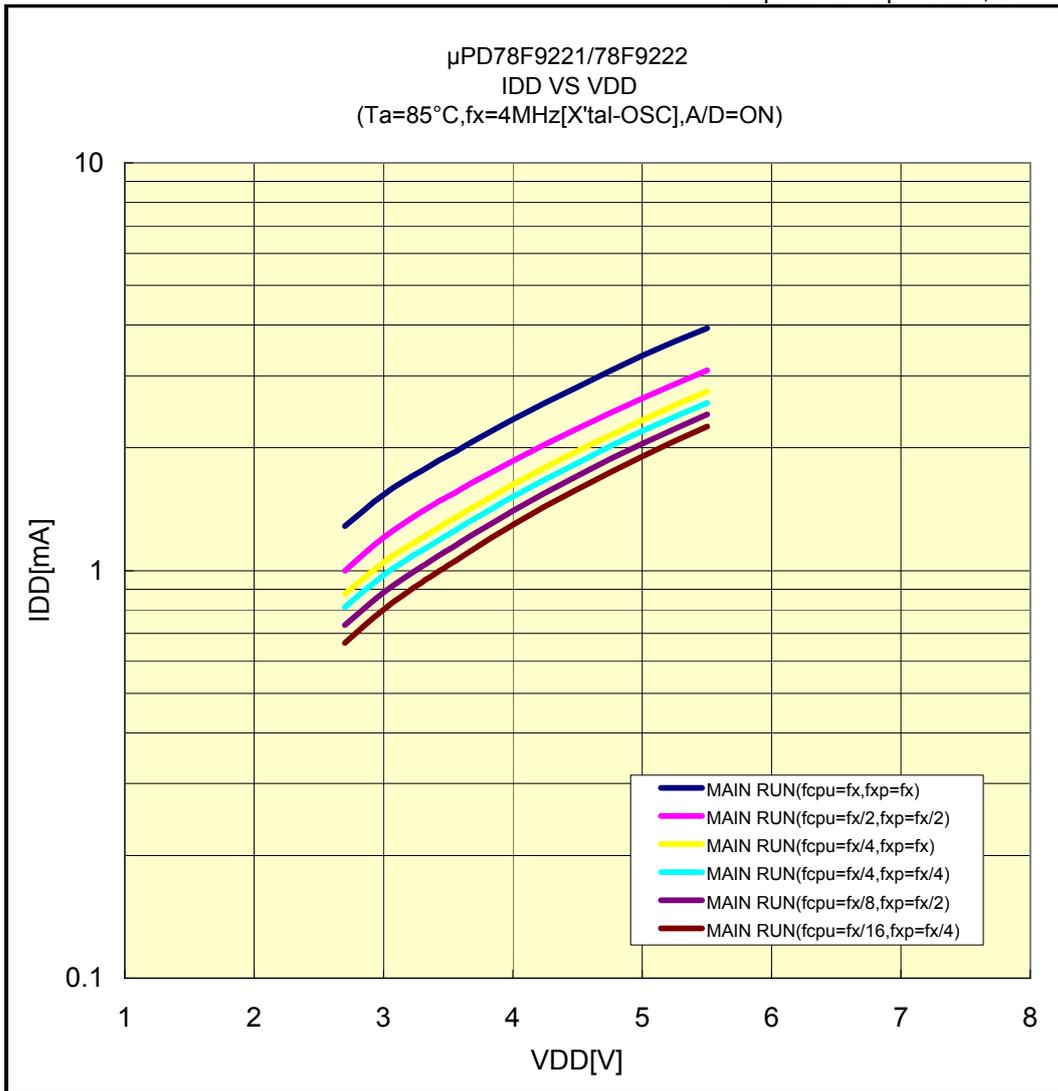
μPD78F9221/78F9222
IDD VS VDD
(Ta=85°C,fx=4MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

X-axis: VDD[V]
Y-axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9221/78F9222

IDD VS VDD(85°C/5MHz[X'tal-OSC]/AD=ON)
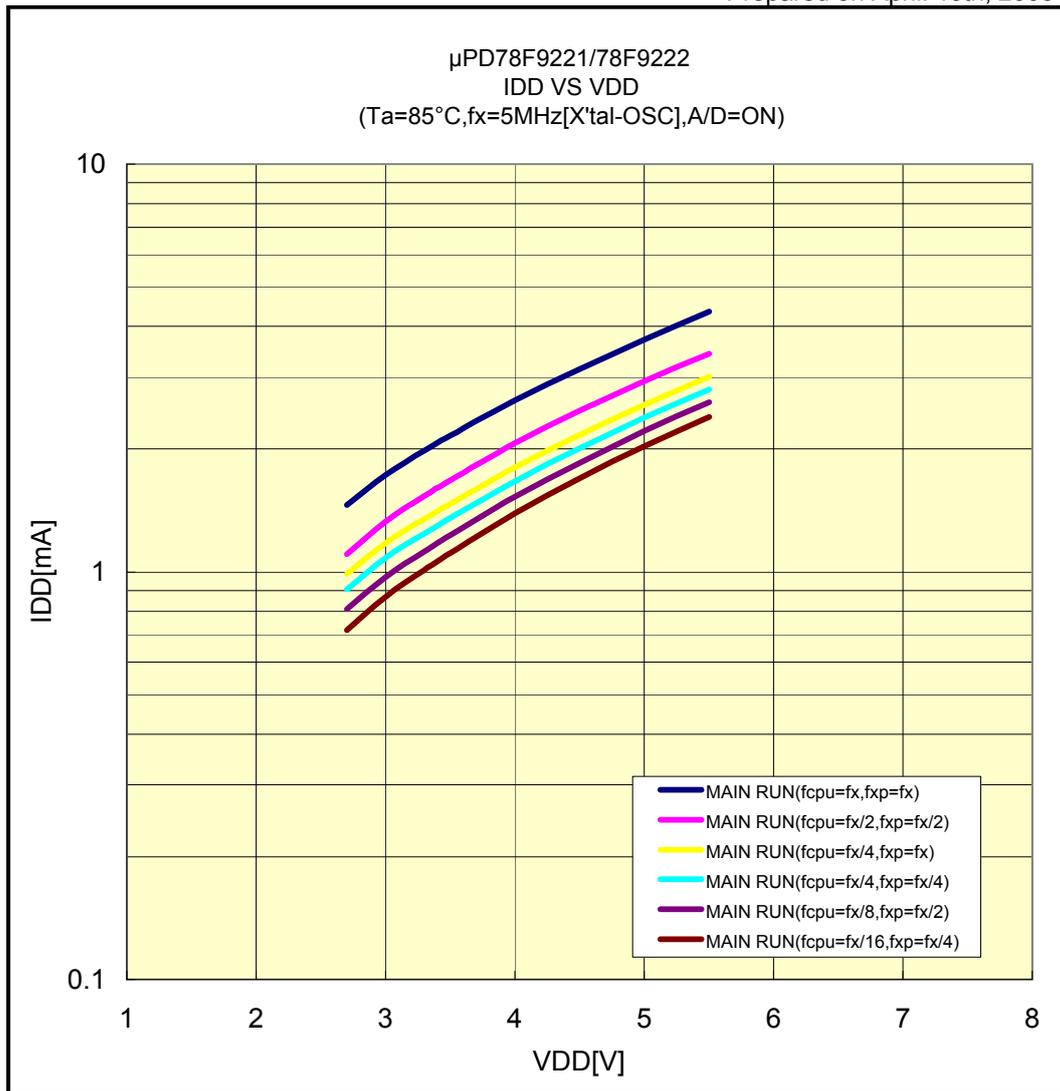
μPD78F9221/78F9222
IDD VS VDD
(Ta=85°C,fx=5MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axes: IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(85°C/6MHz[X'tal-OSC]/AD=ON)
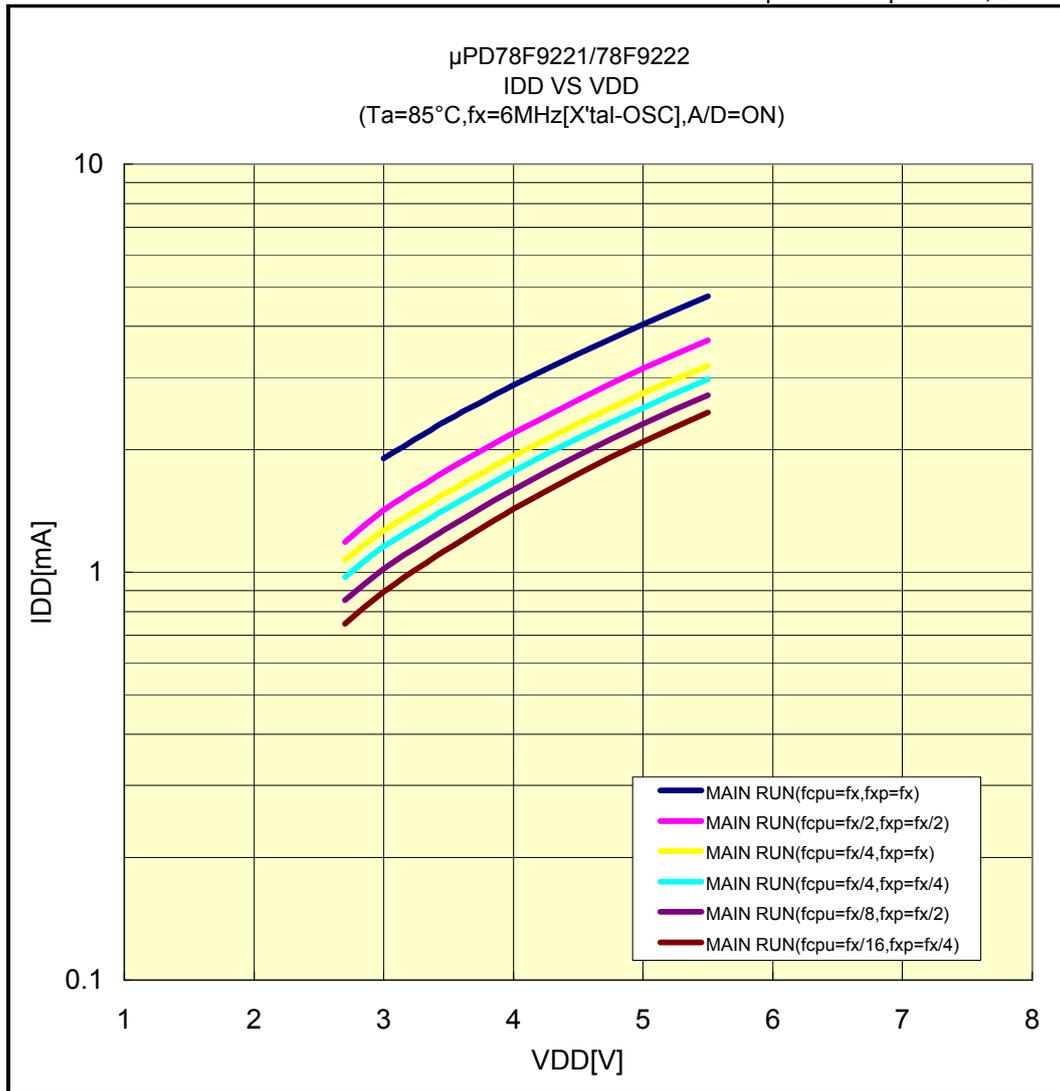
µPD78F9221/78F9222
IDD VS VDD
(Ta=85°C,fx=6MHz[X'tal-OSC],A/D=ON)

Legend:
MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(85°C/8MHz[X'tal-OSC]/AD=ON)
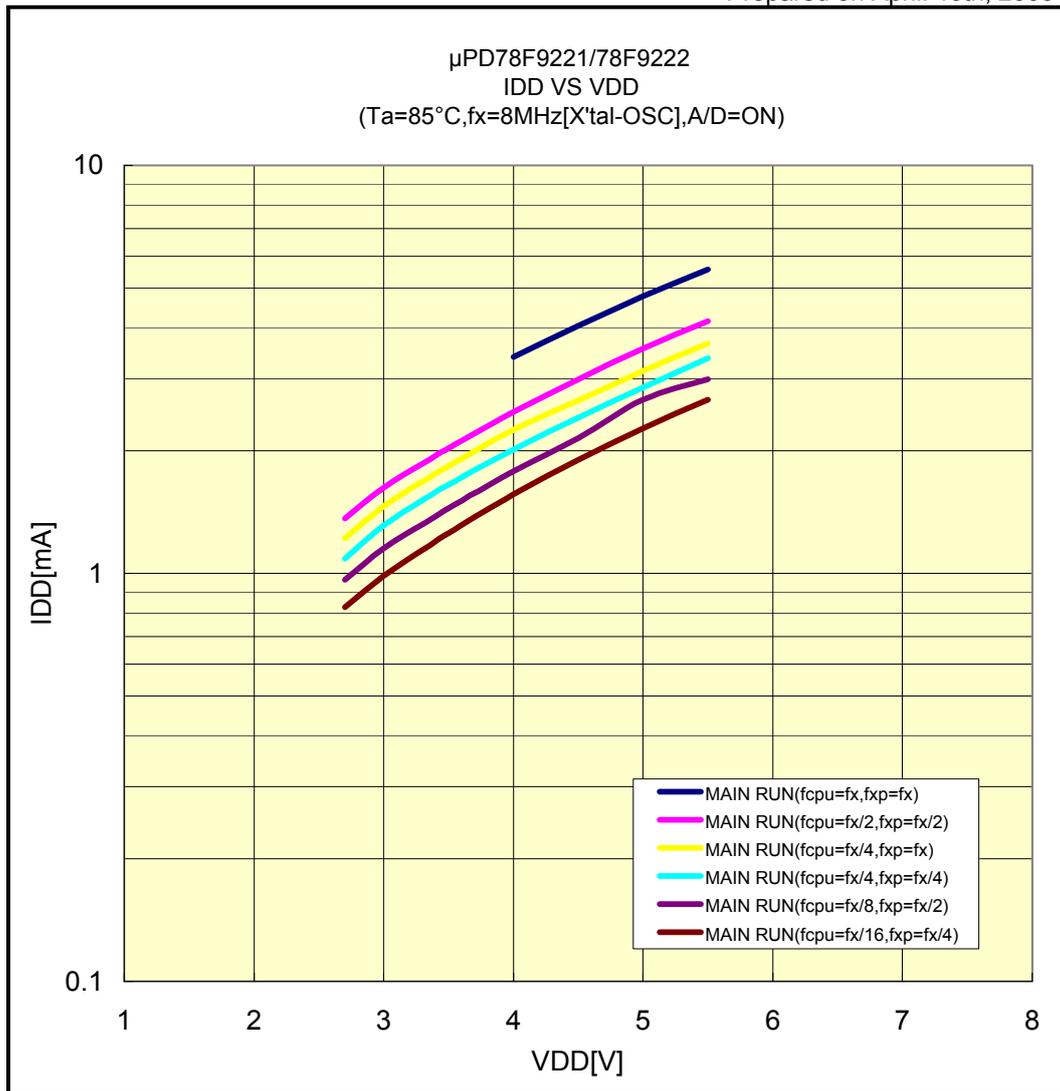
µPD78F9221/78F9222
IDD VS VDD
(Ta=85°C,fx=8MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axis labels: IDD[mA] (vertical), VDD[V] (horizontal)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(85°C/10MHz[X'tal-OSC]/AD=ON)
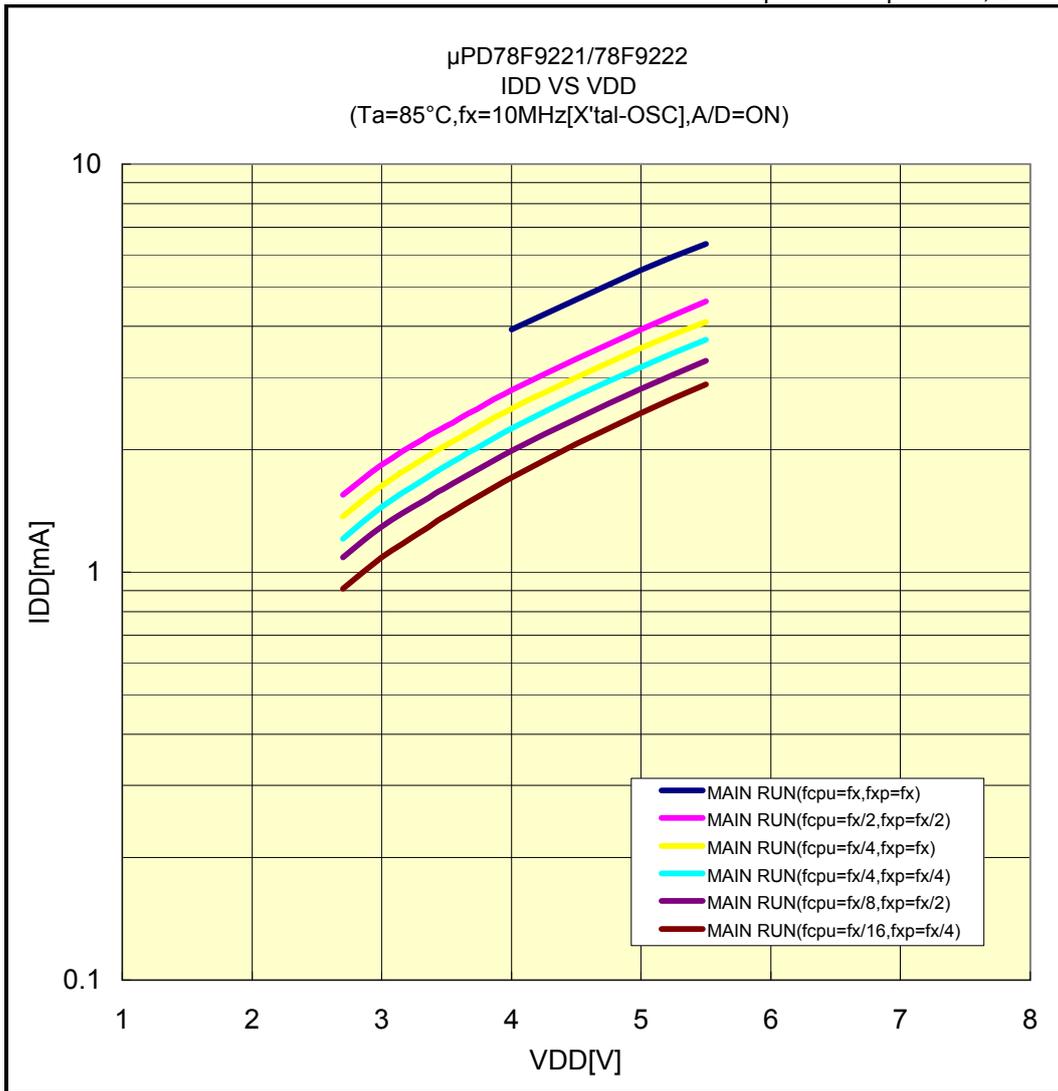
µPD78F9221/78F9222
IDD VS VDD
(Ta=85°C,fx=10MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axes: IDD[mA] (vertical), VDD[V] (horizontal)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(125°C/8MHz[Internal-OSC]/AD=ON)
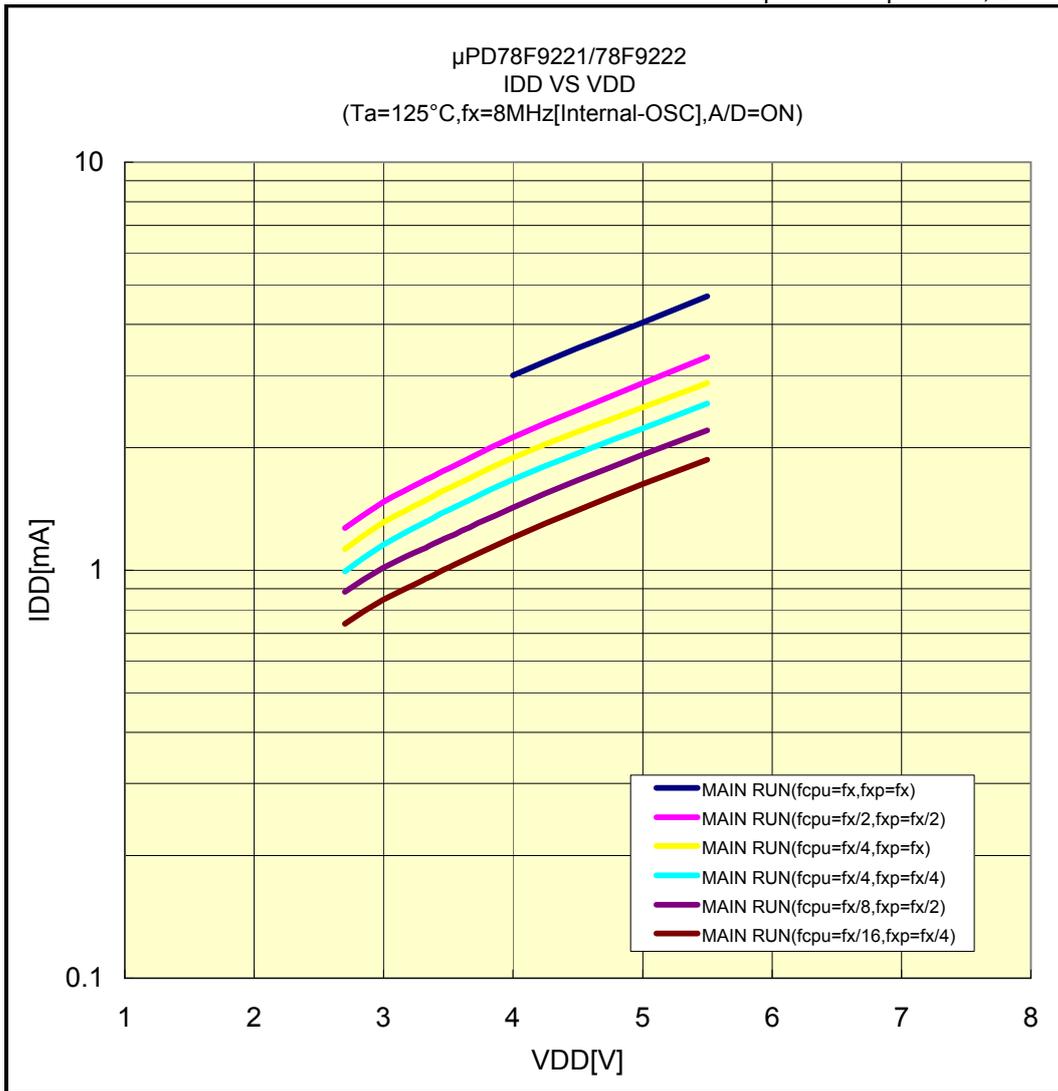
µPD78F9221/78F9222
IDD VS VDD
(Ta=125°C,fx=8MHz[Internal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

X-axis: VDD[V]
Y-axis: IDD[mA]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9221/78F9222

IDD VS VDD(125°C/2MHz[X'tal-OSC]/AD=ON)
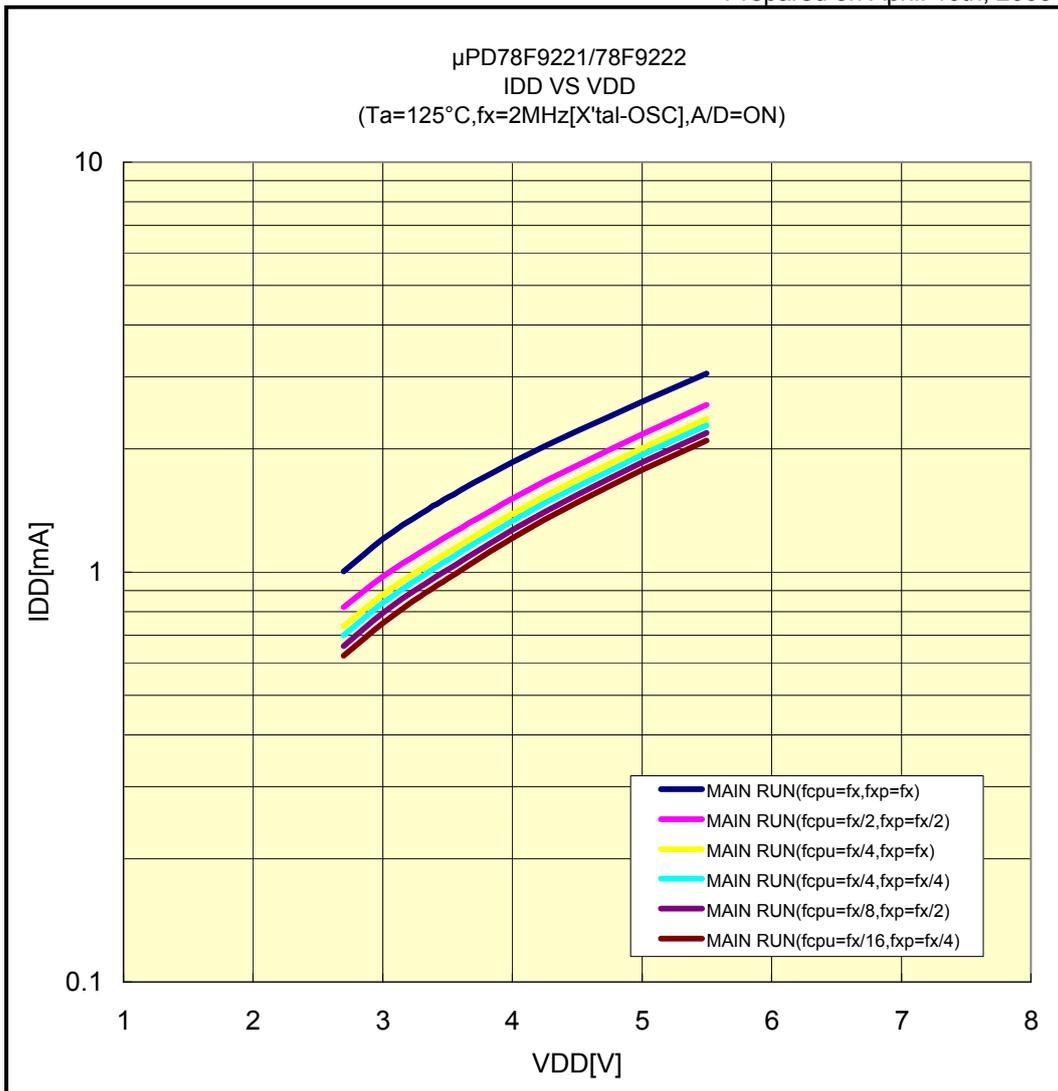
μPD78F9221/78F9222
IDD VS VDD
(Ta=125°C,fx=2MHz[X'tal-OSC],A/D=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(125°C/4MHz[X'tal-OSC]/AD=ON)
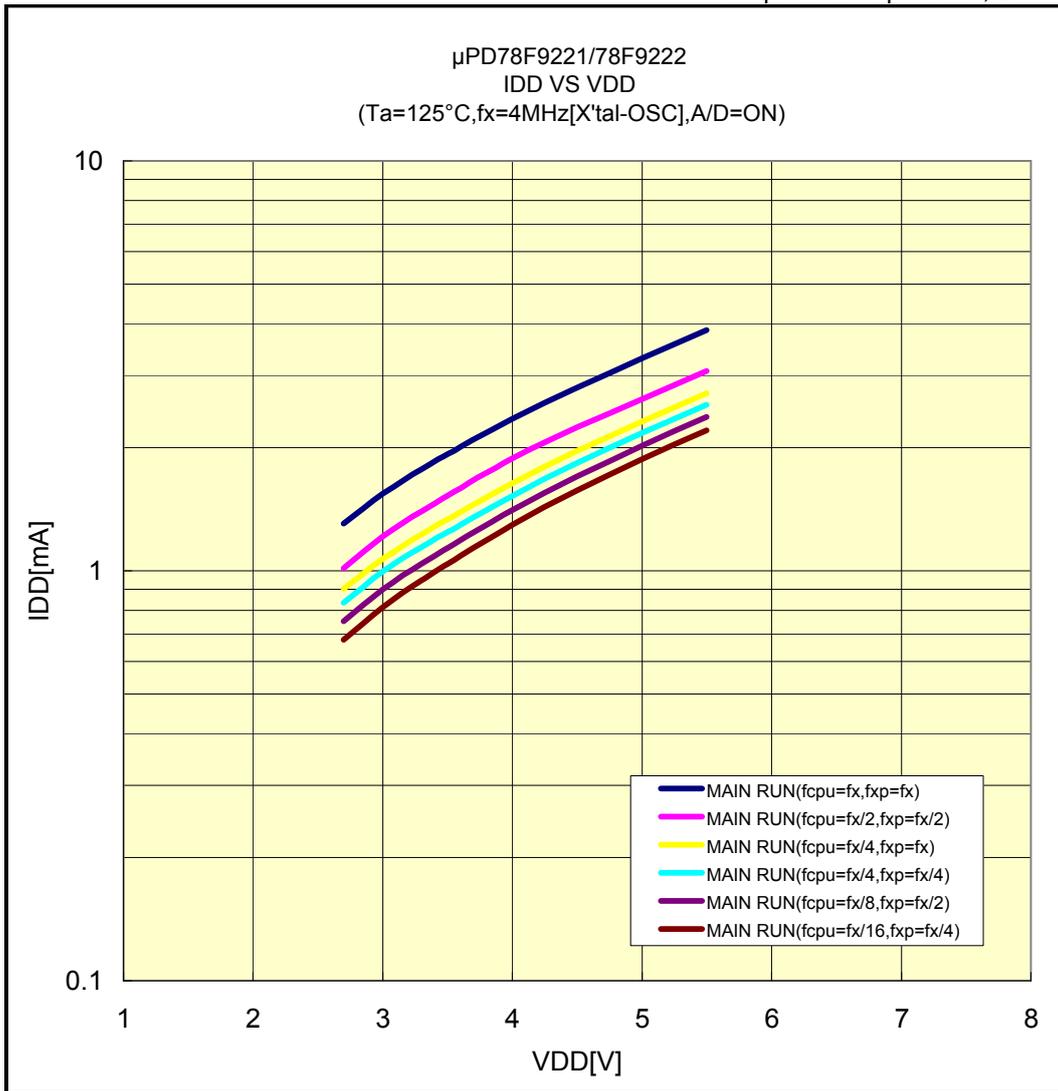
µPD78F9221/78F9222
IDD VS VDD
(Ta=125°C,fx=4MHz[X'tal-OSC],A/D=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(125°C/5MHz[X'tal-OSC]/AD=ON)
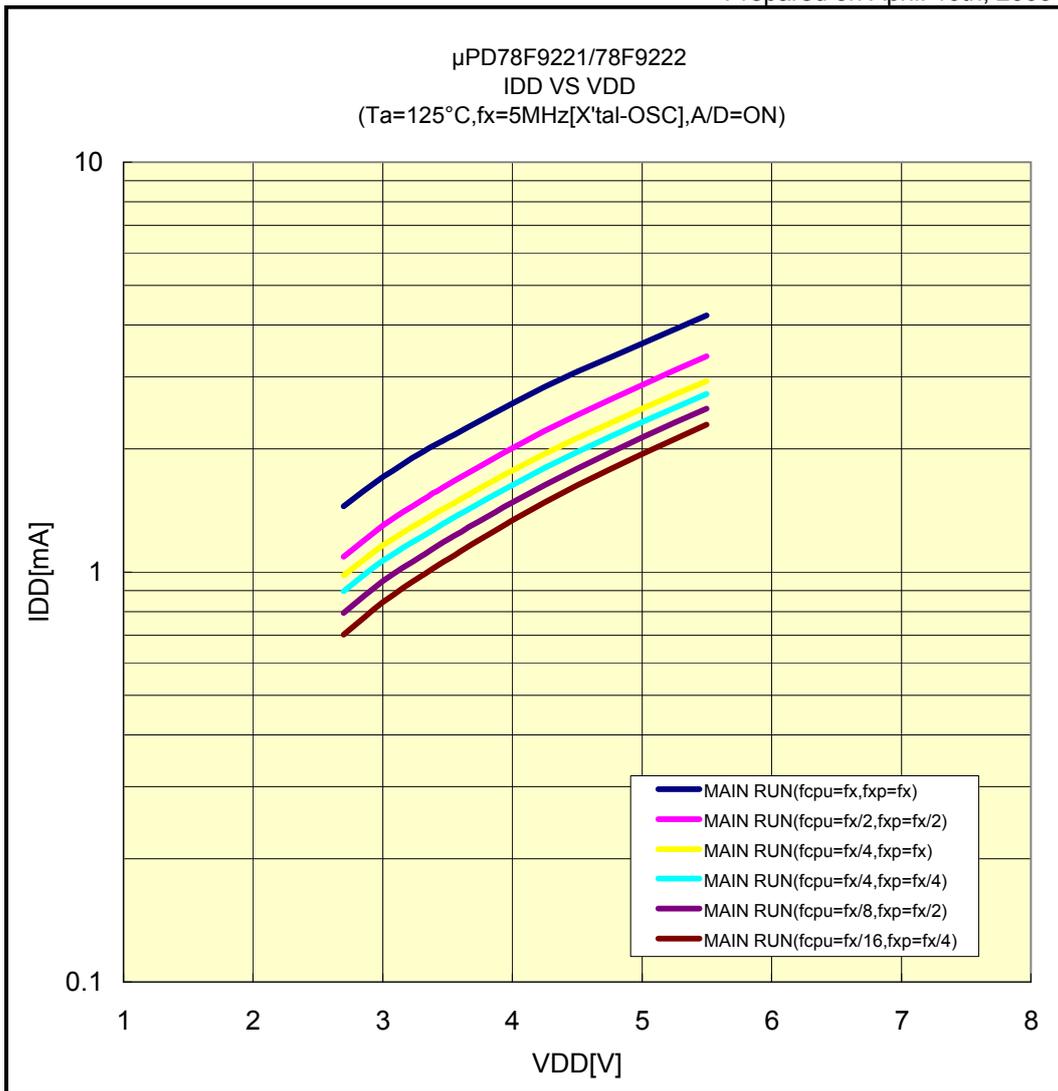
µPD78F9221/78F9222
IDD VS VDD
(Ta=125°C,fx=5MHz[X'tal-OSC],A/D=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

IDD[mA] (vertical axis)
VDD[V] (horizontal axis)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

μPD78F9221/78F9222

IDD VS VDD(125°C/6MHz[X'tal-OSC]/AD=ON)
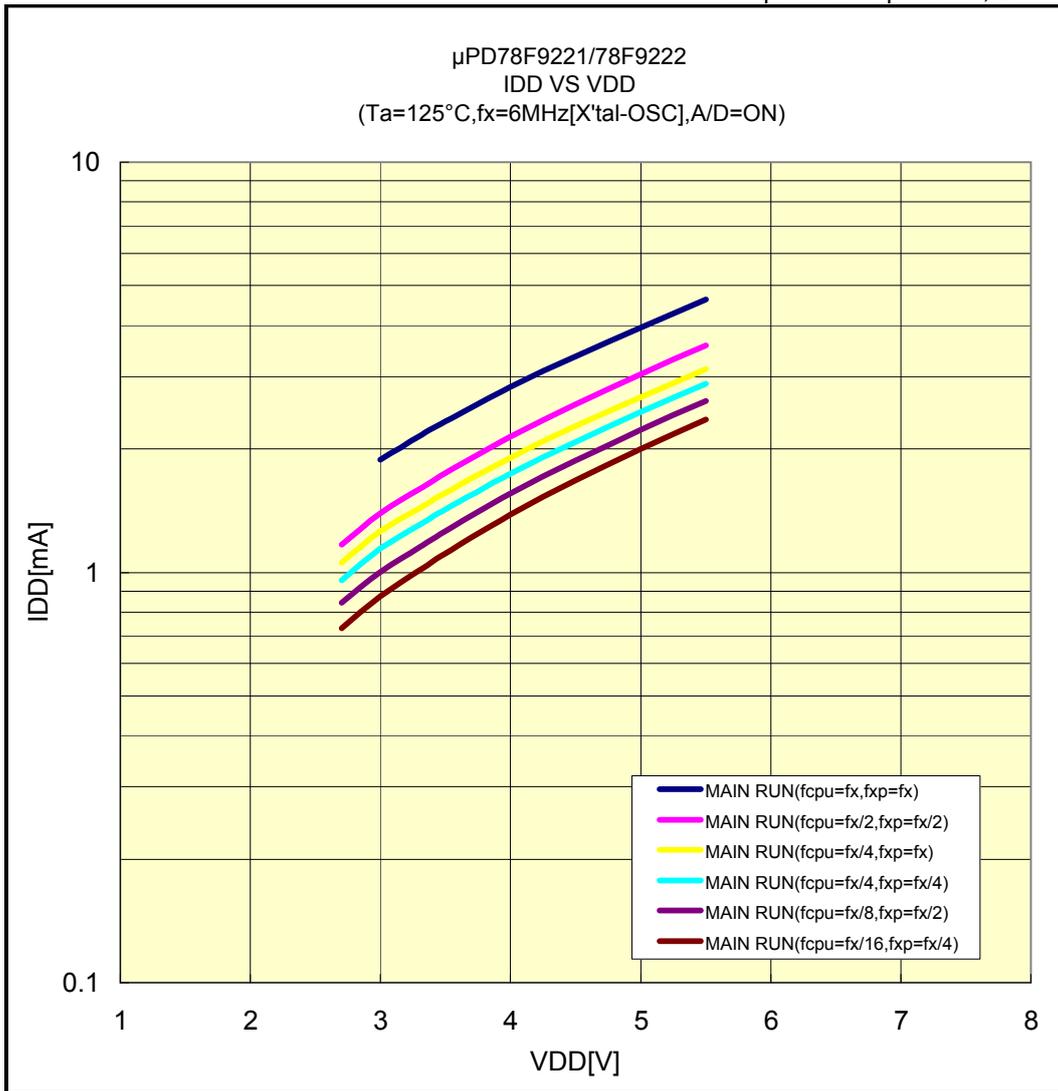
μPD78F9221/78F9222
IDD VS VDD
(Ta=125°C,fx=6MHz[X'tal-OSC],A/D=ON)

Legend:
- MAIN RUN(fcpu=fx,fxp=fx)
- MAIN RUN(fcpu=fx/2,fxp=fx/2)
- MAIN RUN(fcpu=fx/4,fxp=fx)
- MAIN RUN(fcpu=fx/4,fxp=fx/4)
- MAIN RUN(fcpu=fx/8,fxp=fx/2)
- MAIN RUN(fcpu=fx/16,fxp=fx/4)

Axis labels: IDD[mA] (vertical), VDD[V] (horizontal)

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

µPD78F9221/78F9222

IDD VS VDD(125°C/8MHz[X'tal-OSC]/AD=ON)

µPD78F9221/78F9222
IDD VS VDD
(Ta=125°C,fx=8MHz[X'tal-OSC],A/D=ON)

MAIN RUN(fcpu=fx,fxp=fx)
MAIN RUN(fcpu=fx/2,fxp=fx/2)
MAIN RUN(fcpu=fx/4,fxp=fx)
MAIN RUN(fcpu=fx/4,fxp=fx/4)
MAIN RUN(fcpu=fx/8,fxp=fx/2)
MAIN RUN(fcpu=fx/16,fxp=fx/4)

IDD[mA] vs VDD[V]

The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.