

UPD78F15mA

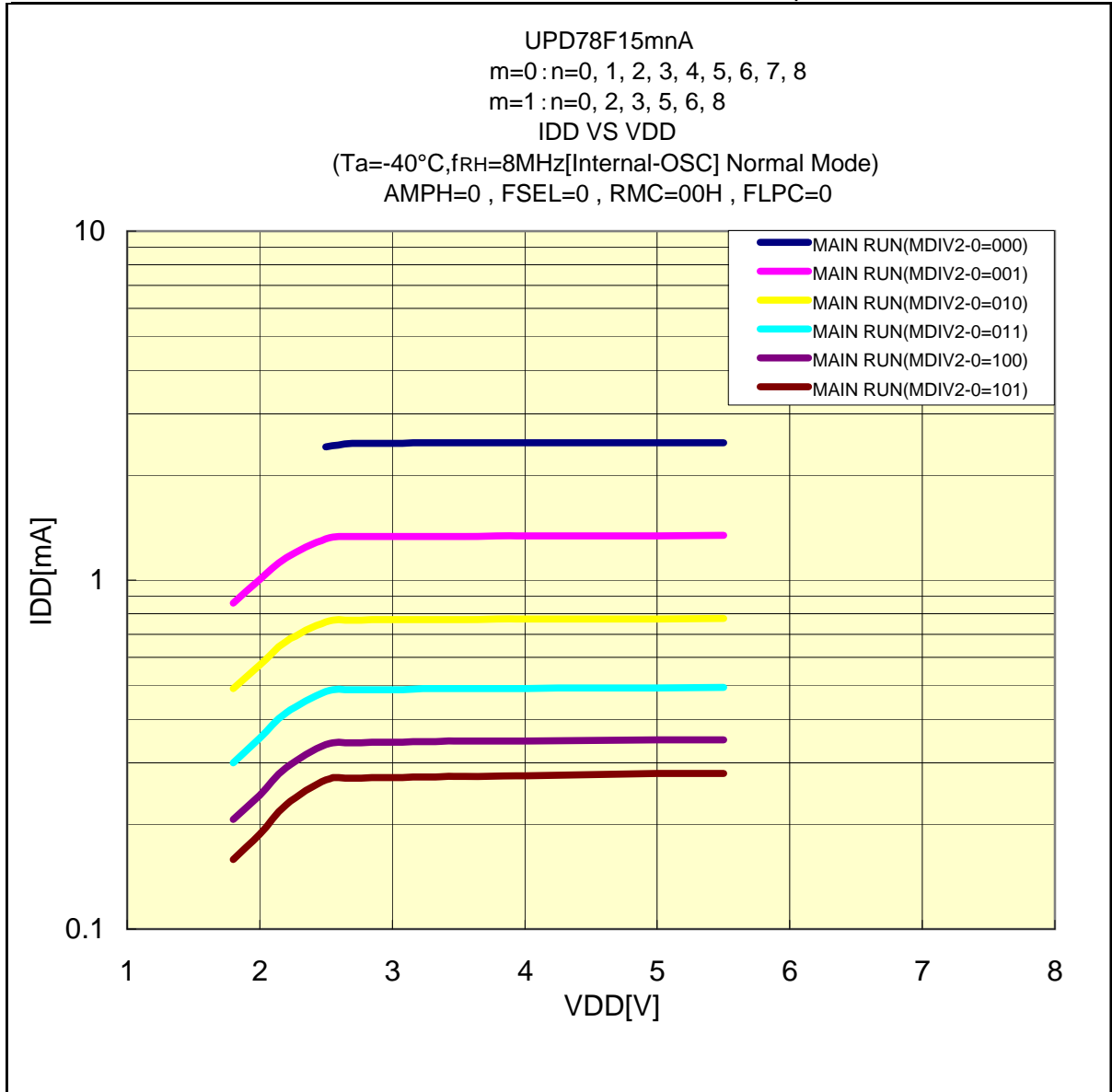
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/8MHz[Internal-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

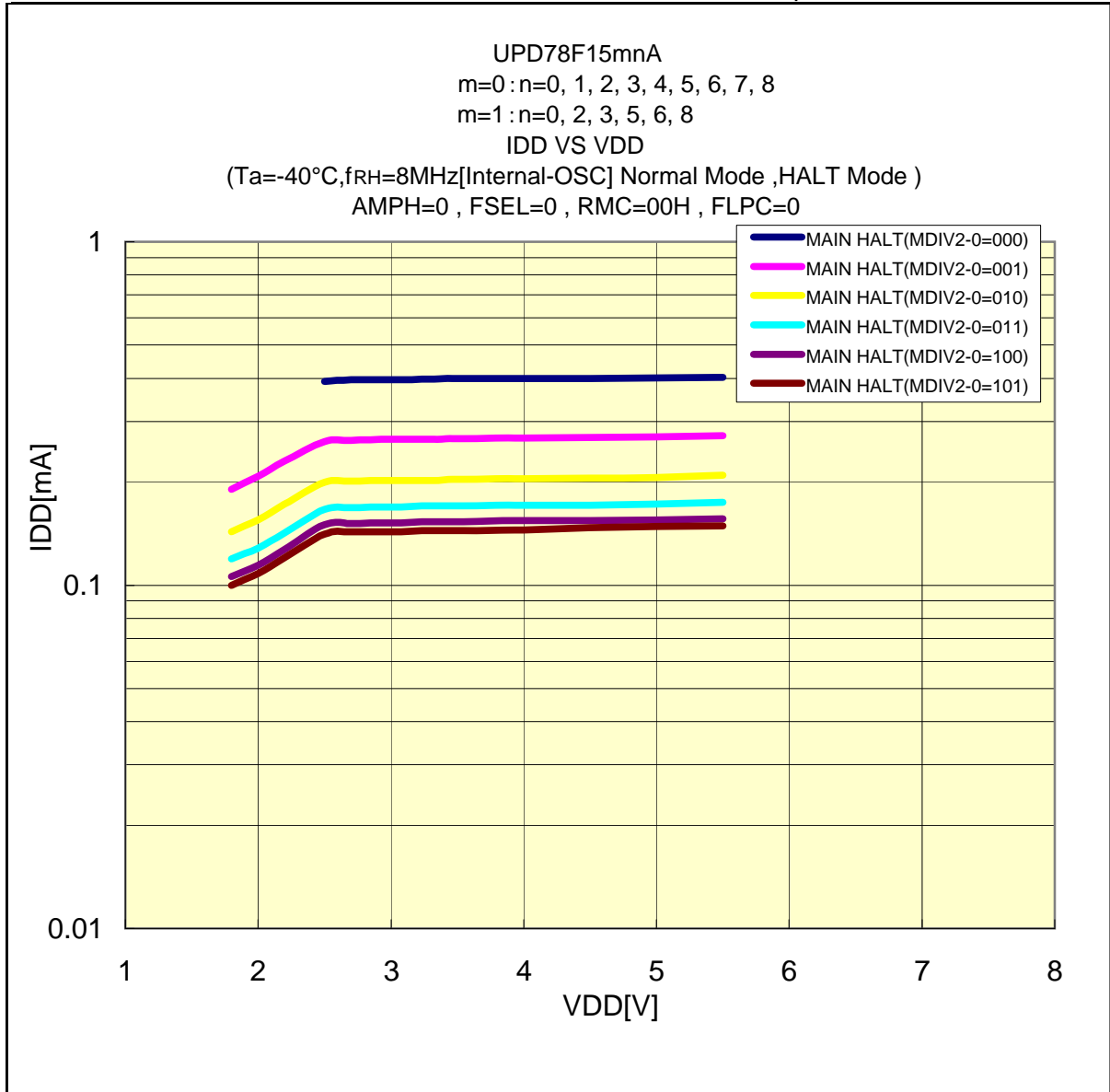
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/8MHz[Internal-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

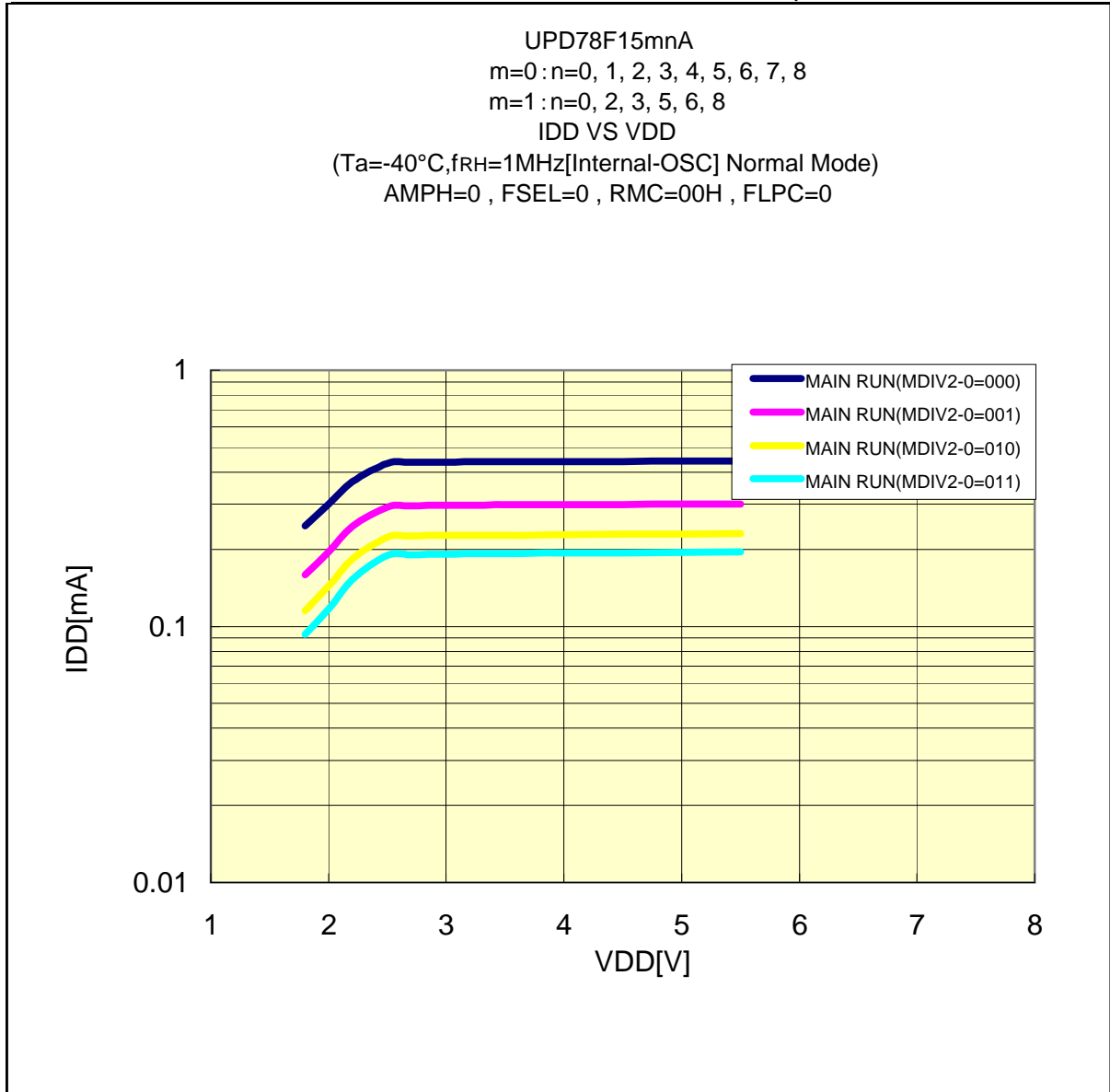
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/1MHz[Internal-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



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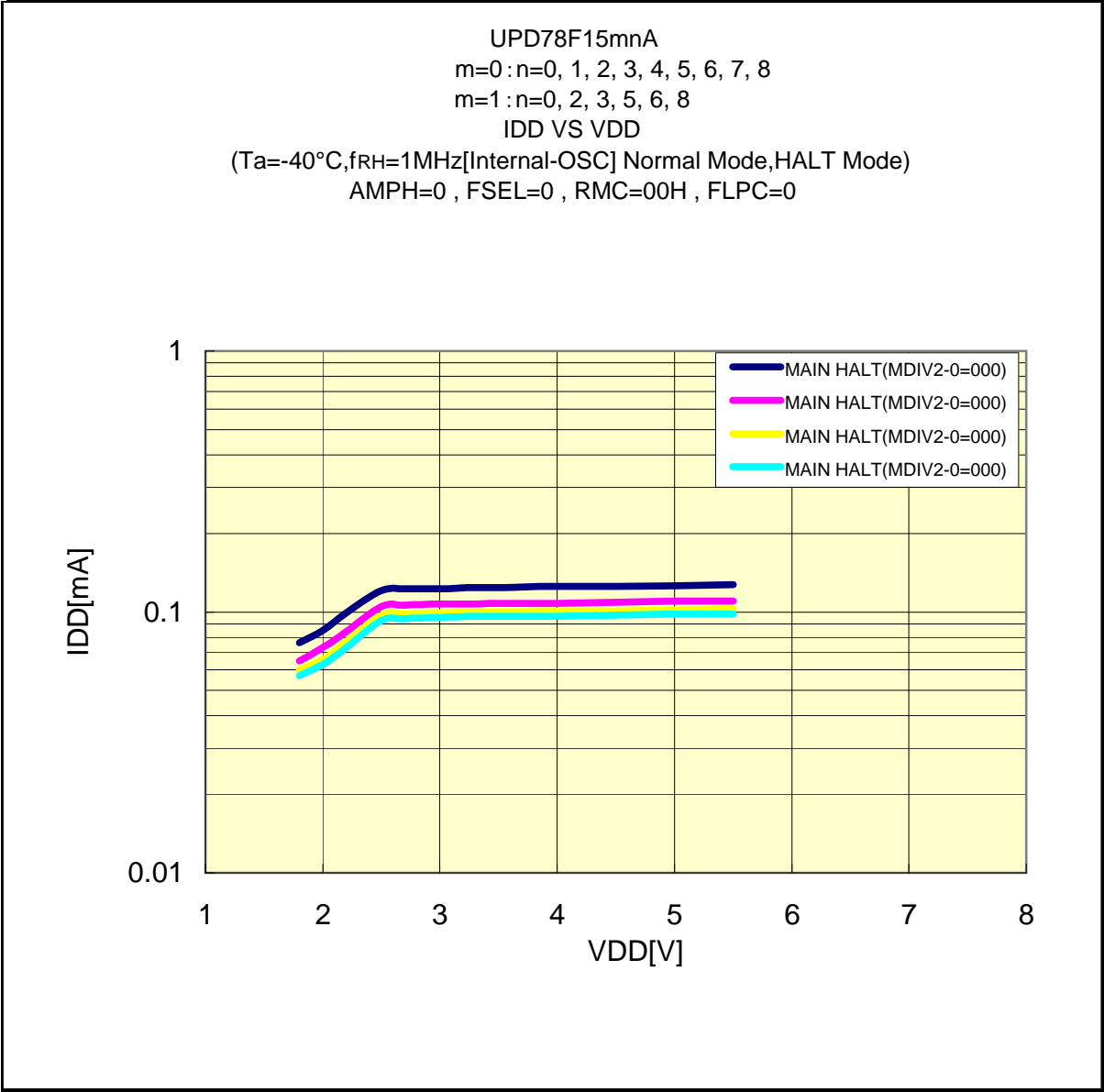
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(-40°C/1MHz[Internal-OSC])
Normal Power Mode(HALT)**

Prepared on Oct. 11th, 2011



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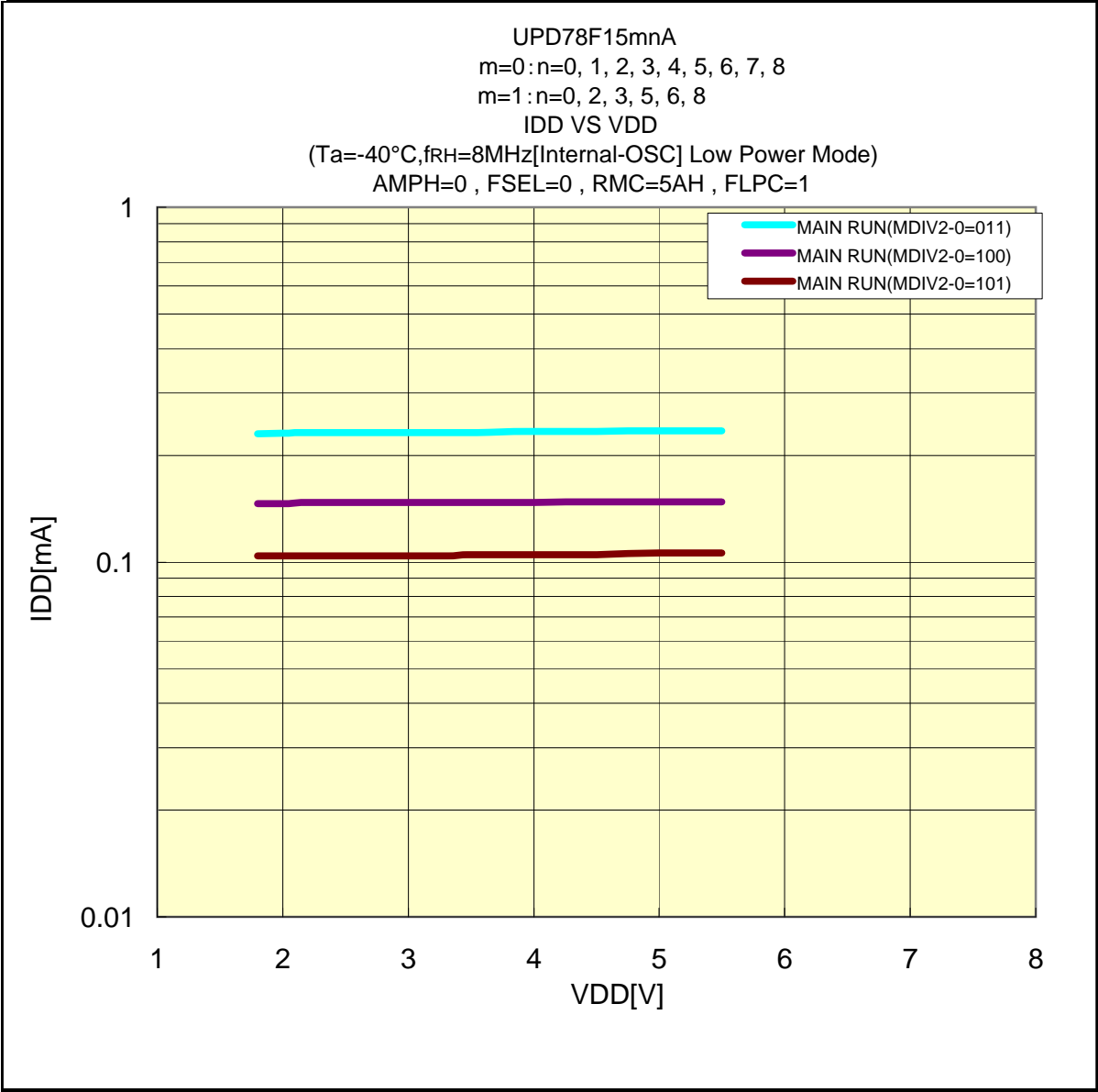
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(-40°C/8MHz[Internal-OSC])
Low Power Mode**

Prepared on Oct. 11th, 2011



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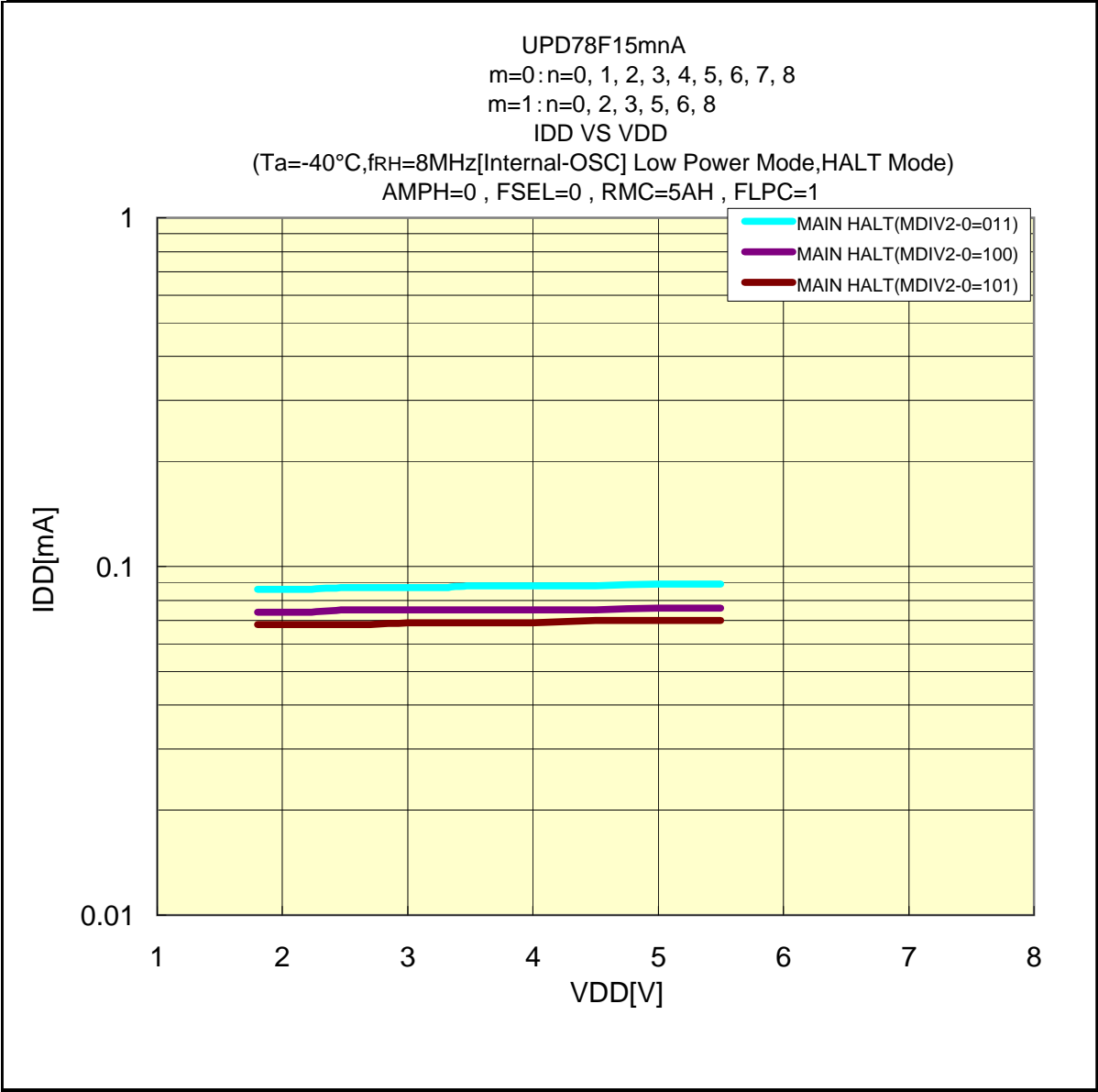
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(-40°C/8MHz[Internal-OSC])
Low Power Mode(HALT)**

Prepared on Oct. 11th, 2011



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UPD78F15mA

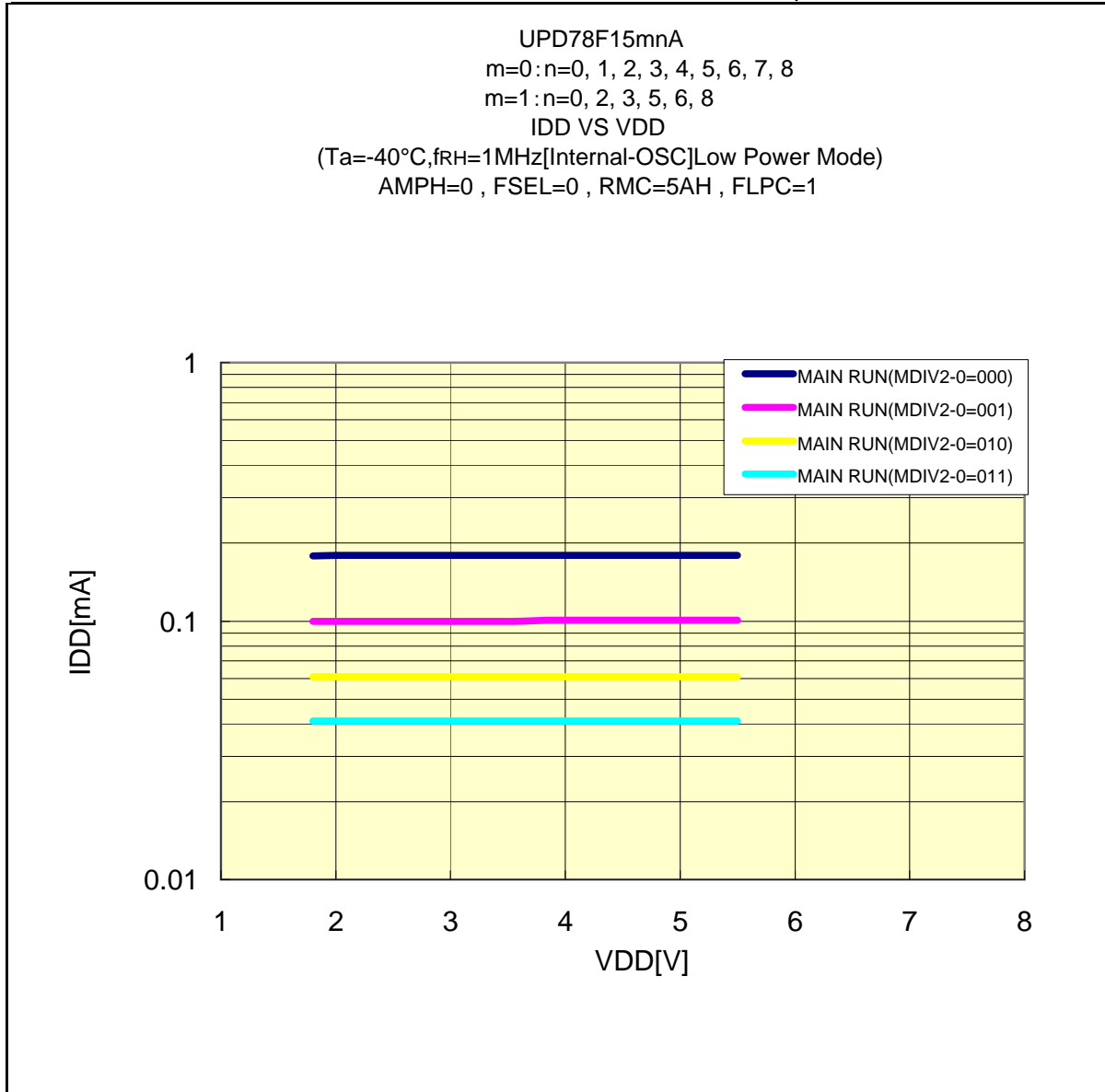
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/1MHz[Internal-OSC])

Low Power Mode

Prepared on Oct. 11th, 2011



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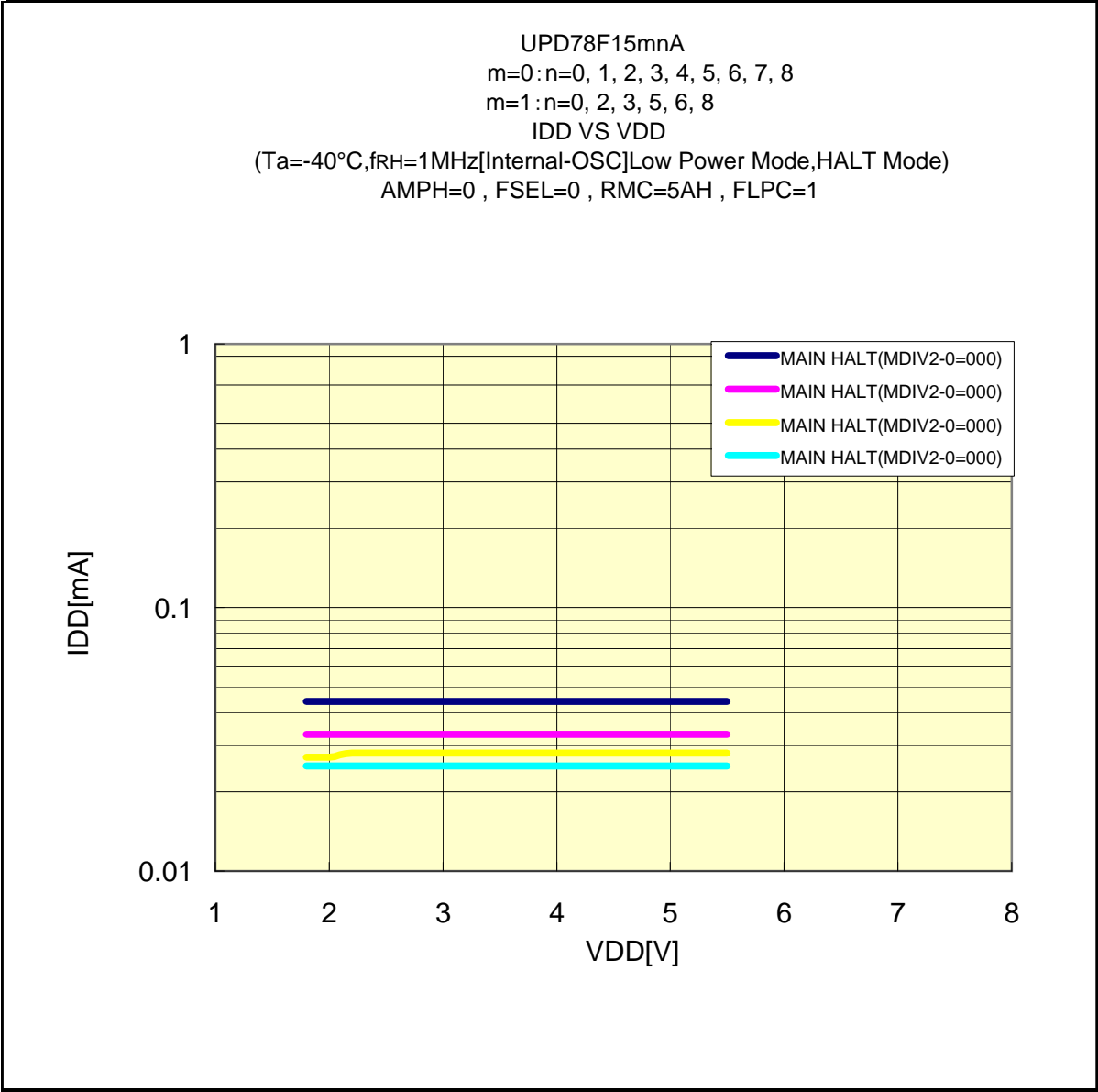
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(-40°C/1MHz[Internal-OSC])
Low Power Mode(HALT)**

Prepared on Oct. 11th, 2011



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UPD78F15mA

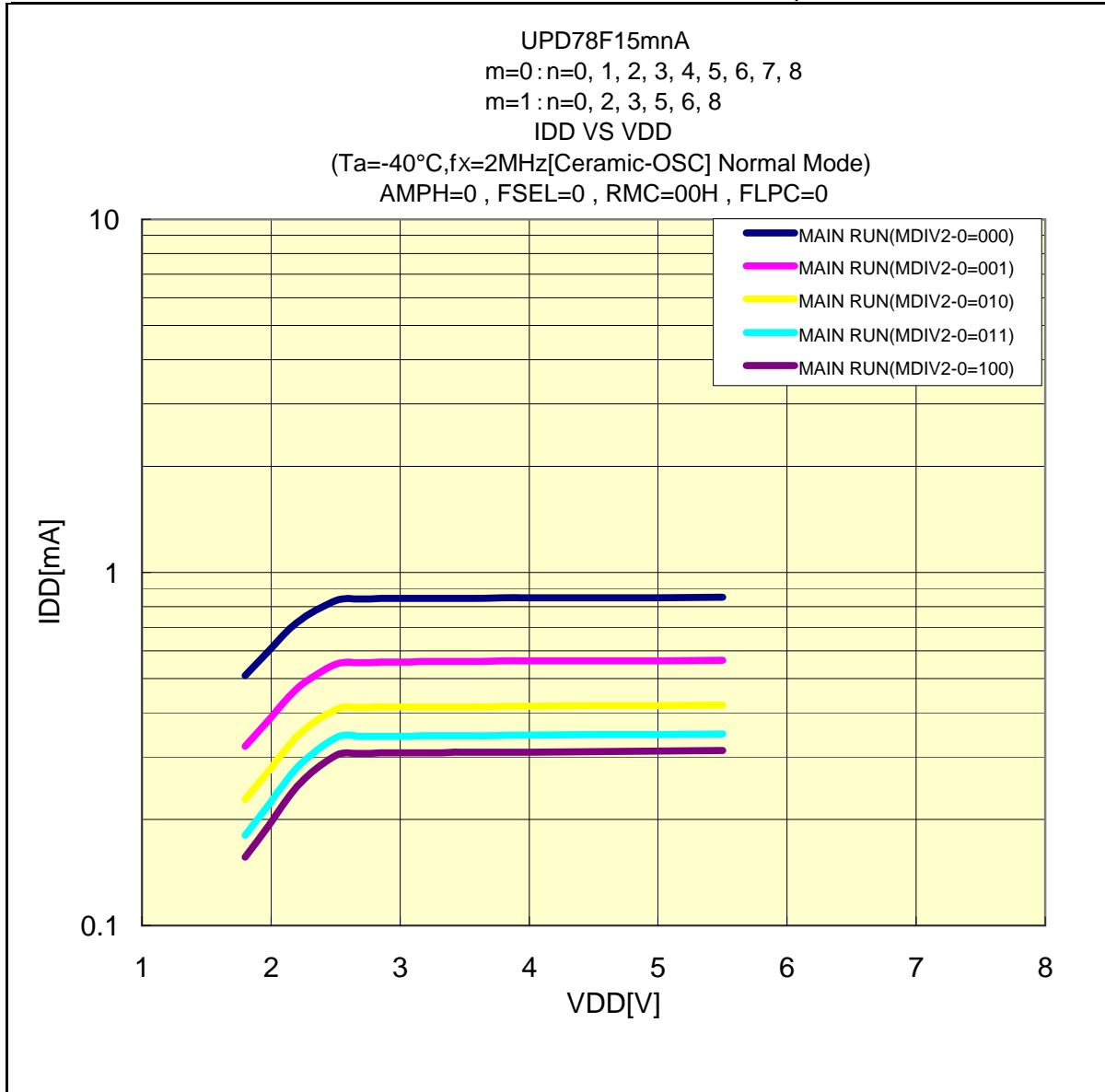
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/2MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



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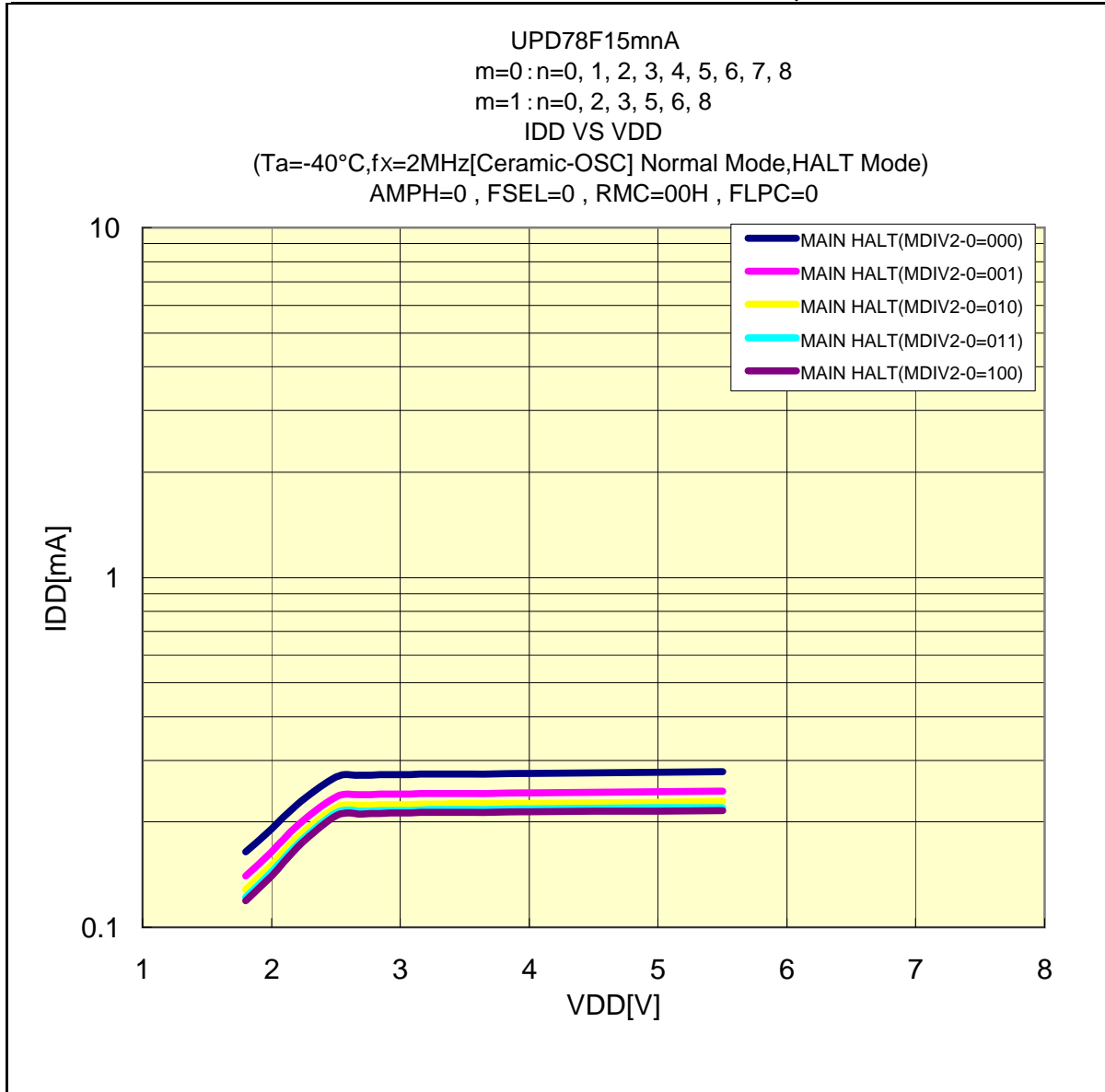
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/2MHzCeramic-OSC] Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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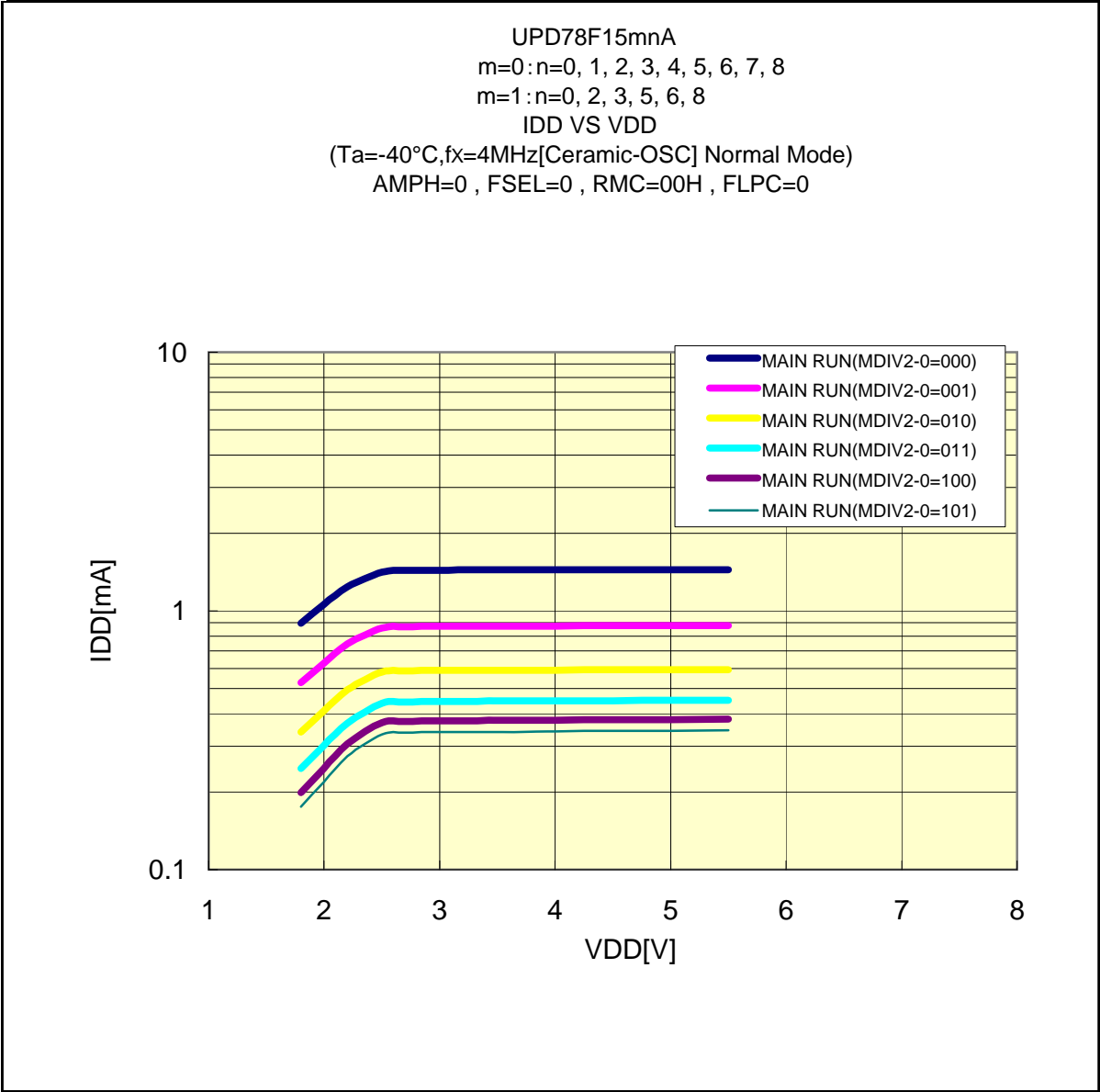
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/4MHzCeramic-OSC] Normal Power Mode

Prepared on Oct. 11th, 2011



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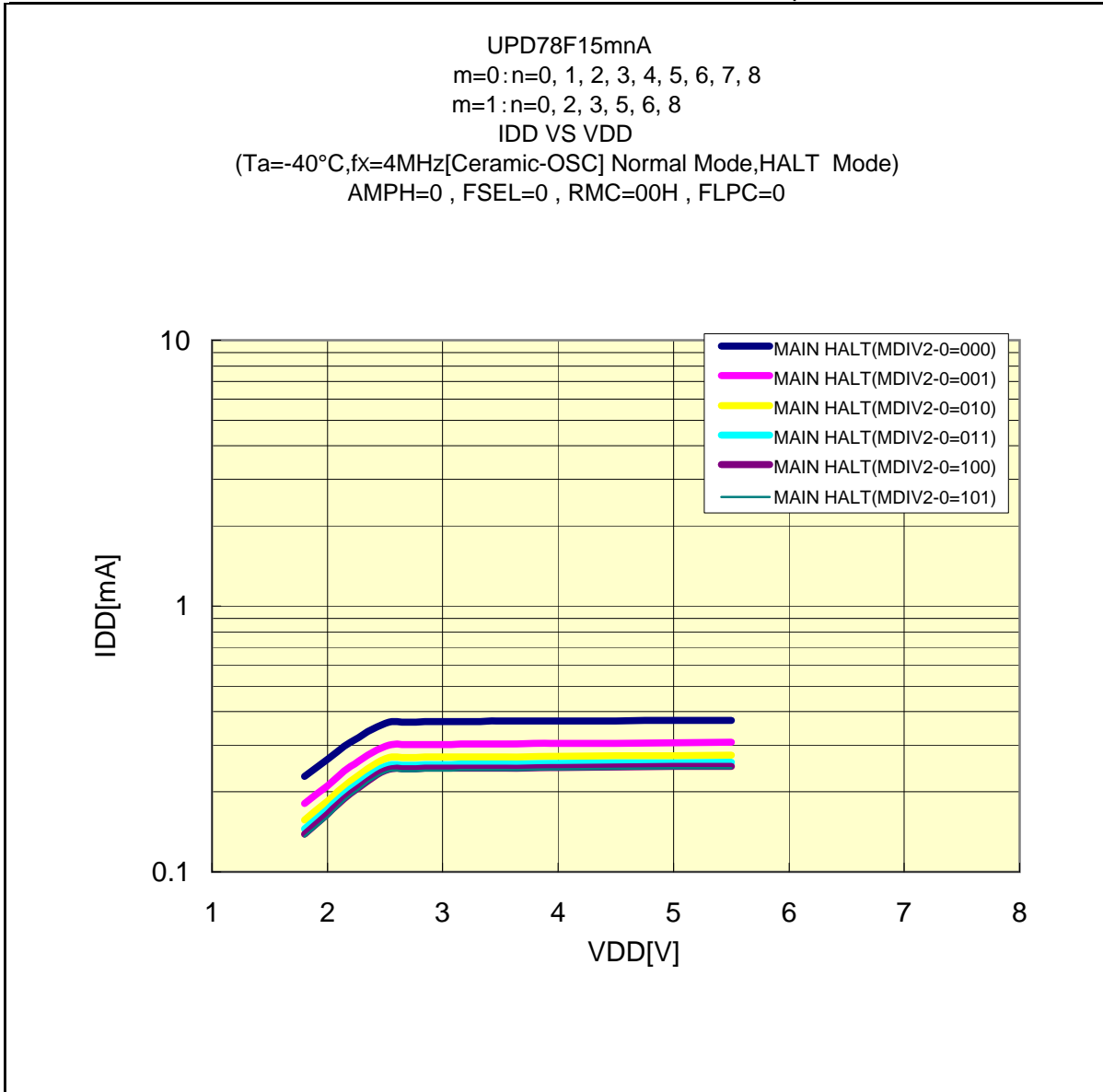
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/4MHzCeramic-OSC] Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

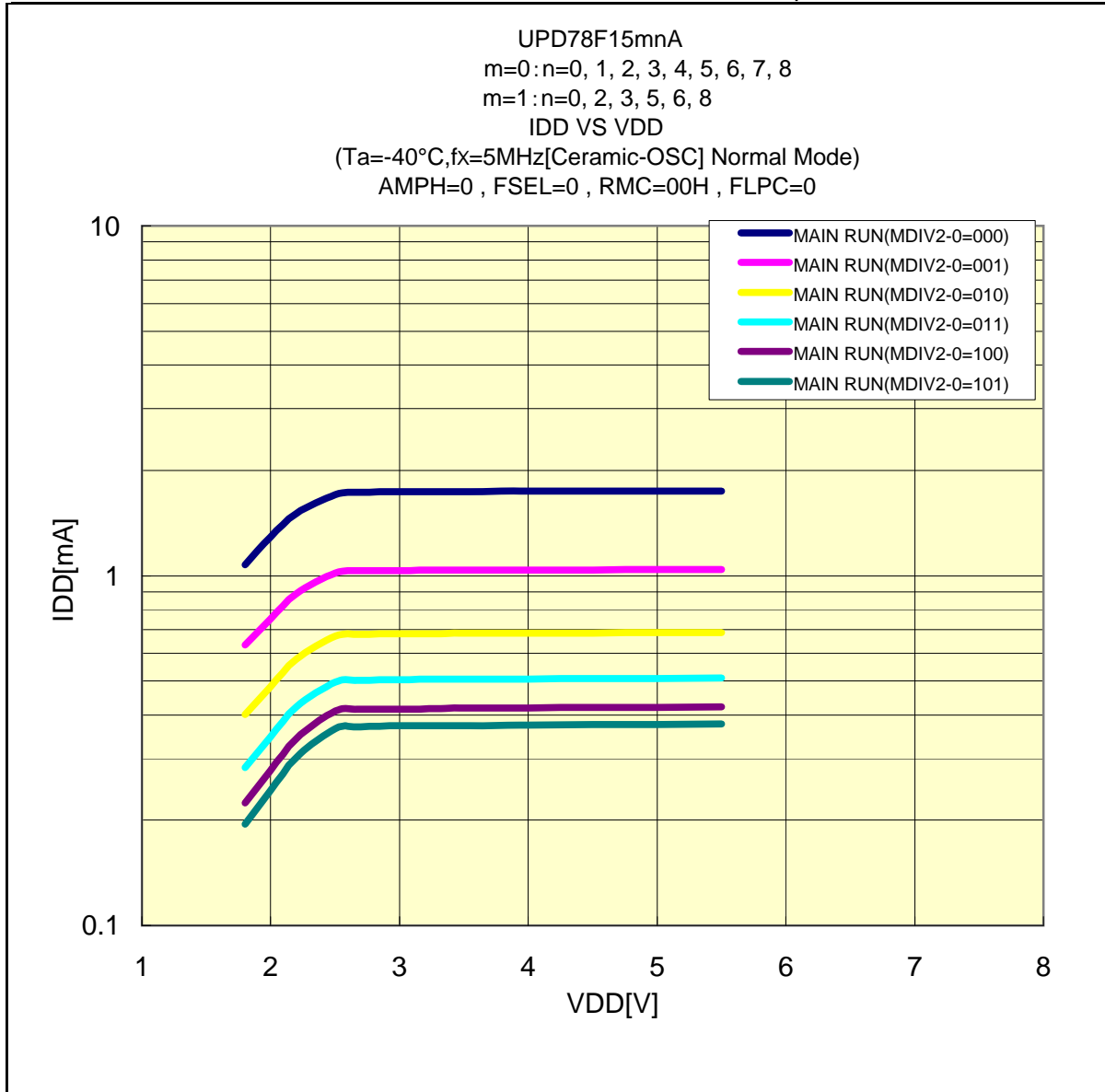
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/5MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



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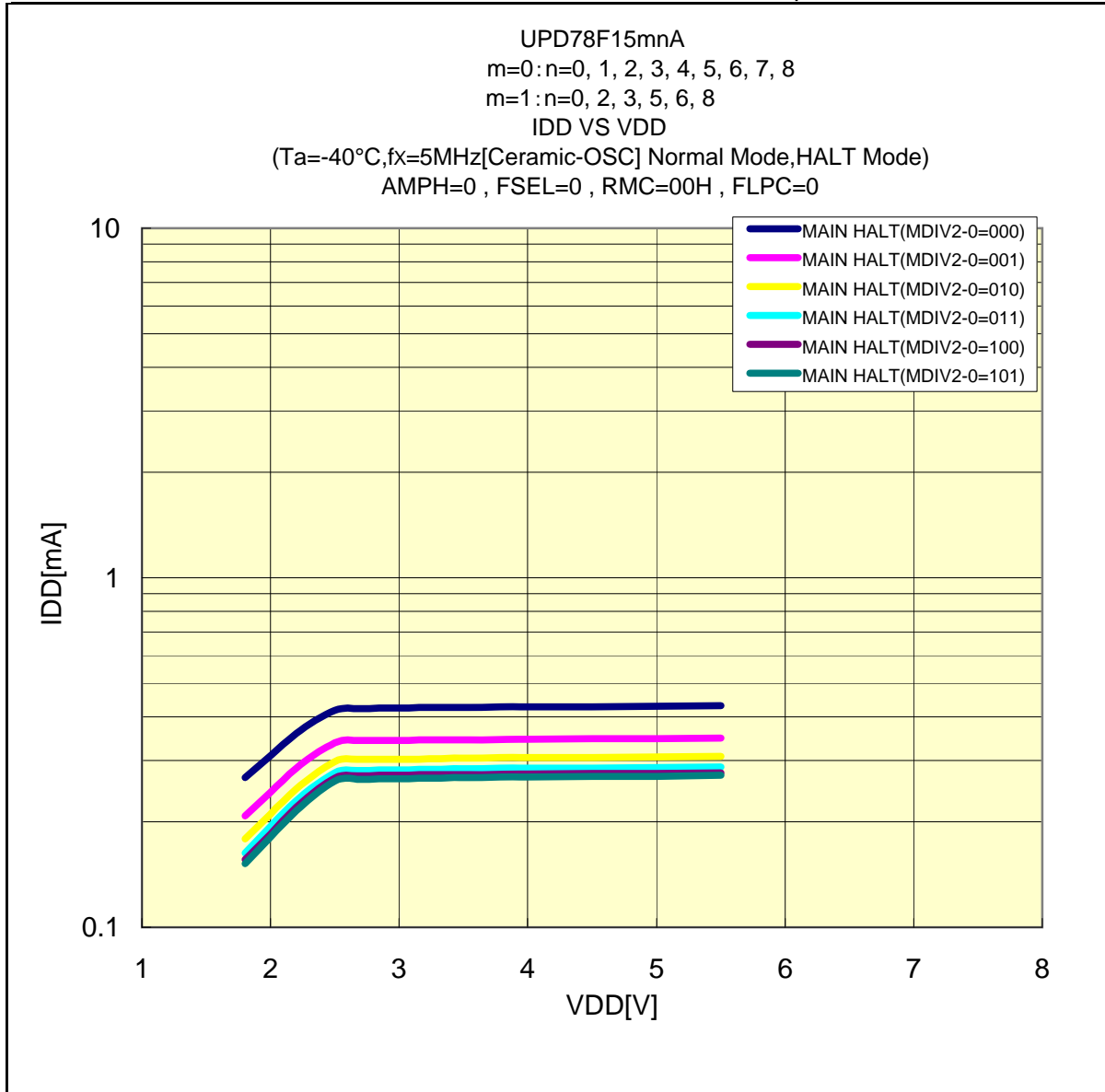
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/5MHzCeramic-OSC] Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

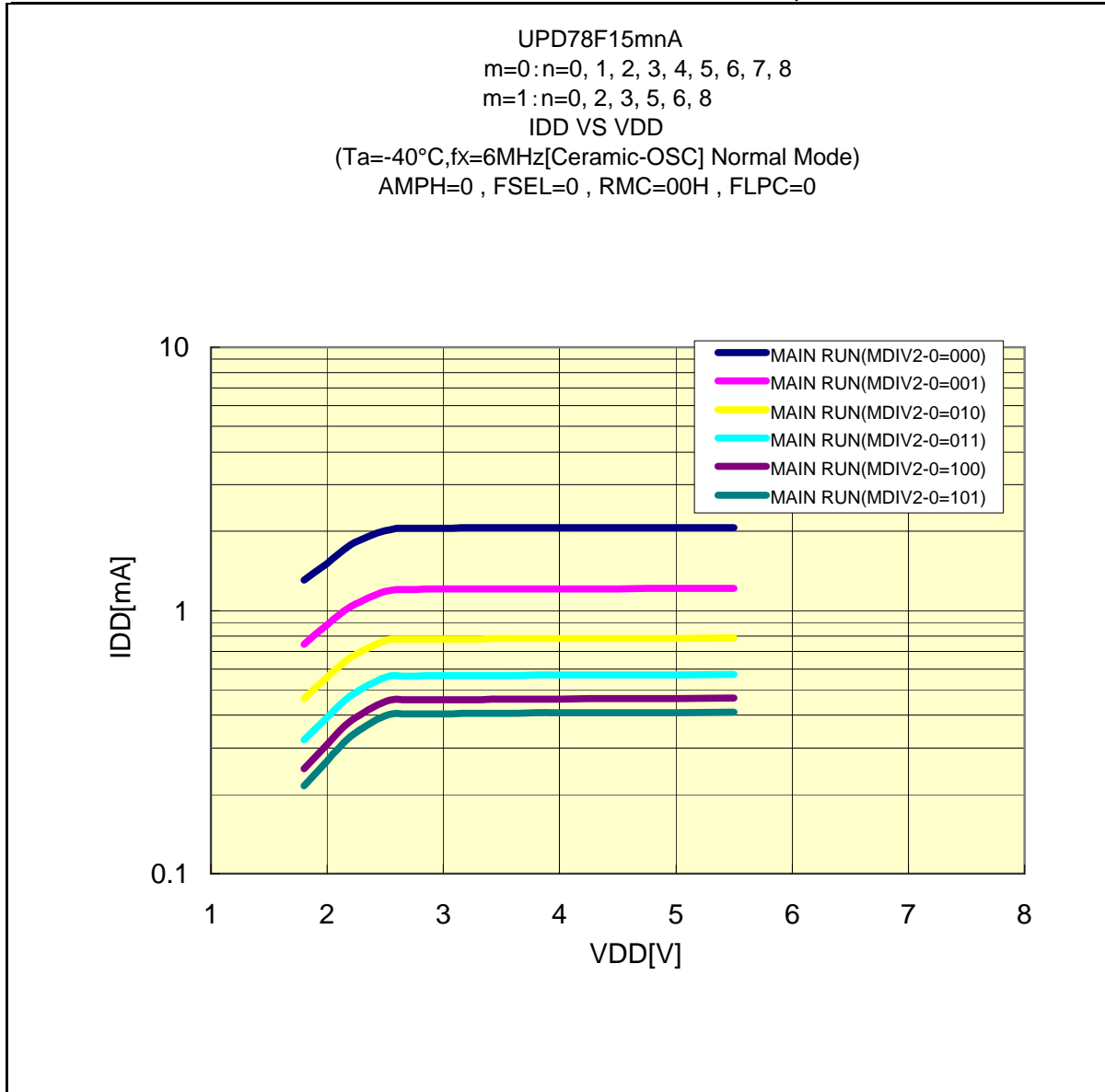
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/6MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



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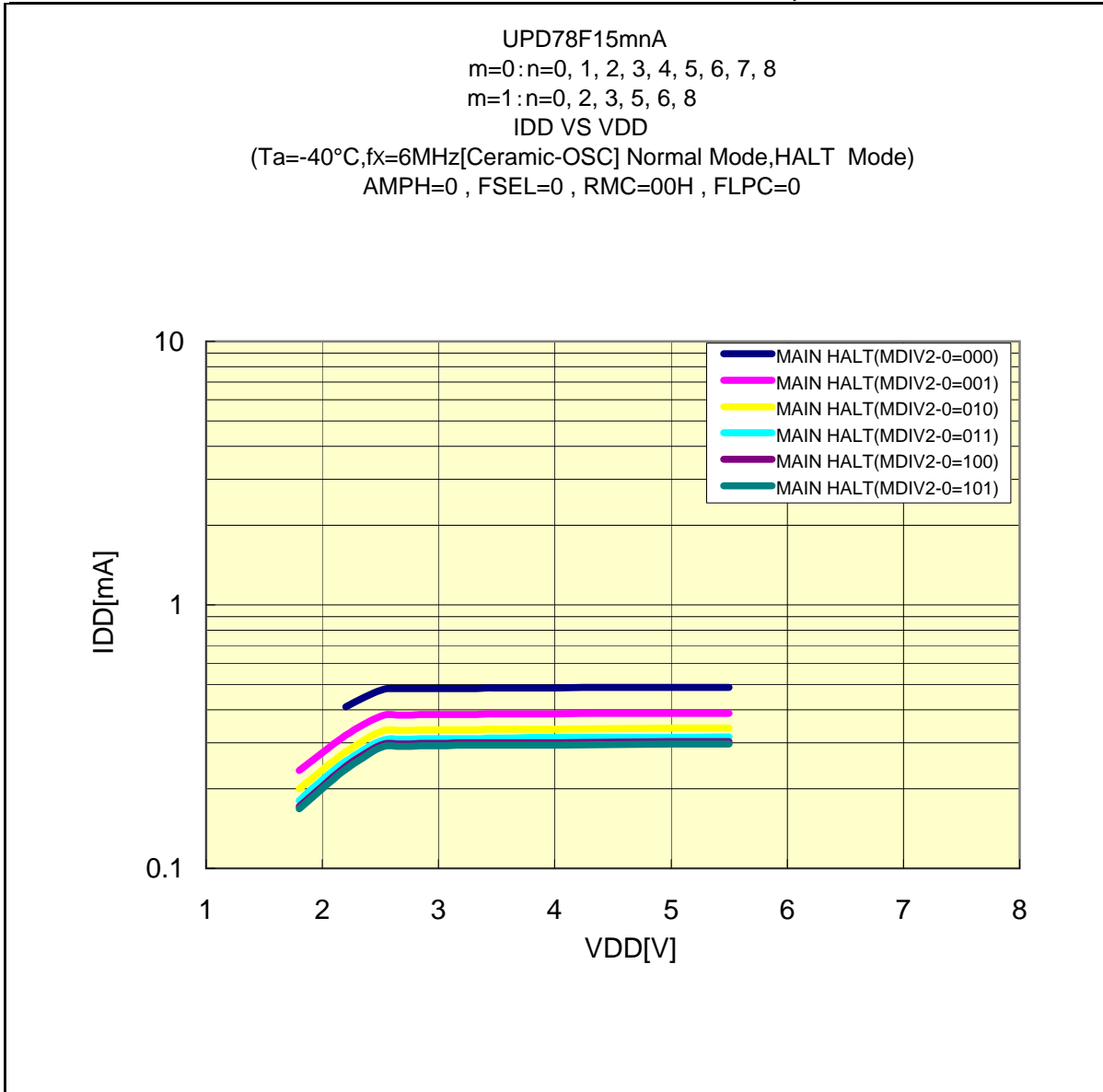
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/6MHzCeramic-OSC] Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

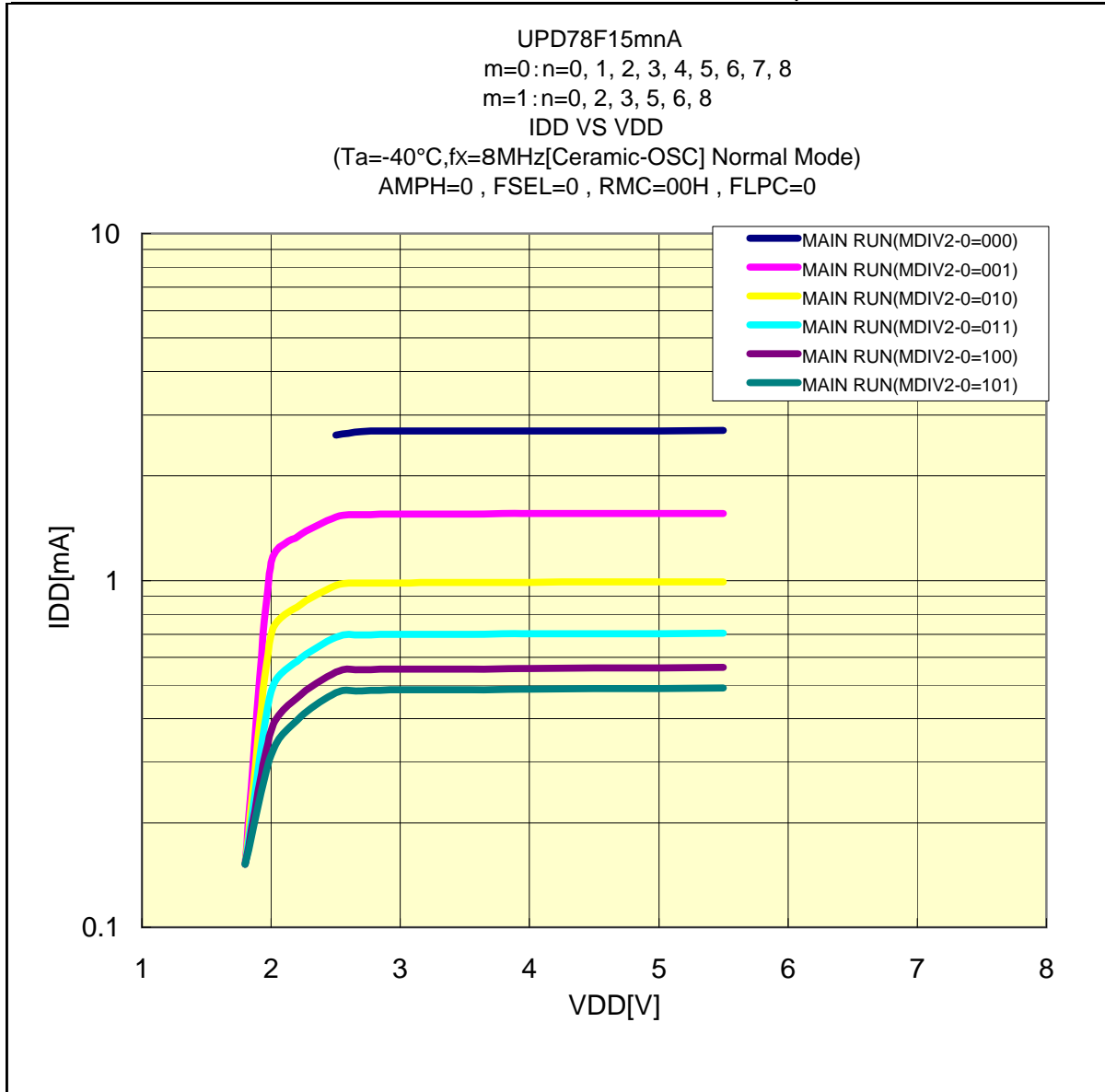
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/8MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



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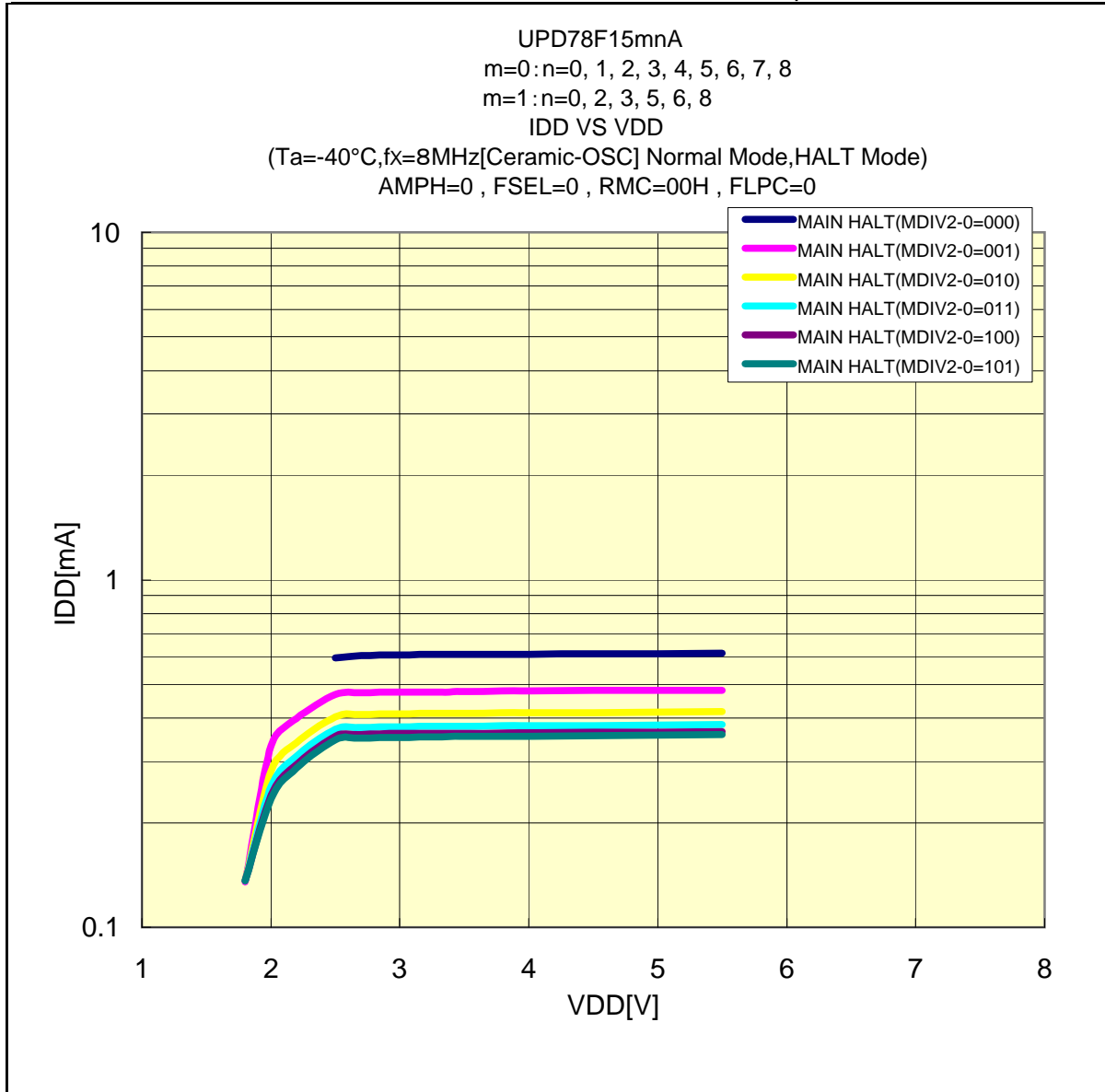
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/8MHzCeramic-OSC] Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

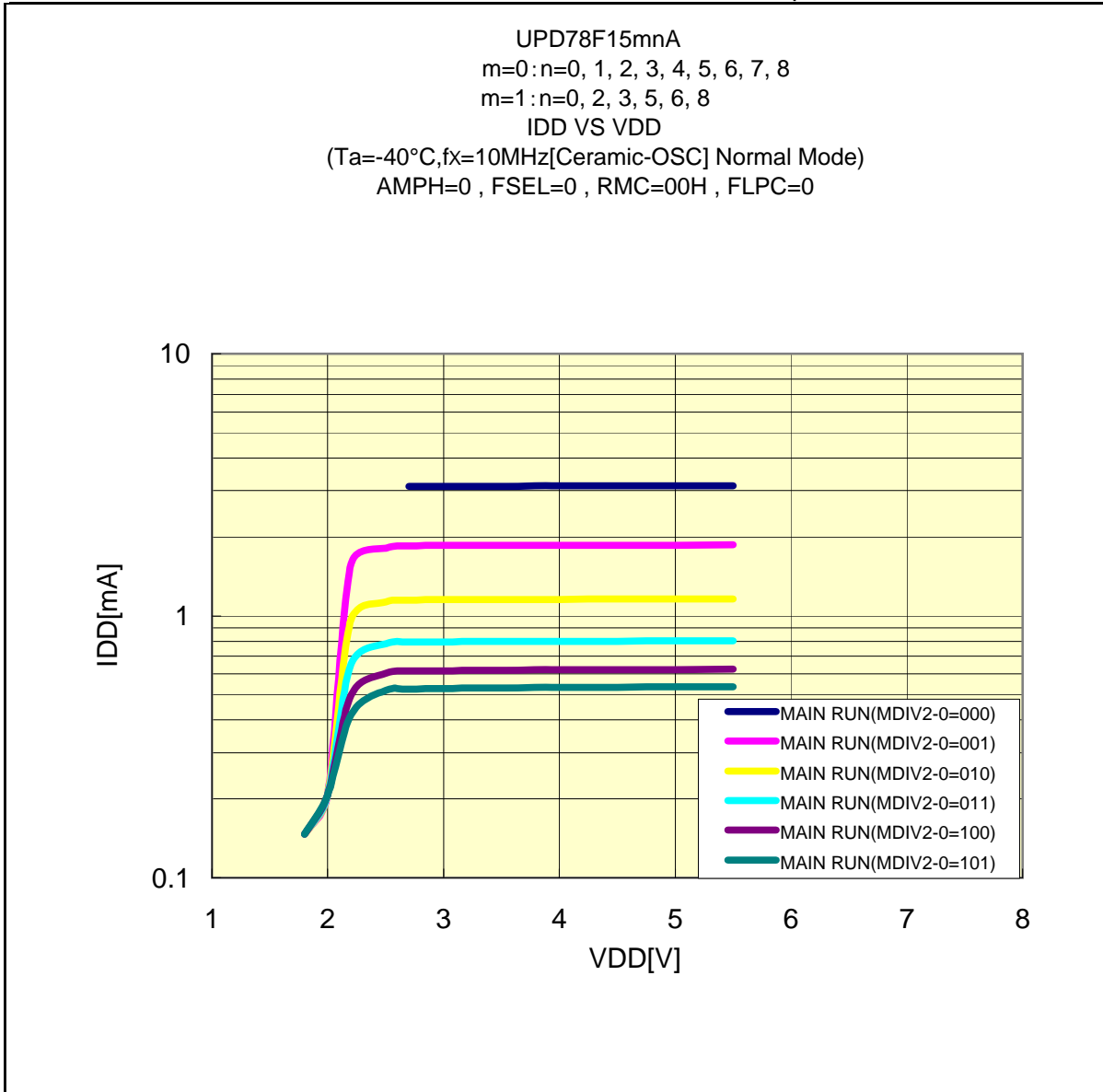
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/10MHzCeramic-OSC)

Normal Power Mode

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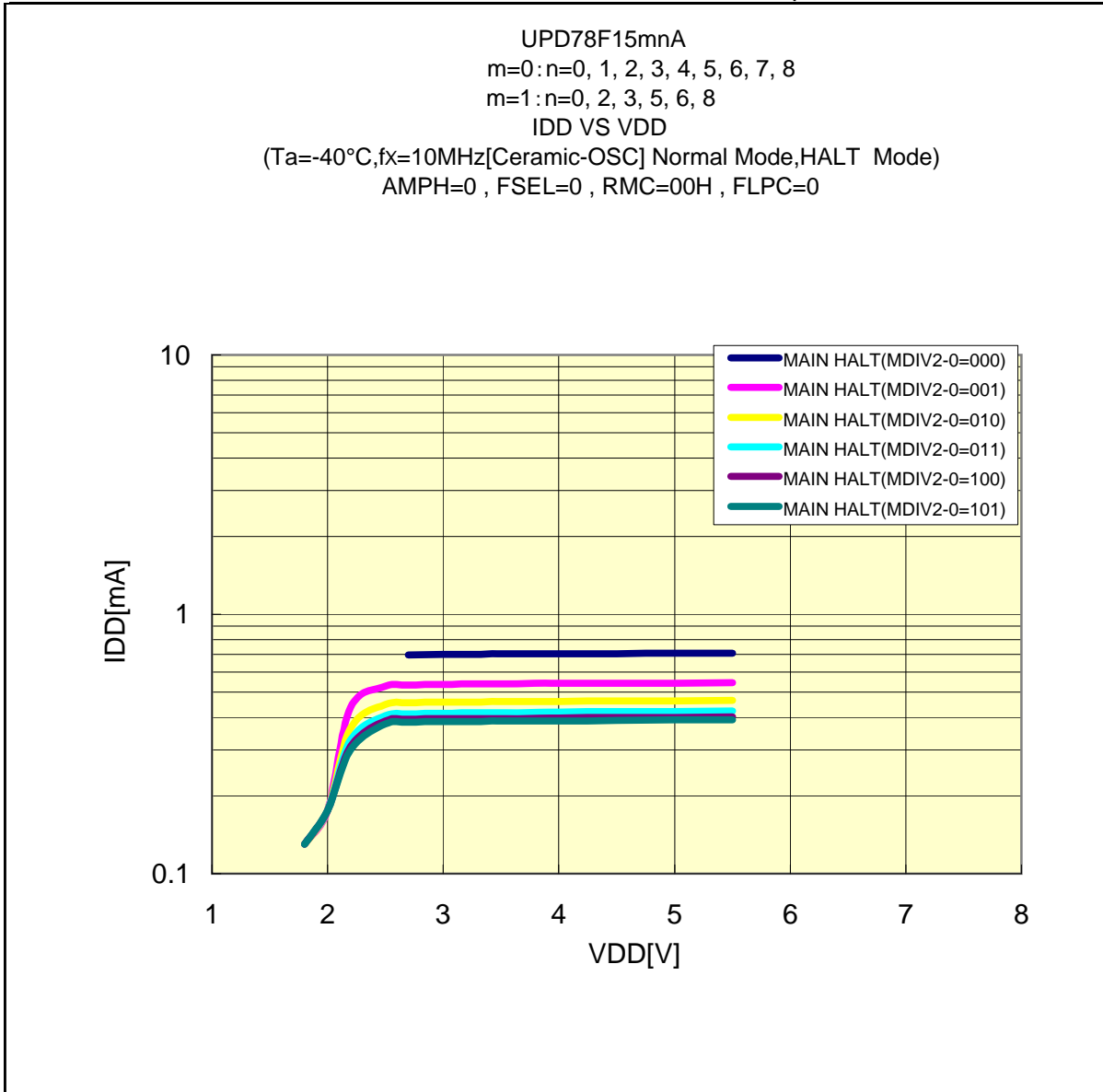
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/10MHzCeramic-OSC) Normal Power Mode(HALT)

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UPD78F15mA

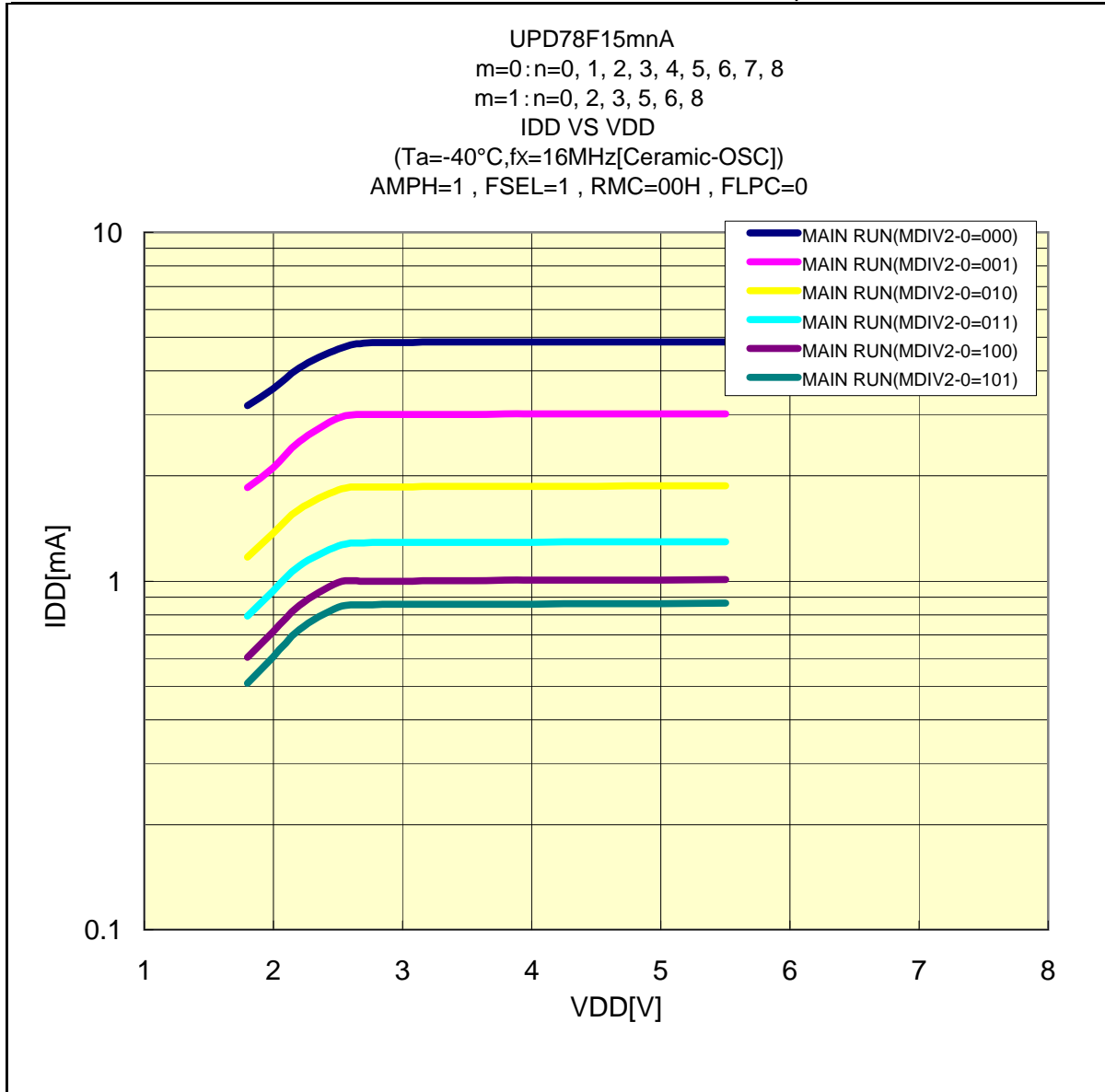
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/16MHzCeramic-OSC)

AMPH=1,FSEL=1

Prepared on Oct. 11th, 2011



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UPD78F15mA

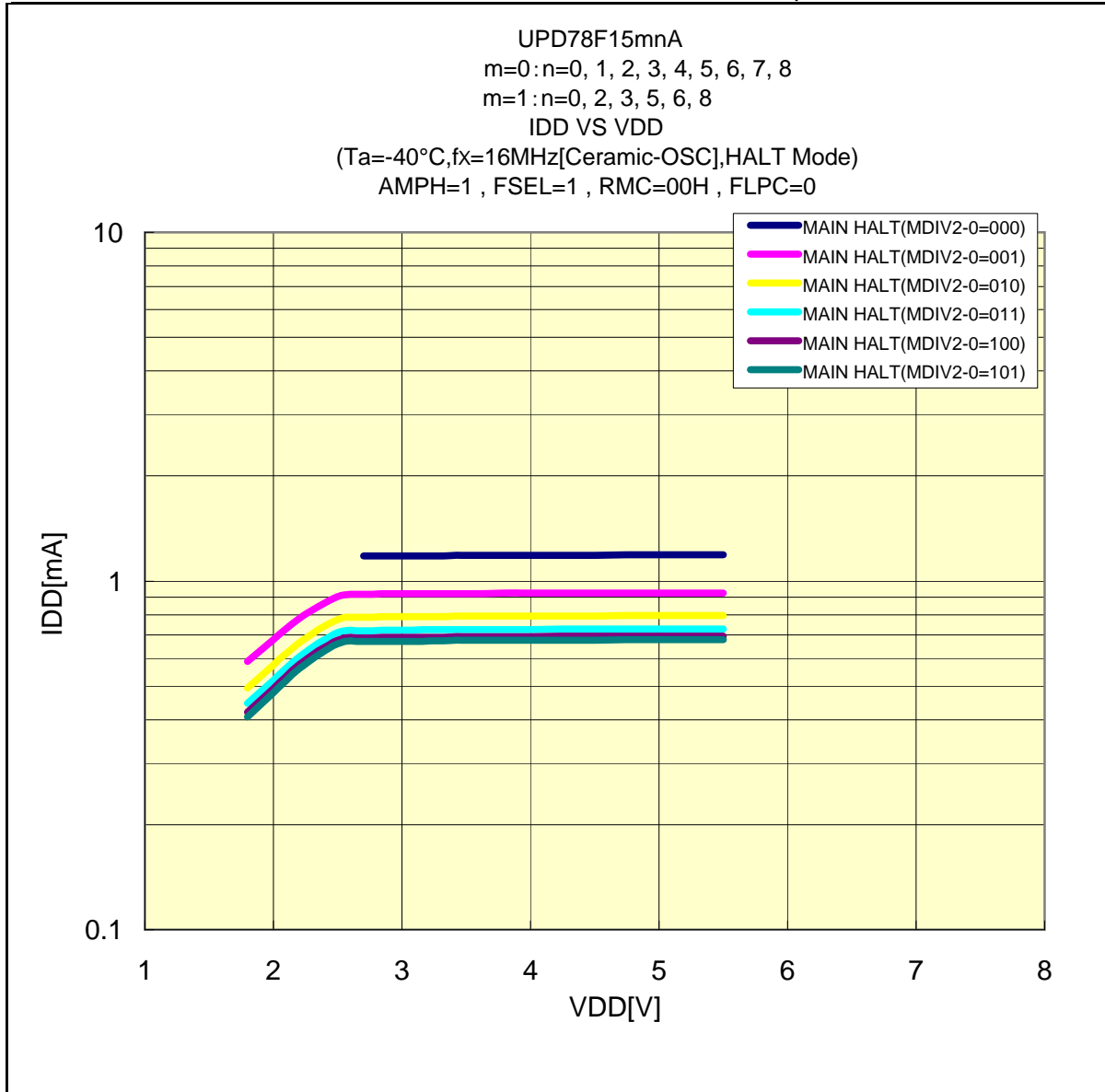
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/16MHzCeramic-OSC)

AMPH=1,FSEL=1(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

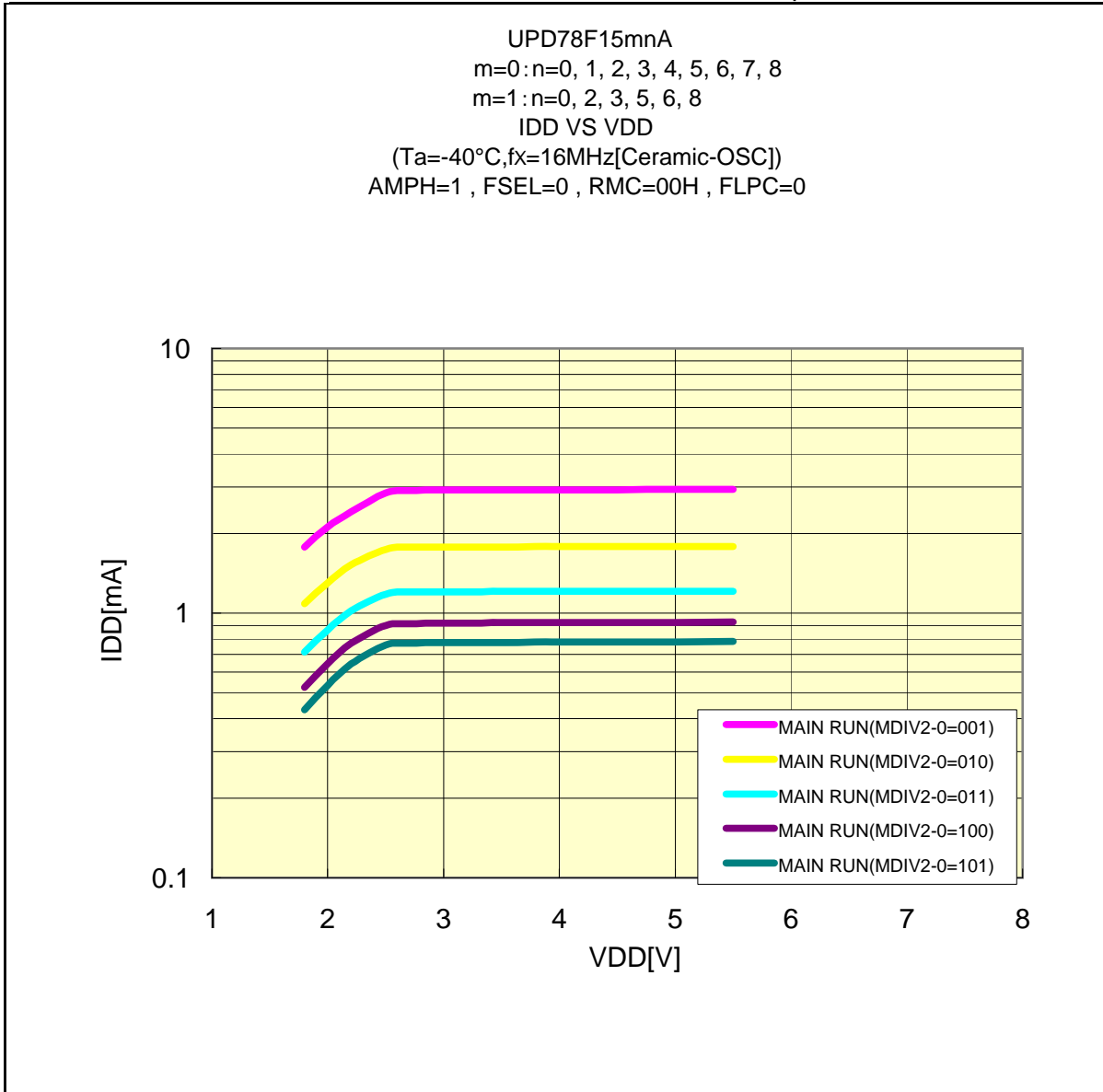
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/16MHzCeramic-OSC)

AMPH=1,FSEL=0

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UPD78F15mA

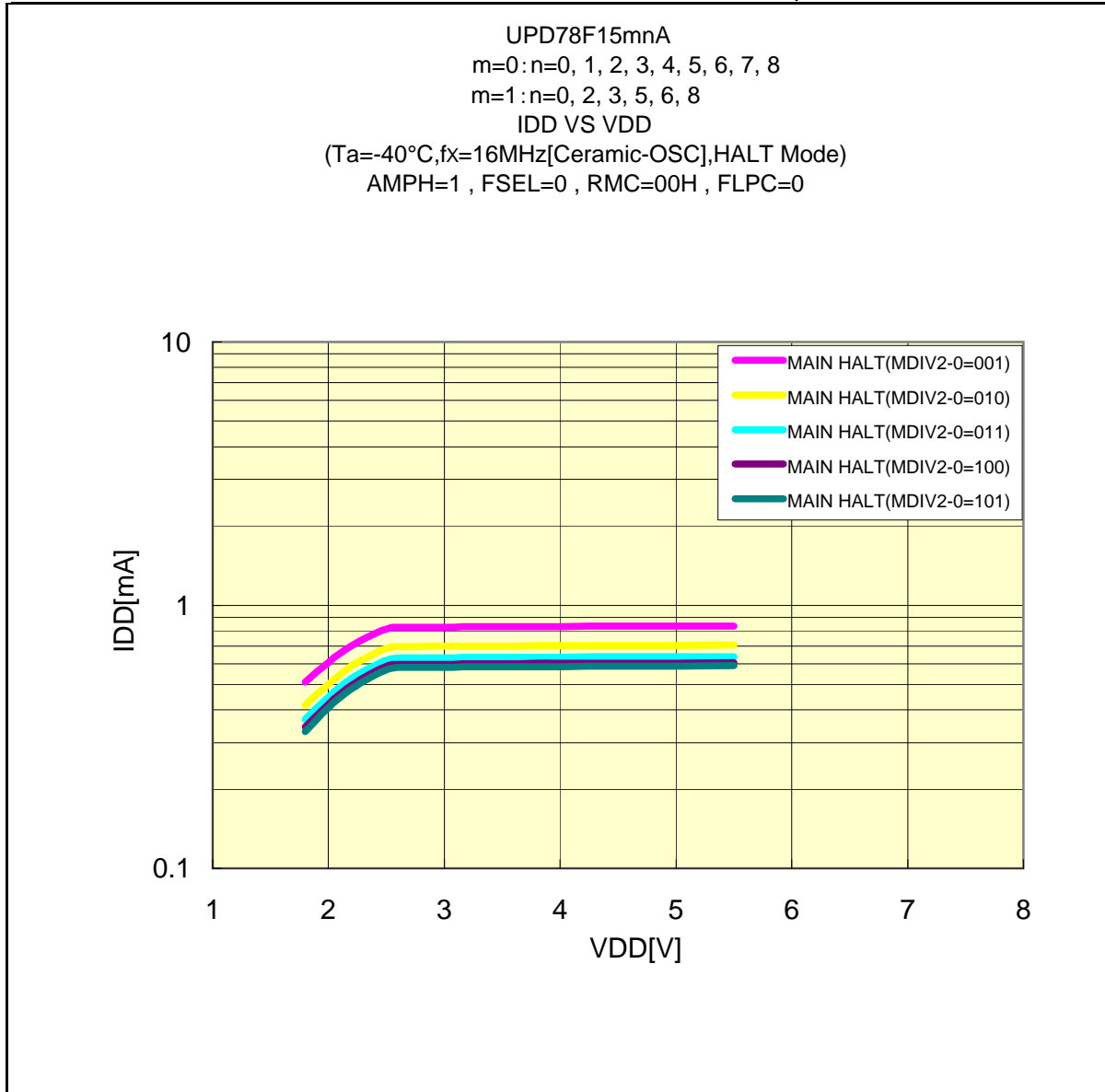
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/16MHzCeramic-OSC)

AMPH=1,FSEL=0(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

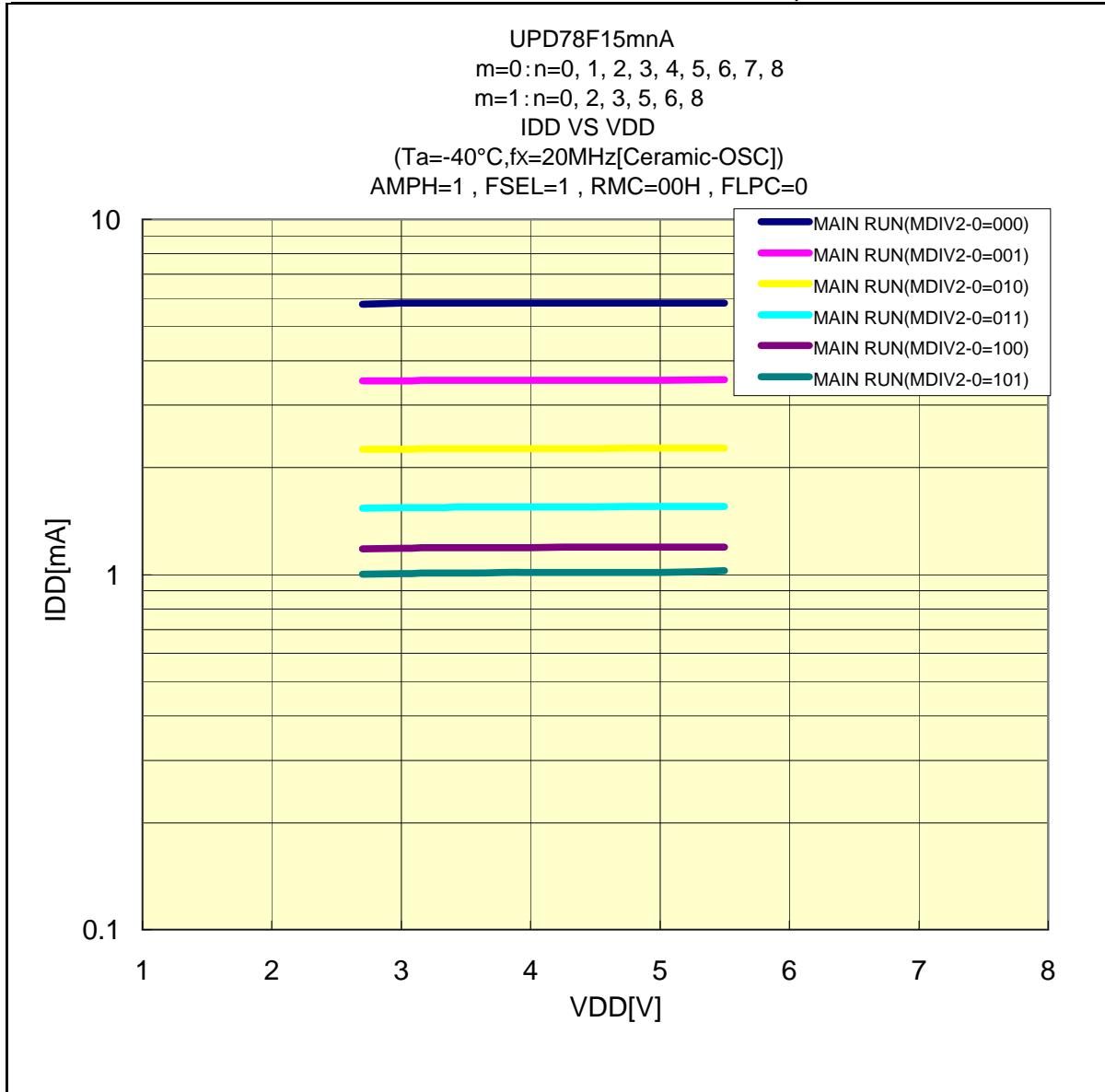
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/20MHzCeramic-OSC)

AMPH=1,FSEL=1

Prepared on Oct. 11th, 2011



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UPD78F15mA

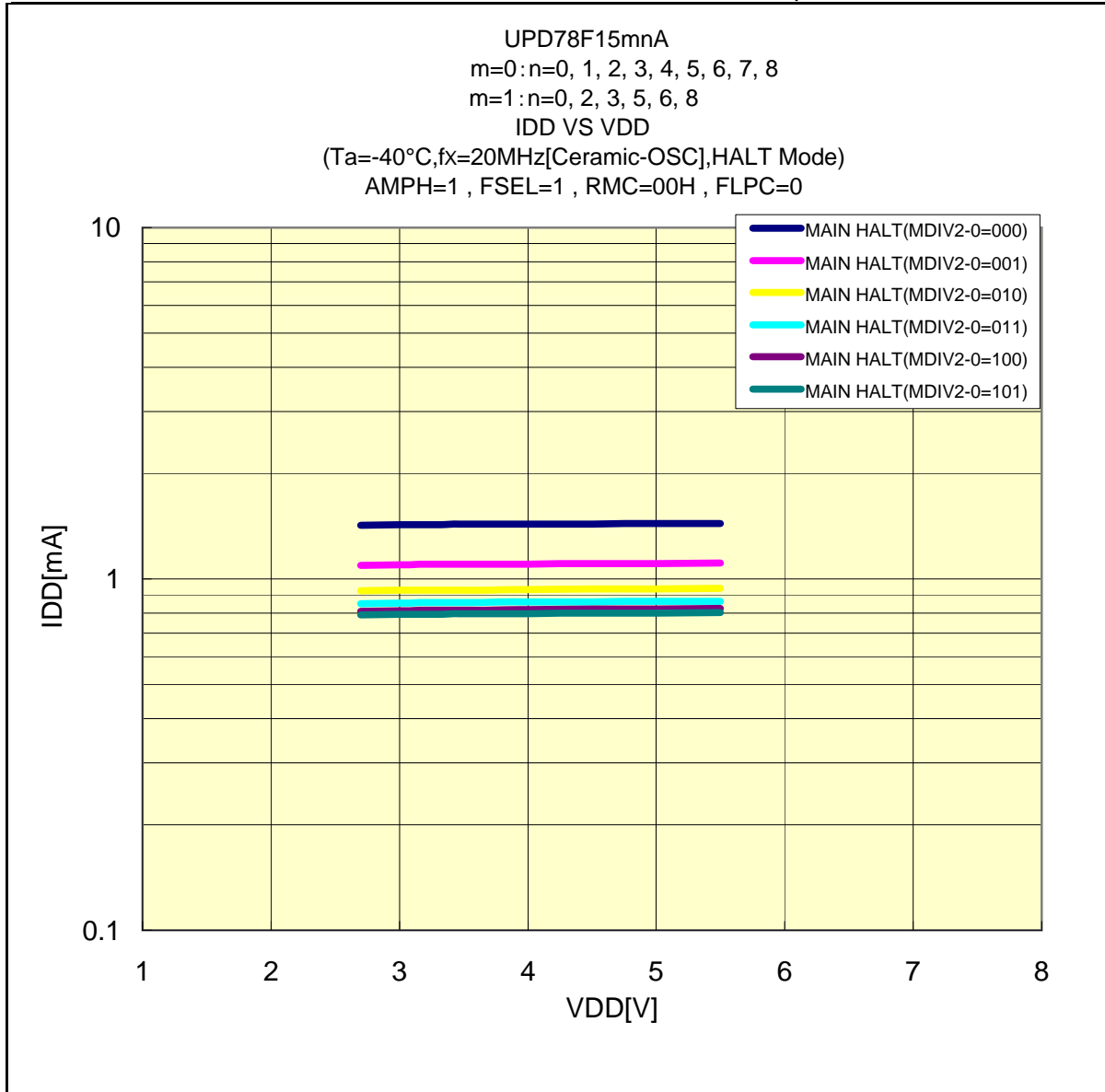
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/20MHzCeramic-OSC)

AMPH=1,FSEL=1(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

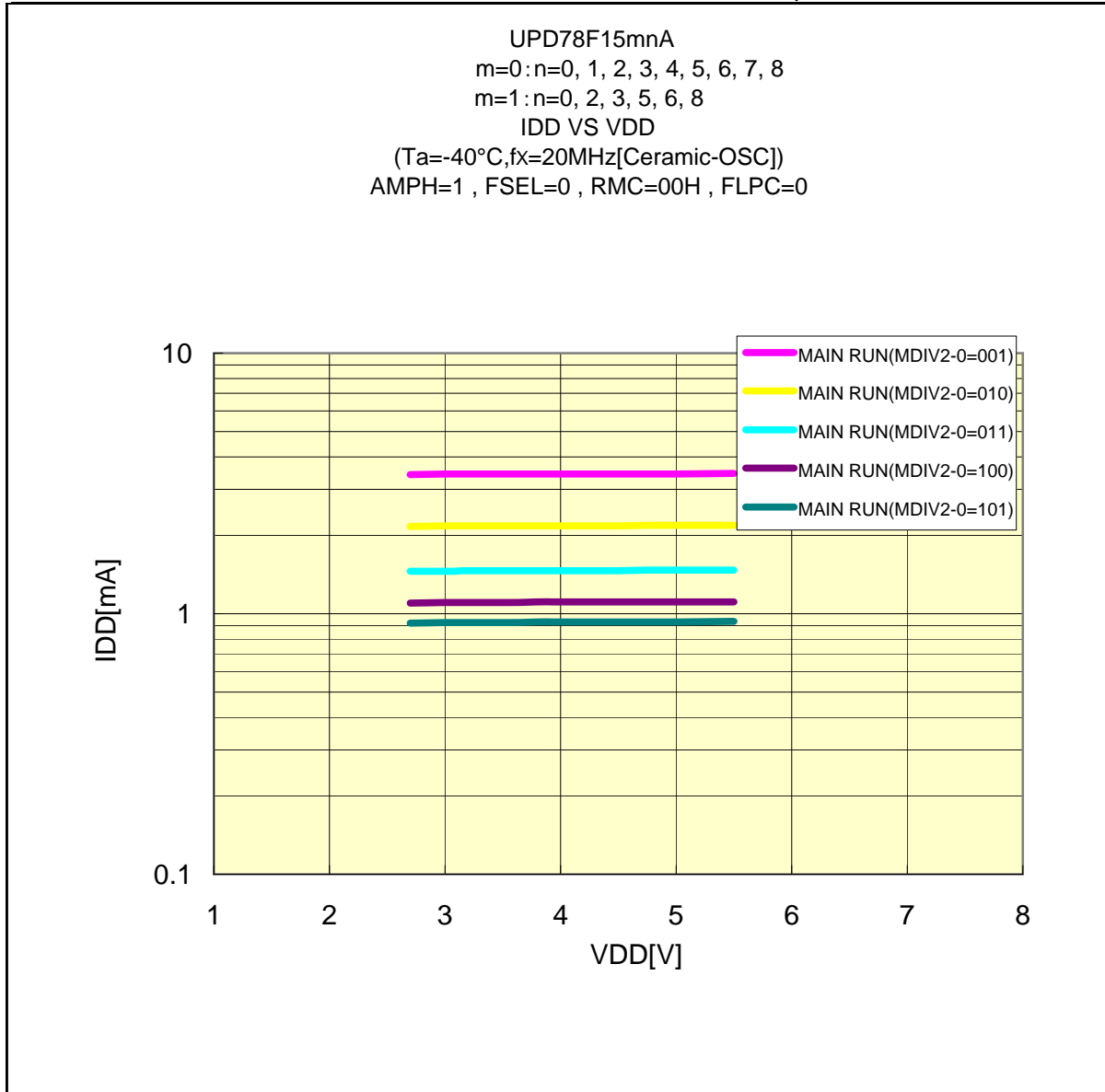
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/20MHzCeramic-OSC)

AMPH=1,FSEL=0

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UPD78F15mA

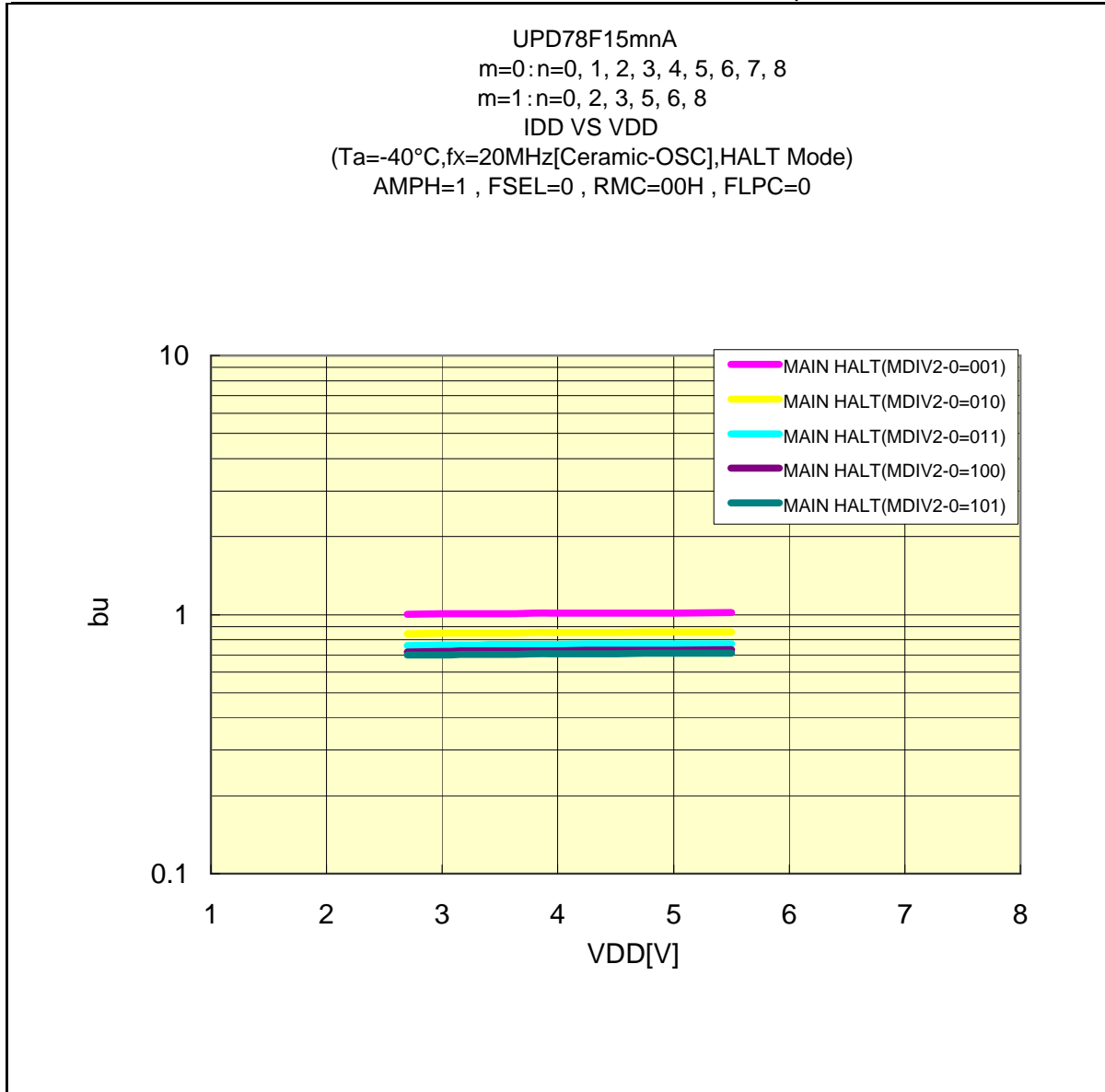
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/20MHzCeramic-OSC)

AMPH=1,FSEL=0(HALT)

Prepared on Oct. 11th, 2011



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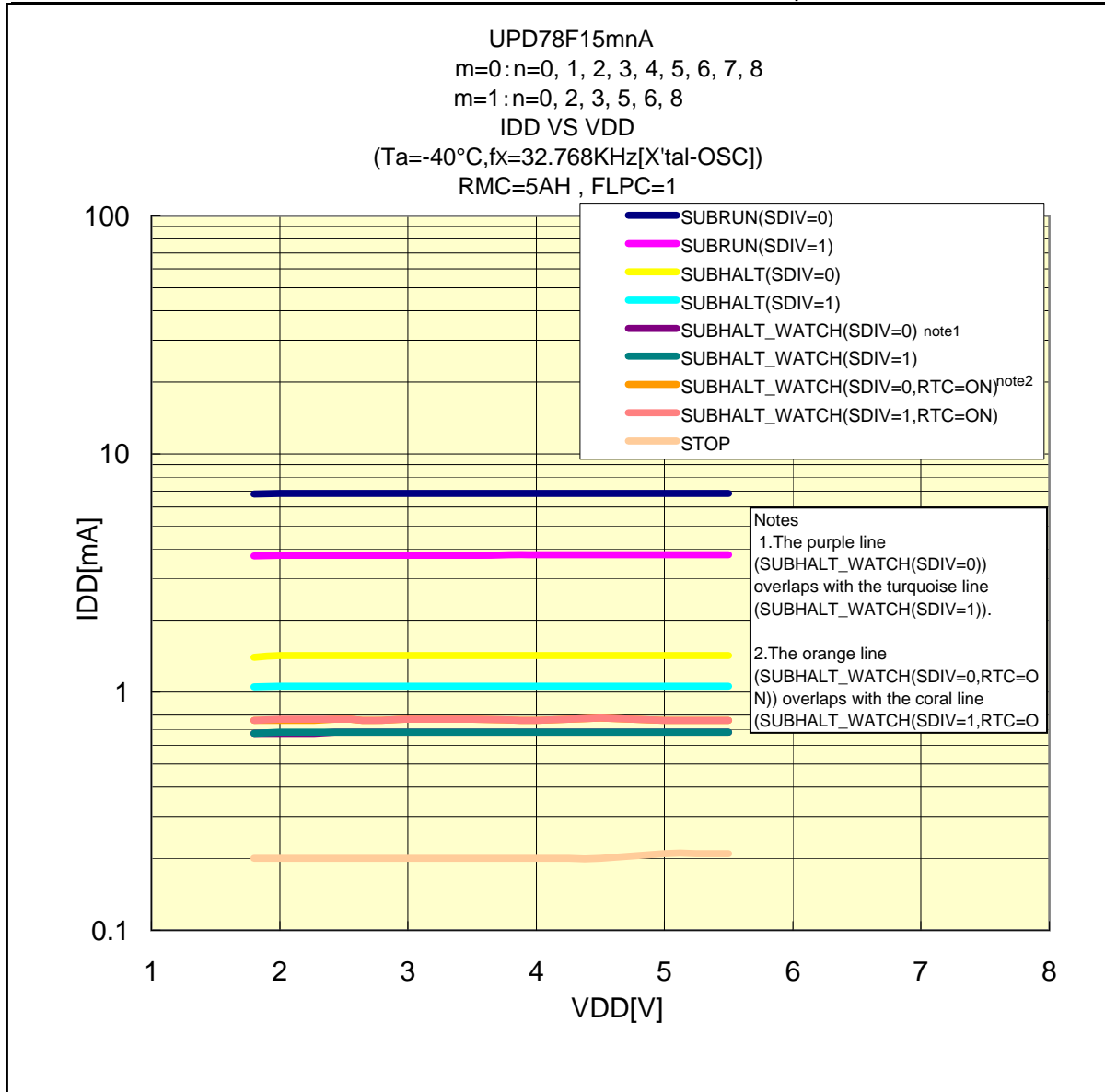
UPD78F15mnA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(-40°C/32.768KHz[X'tal-OSC])

Prepared on Oct. 11th, 2011



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UPD78F15mA

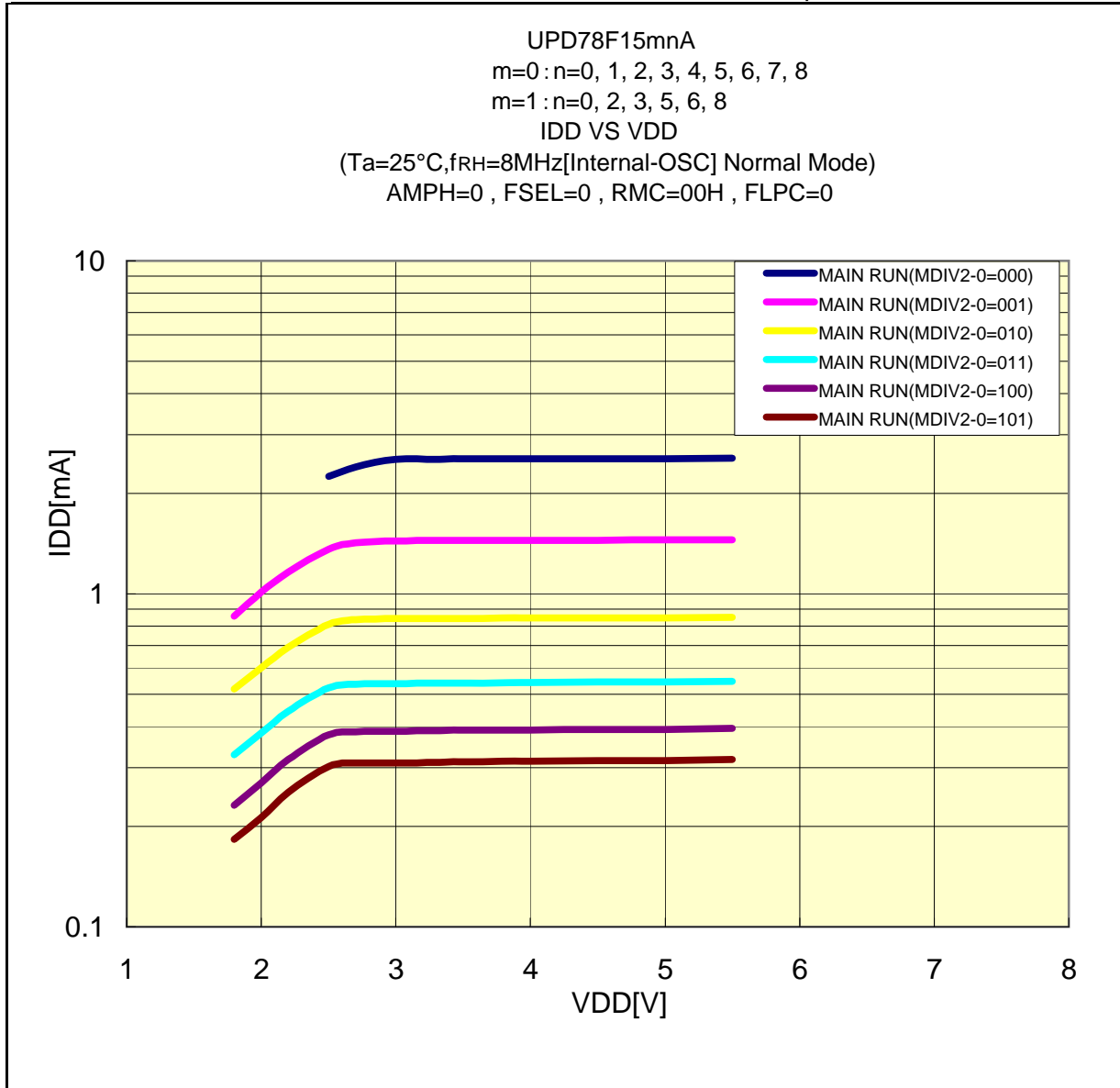
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHz[Internal-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



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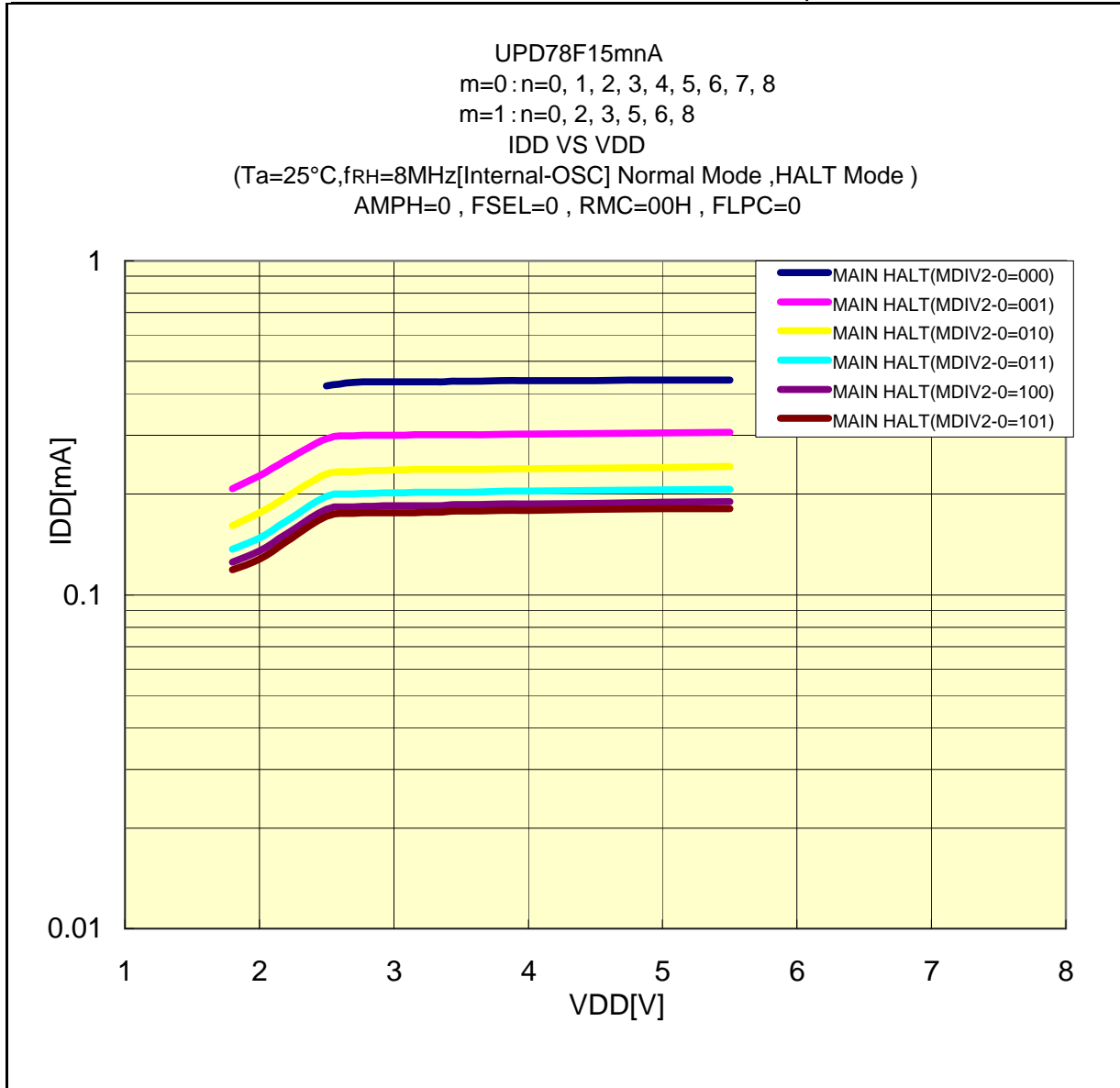
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHz[Internal-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

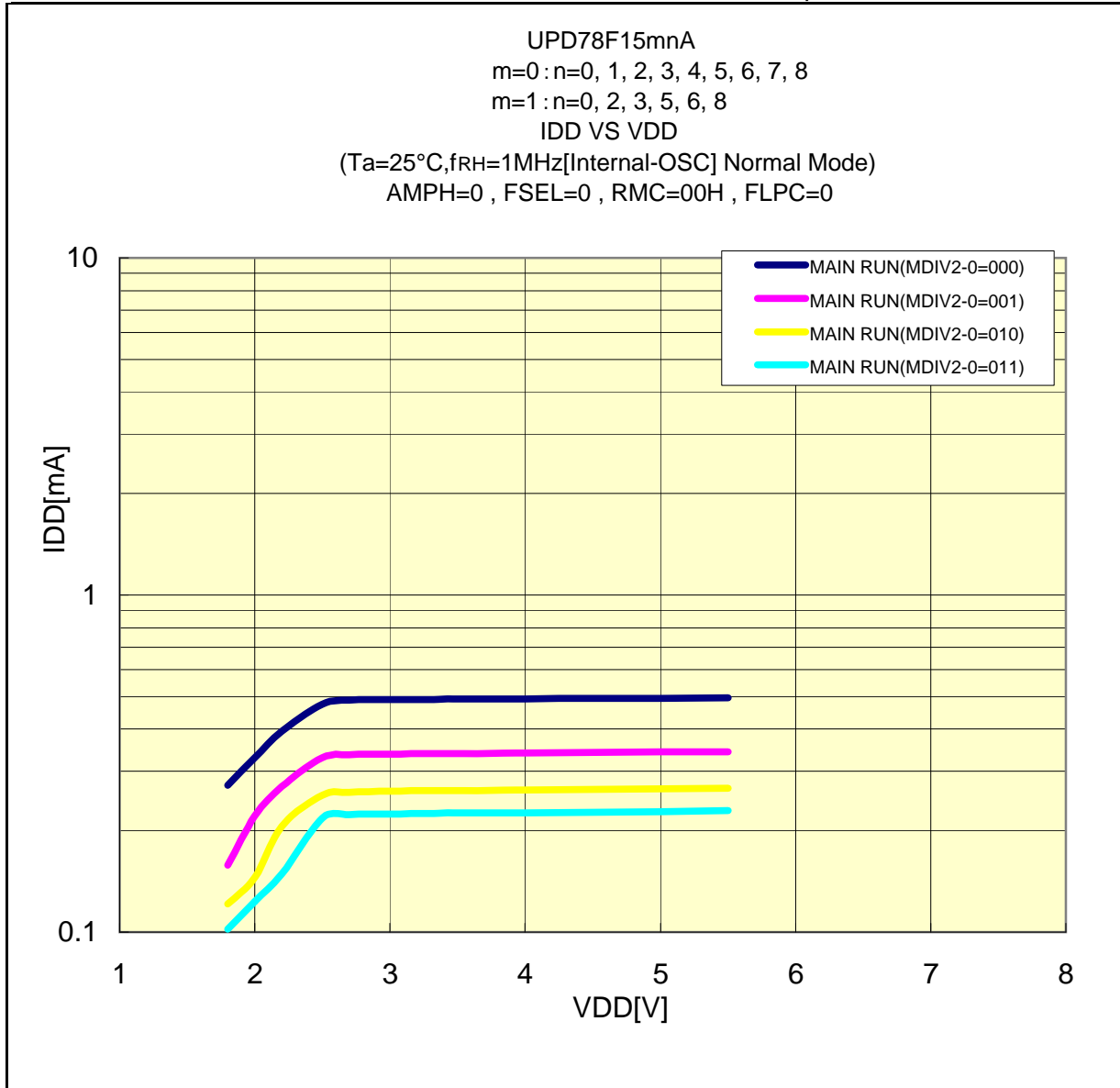
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/1MHz[Internal-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



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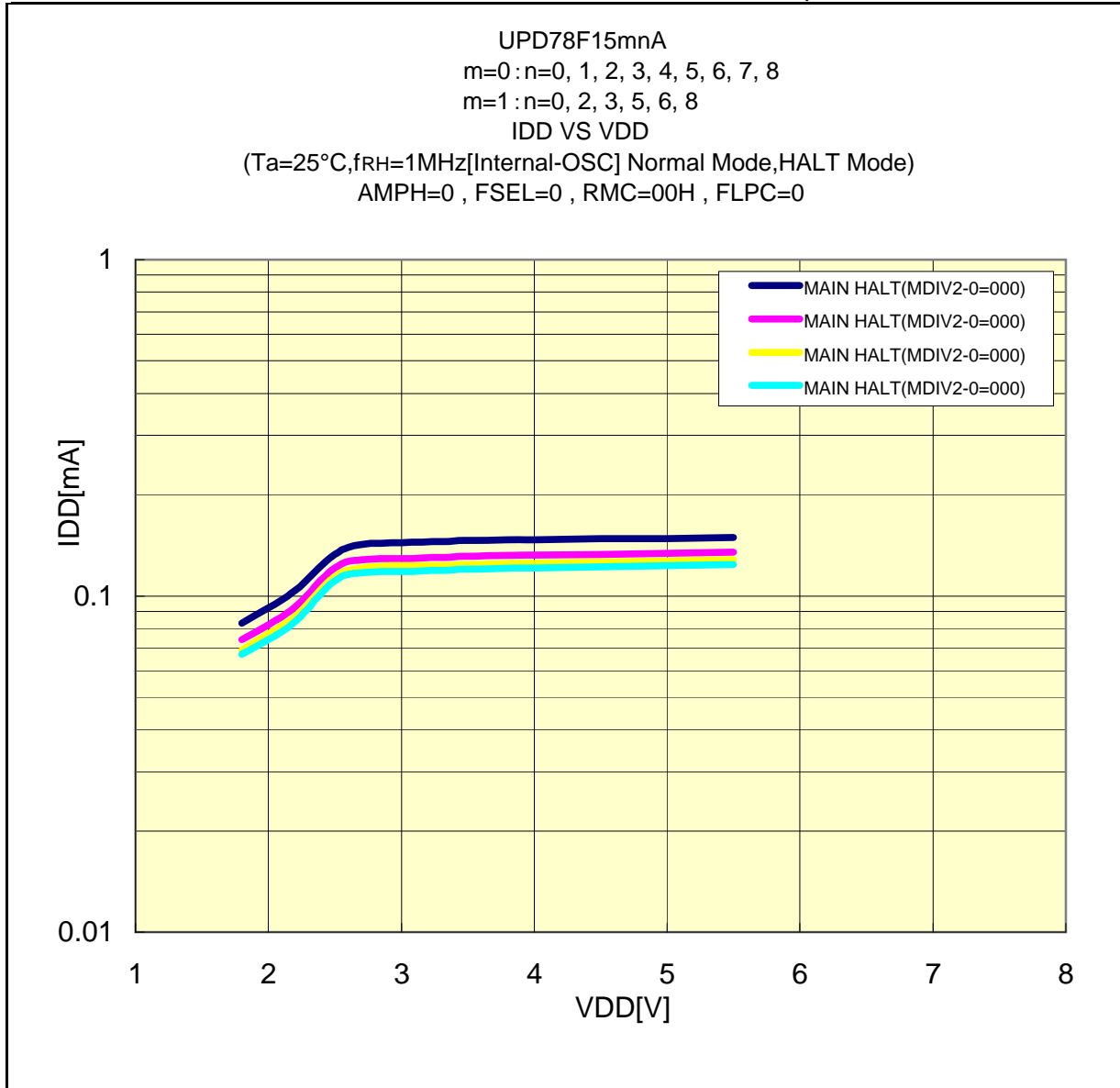
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/1MHz[Internal-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

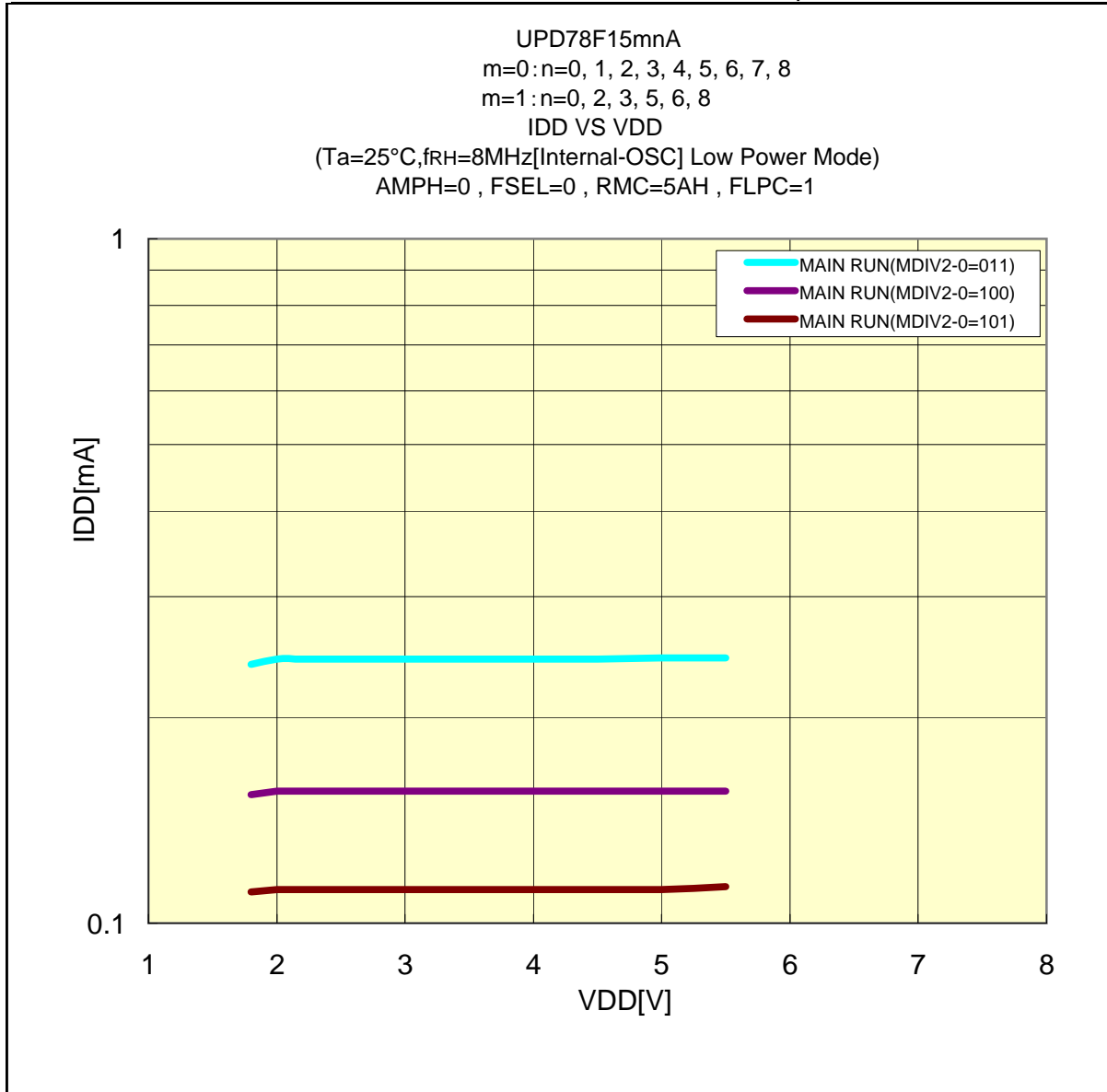
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHz[Internal-OSC])

Low Power Mode

Prepared on Oct. 11th, 2011



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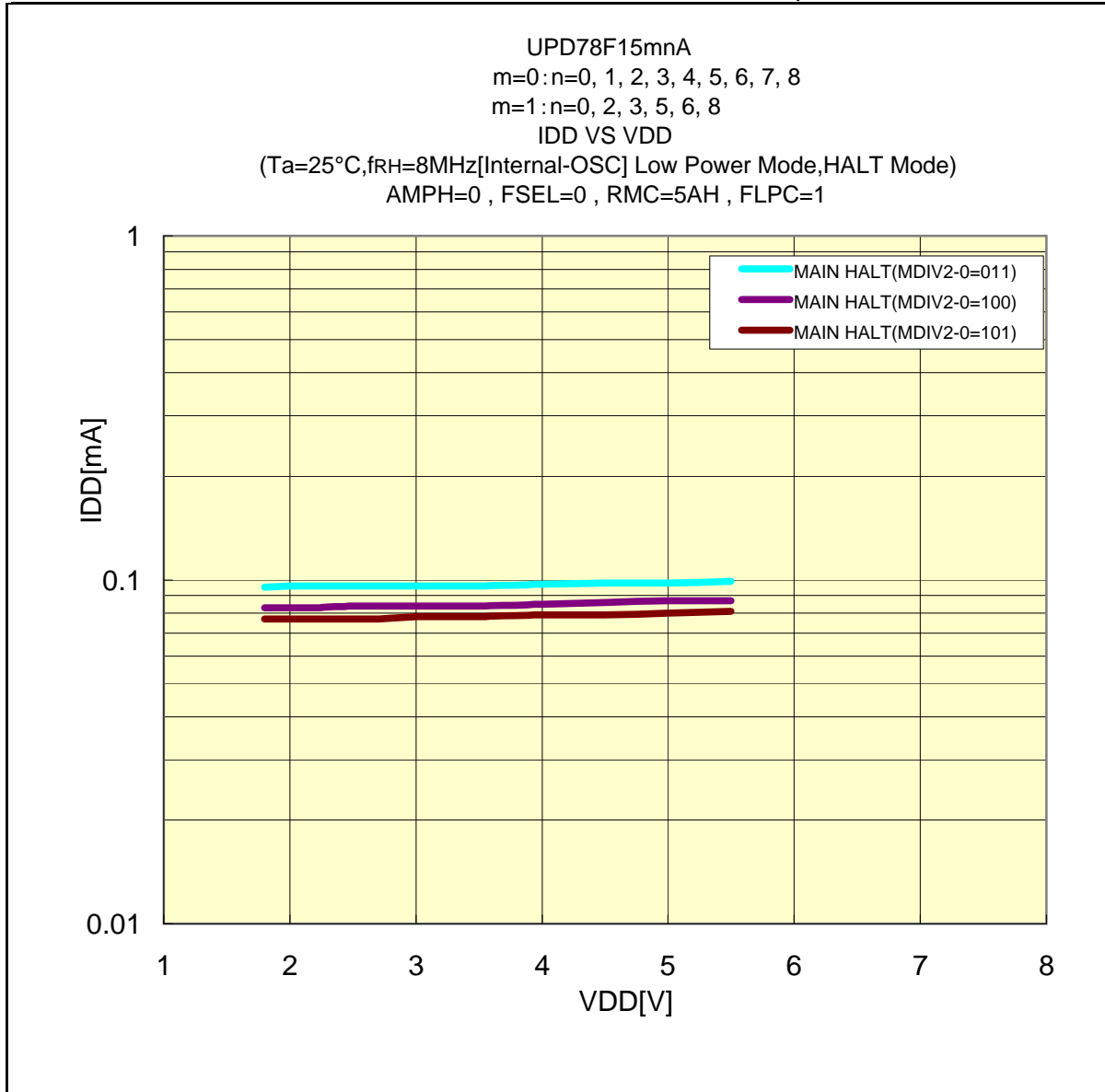
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHz[Internal-OSC]) Low Power Mode(HALT)

Prepared on Oct. 11th, 2011



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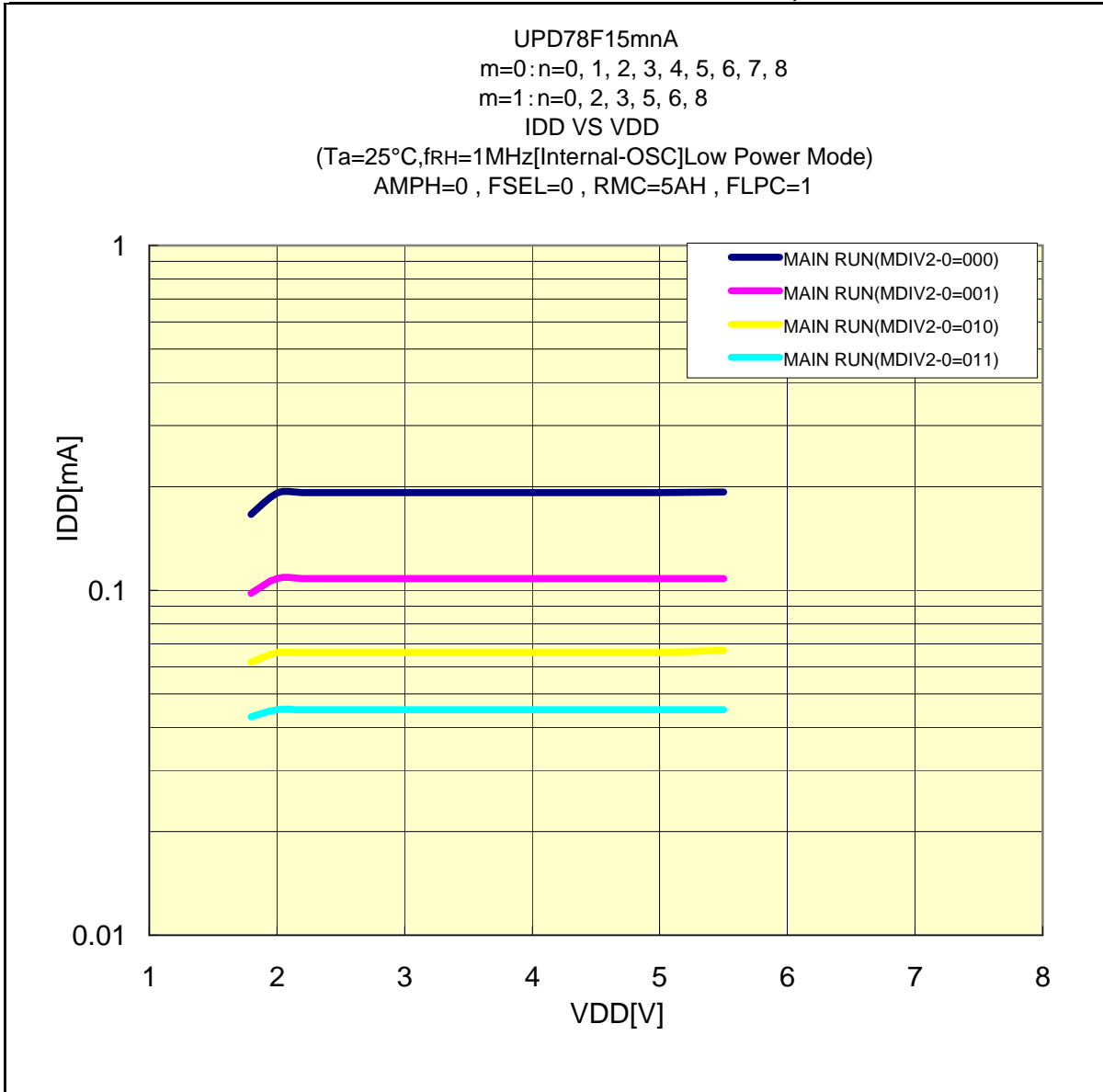
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/1MHz[Internal-OSC]) Low Power Mode

Prepared on Oct. 11th, 2011



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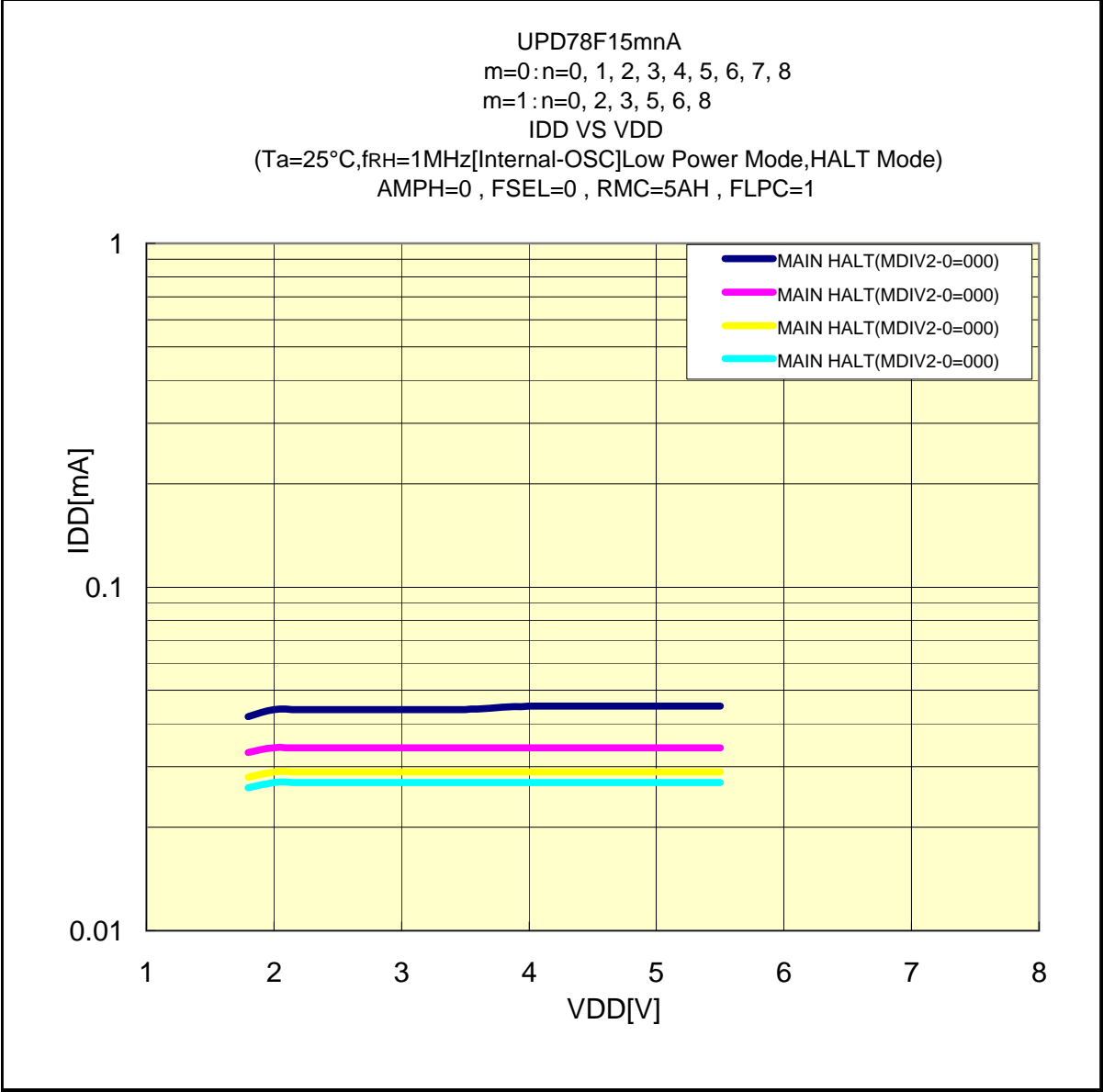
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(25°C/1MHz[Internal-OSC])
Low Power Mode(HALT)**

Prepared on Oct. 11th, 2011



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UPD78F15mA

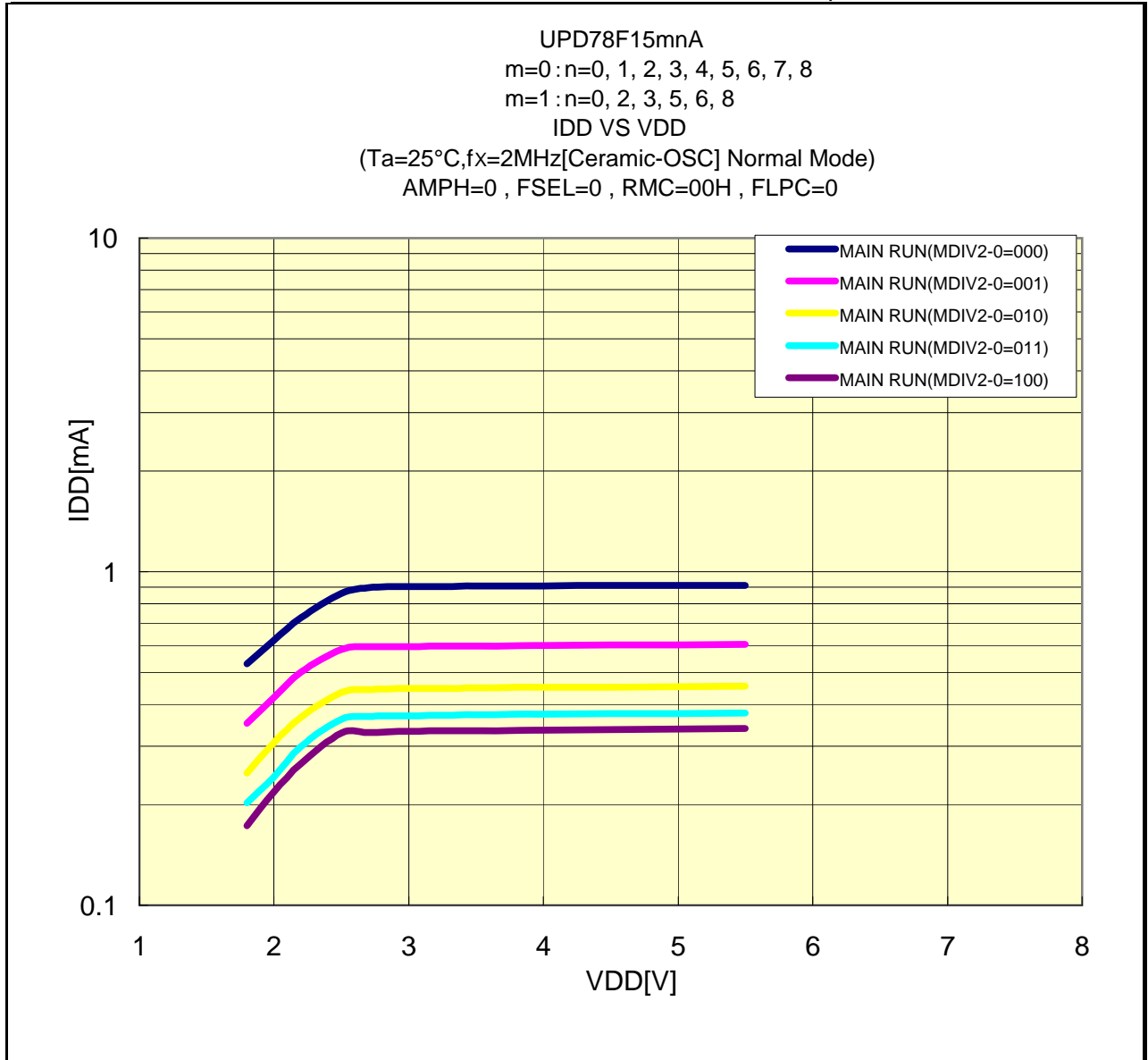
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/2MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



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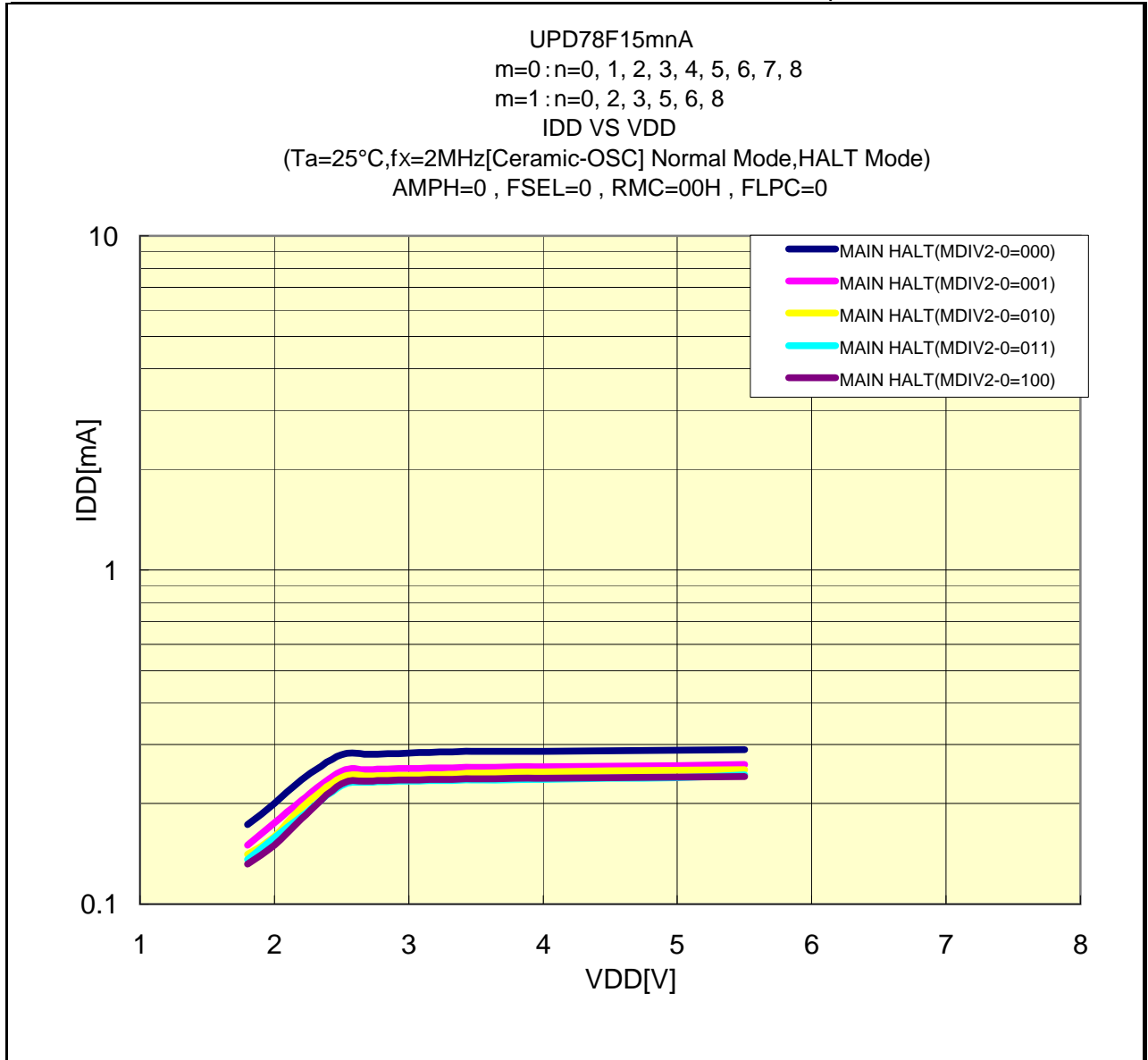
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/2MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

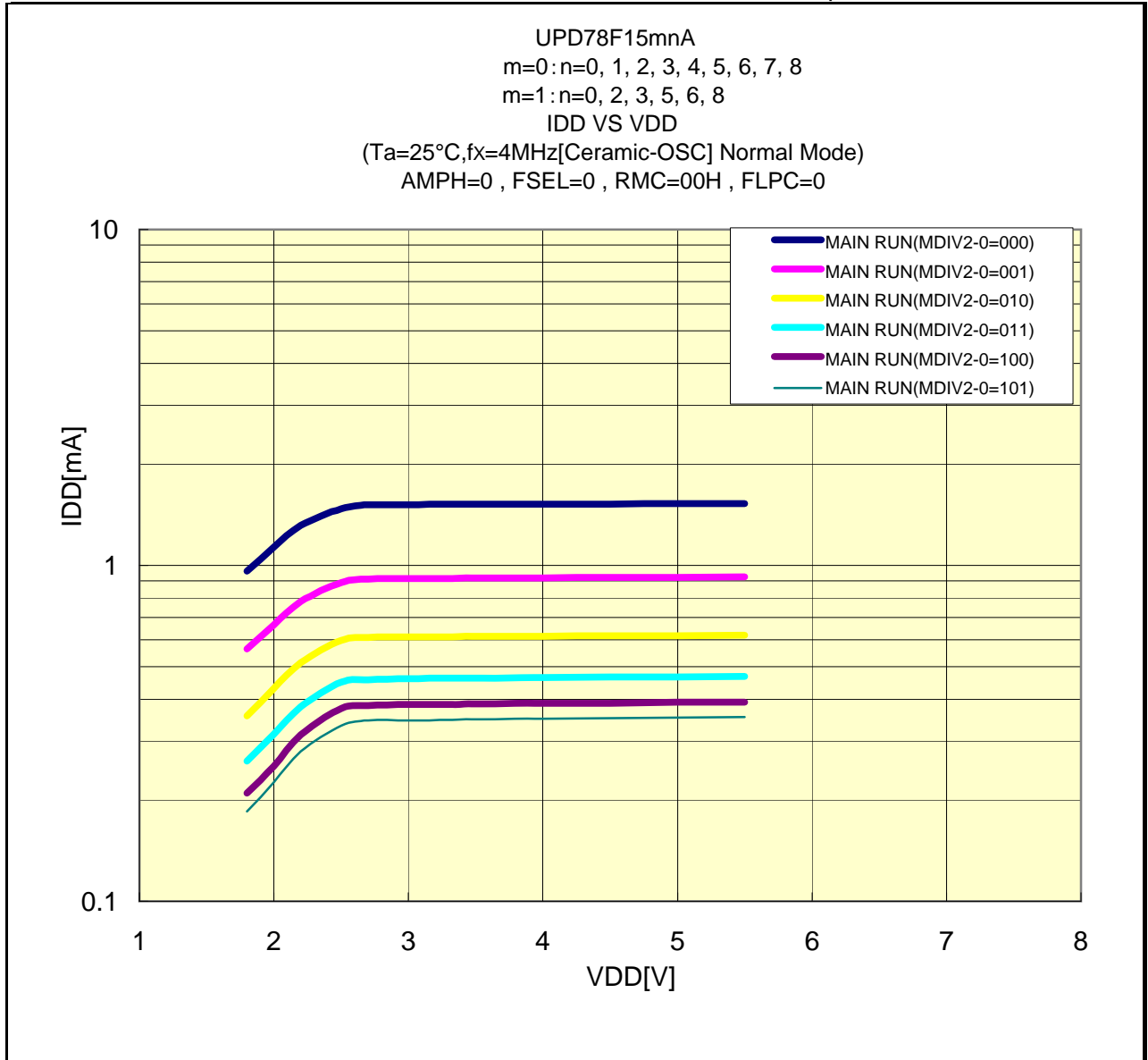
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/4MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



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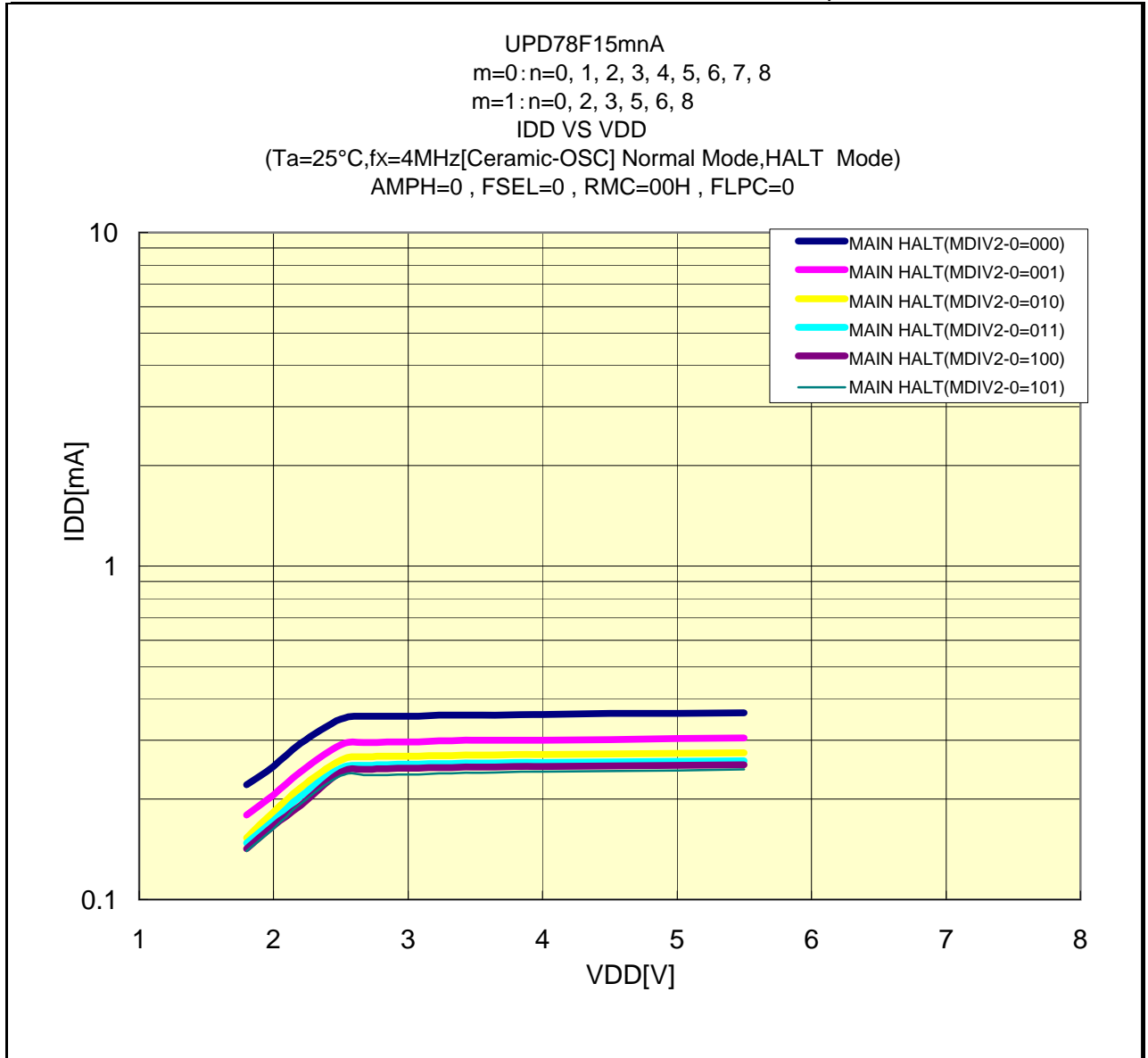
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/4MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

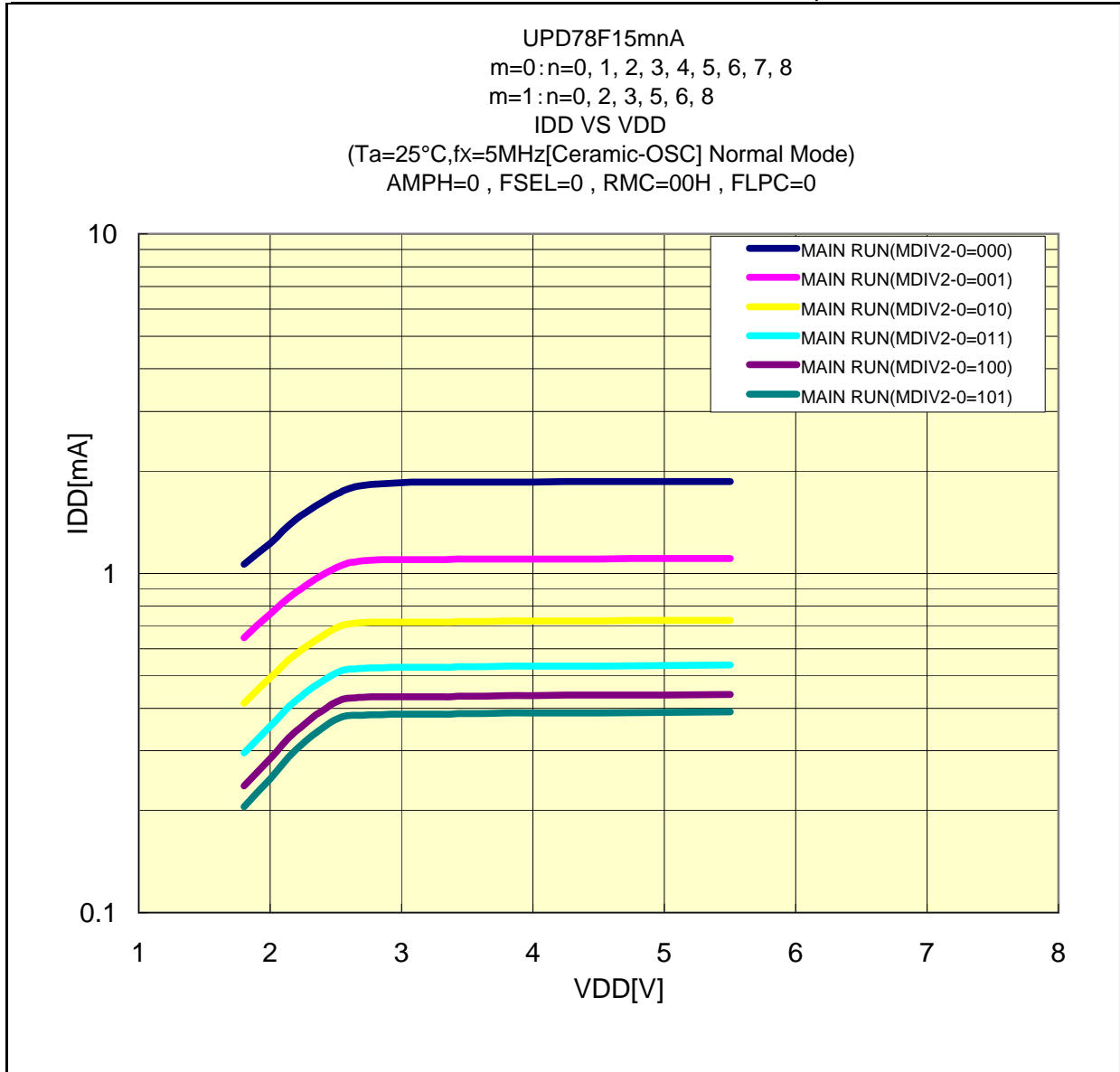
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/5MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

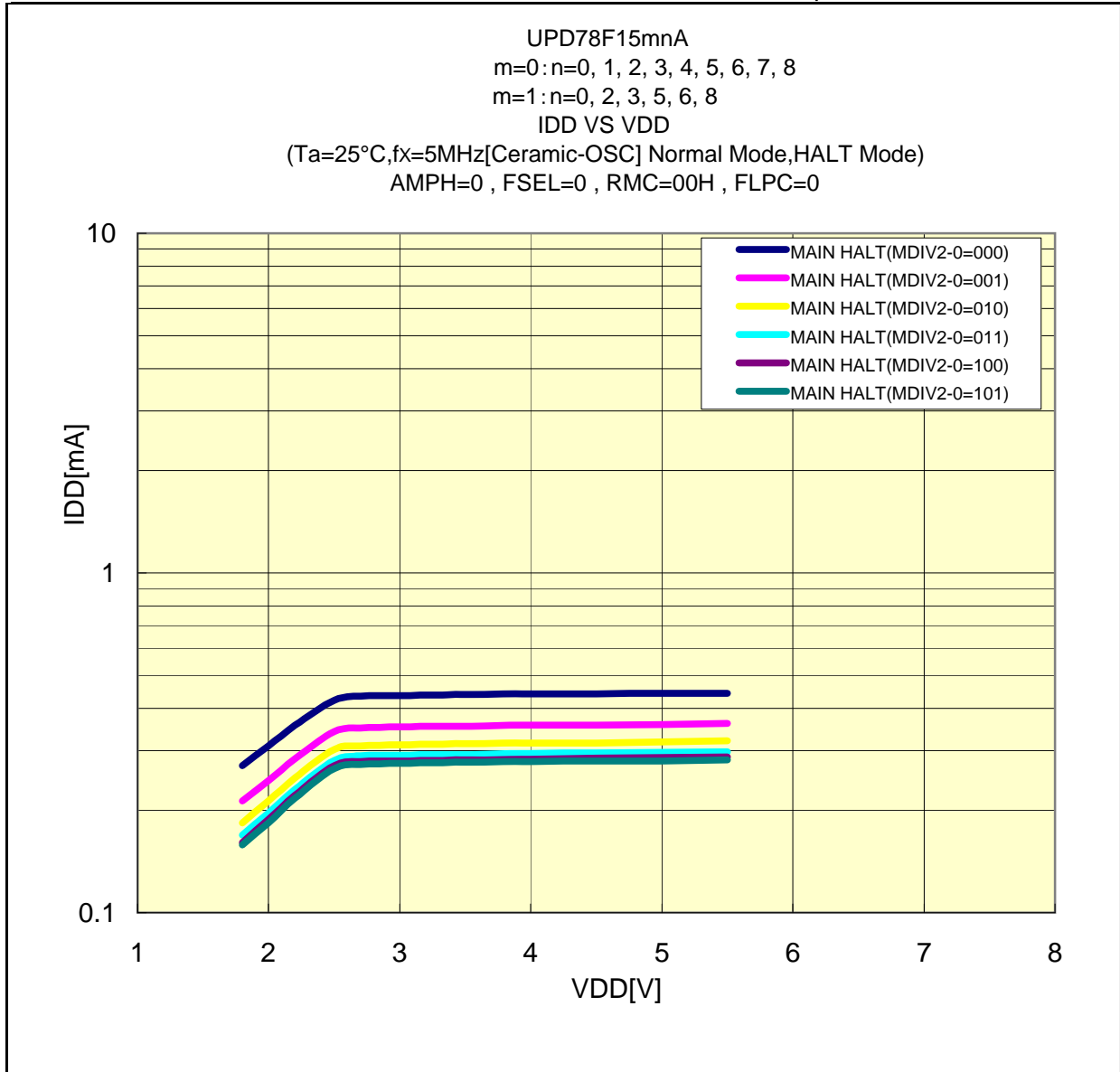
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/5MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

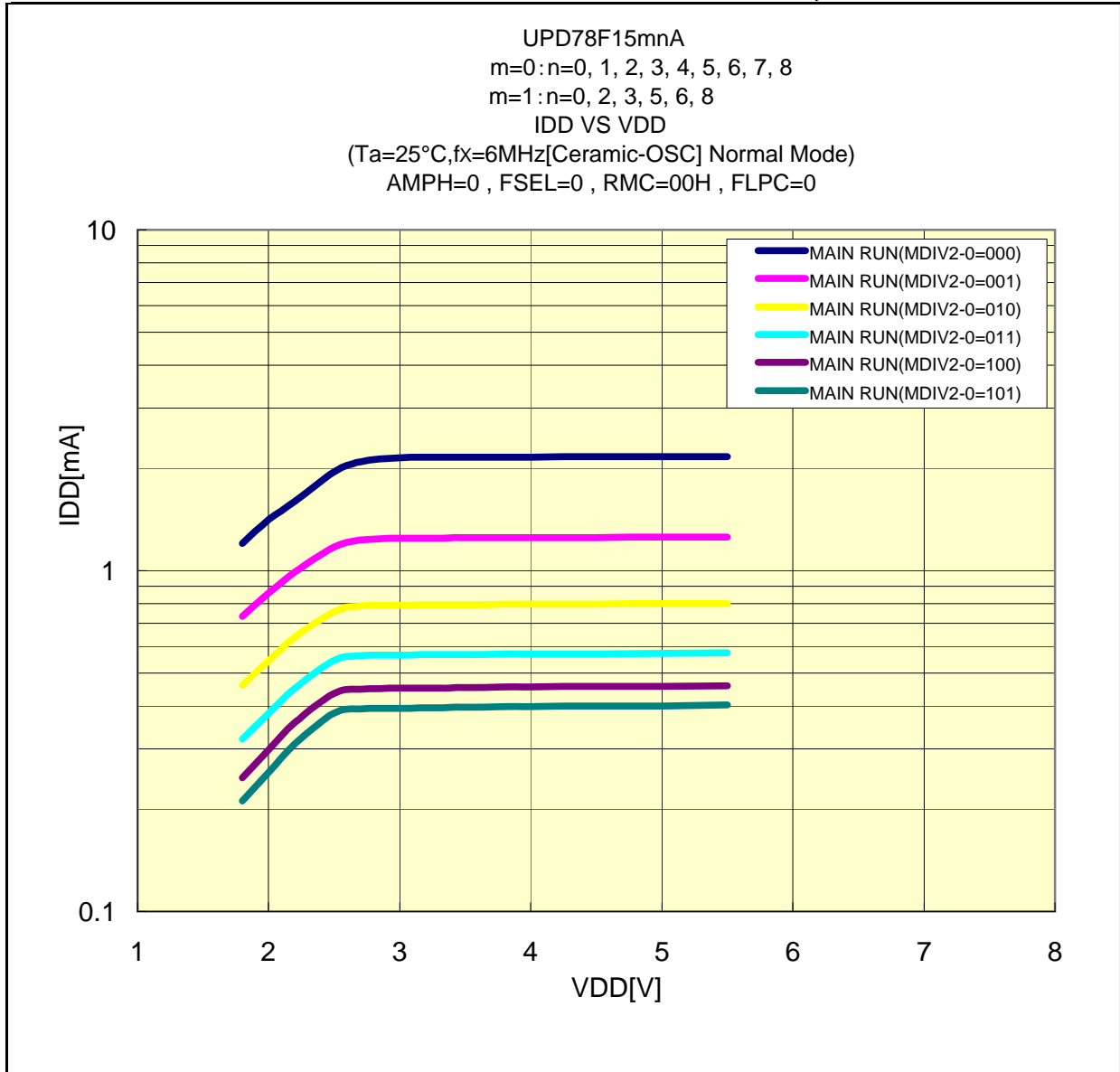
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/6MHzCeramic-OSC)

Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

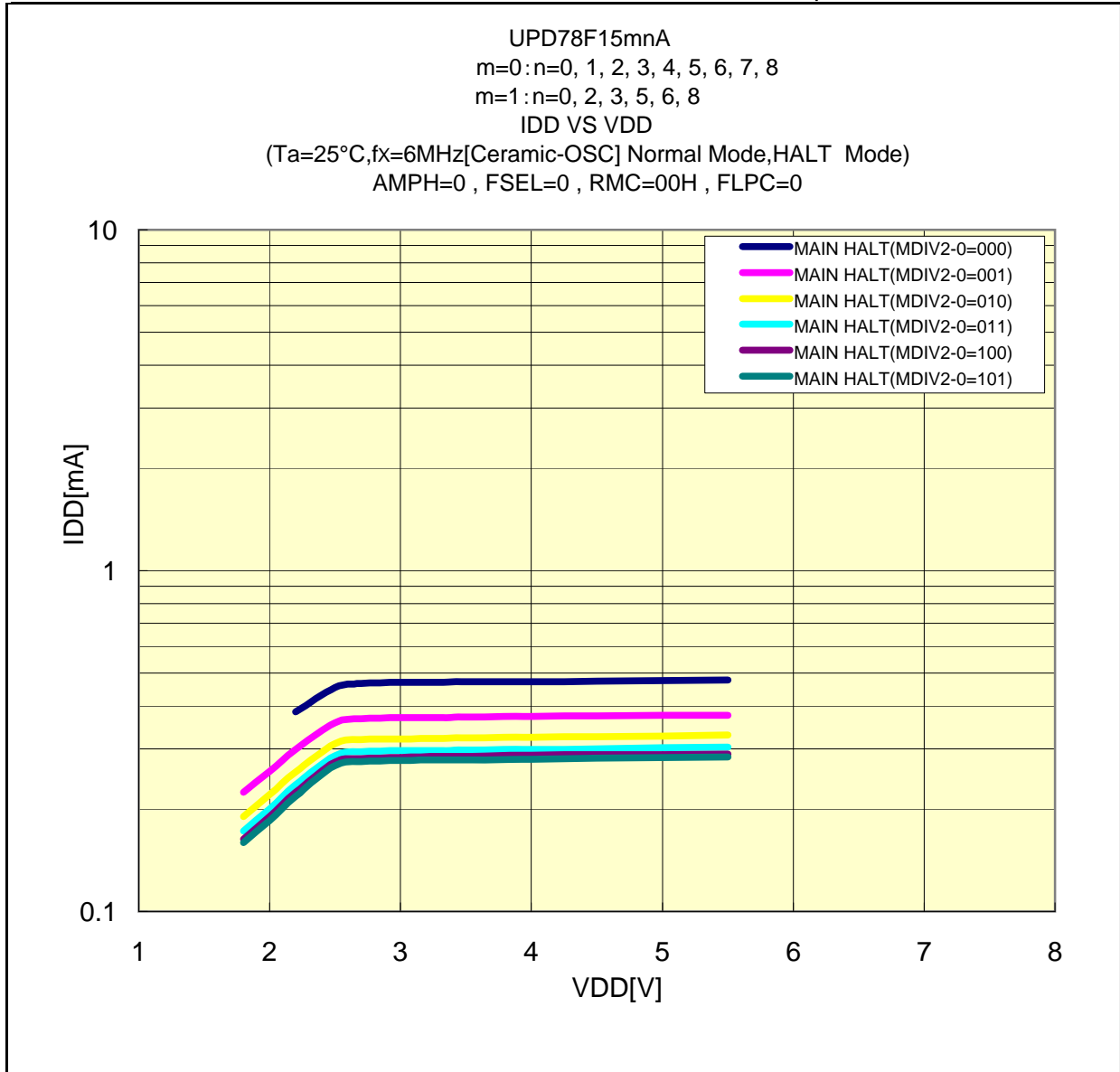
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/6MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

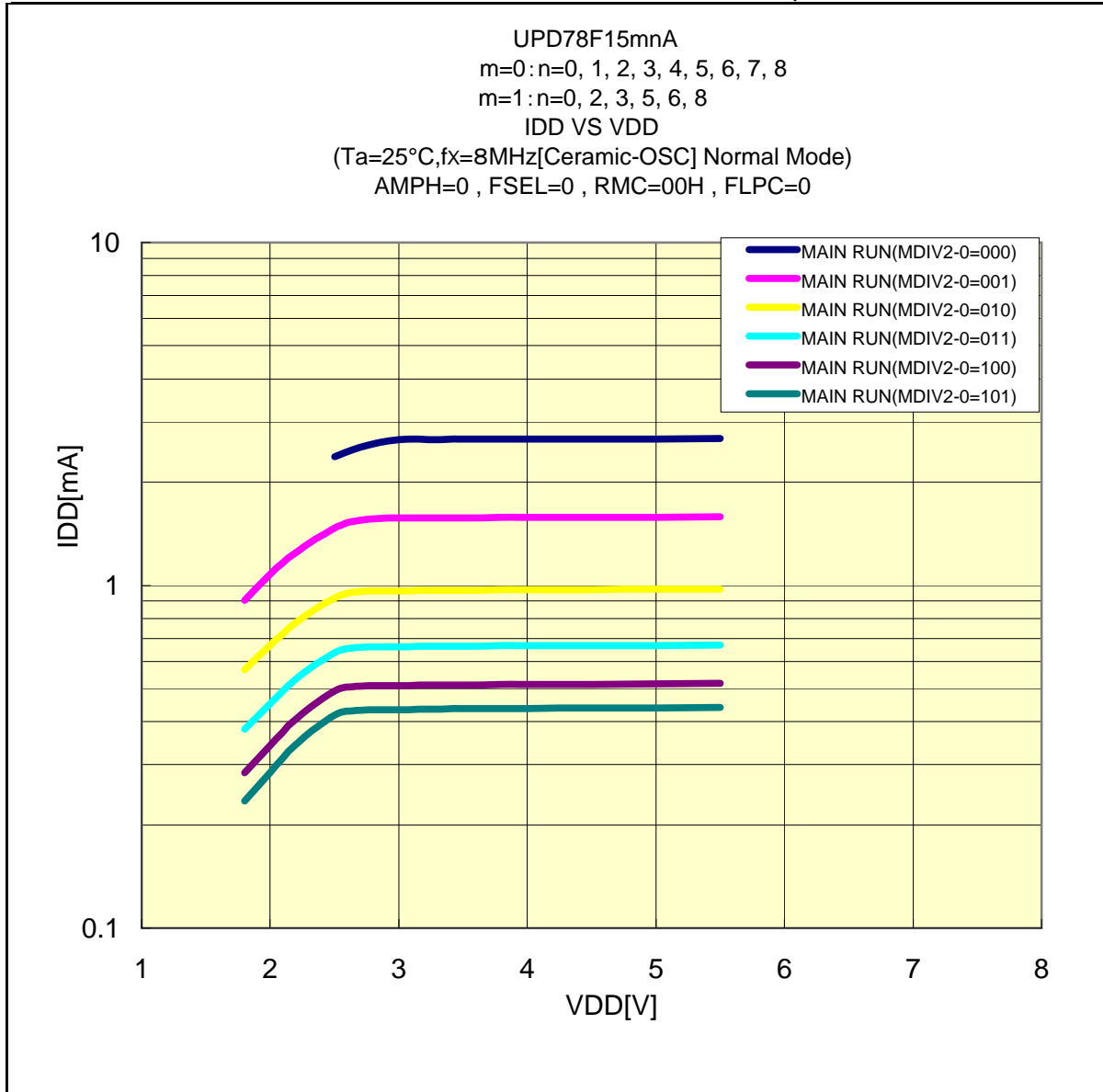
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHzCeramic-OSC) Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

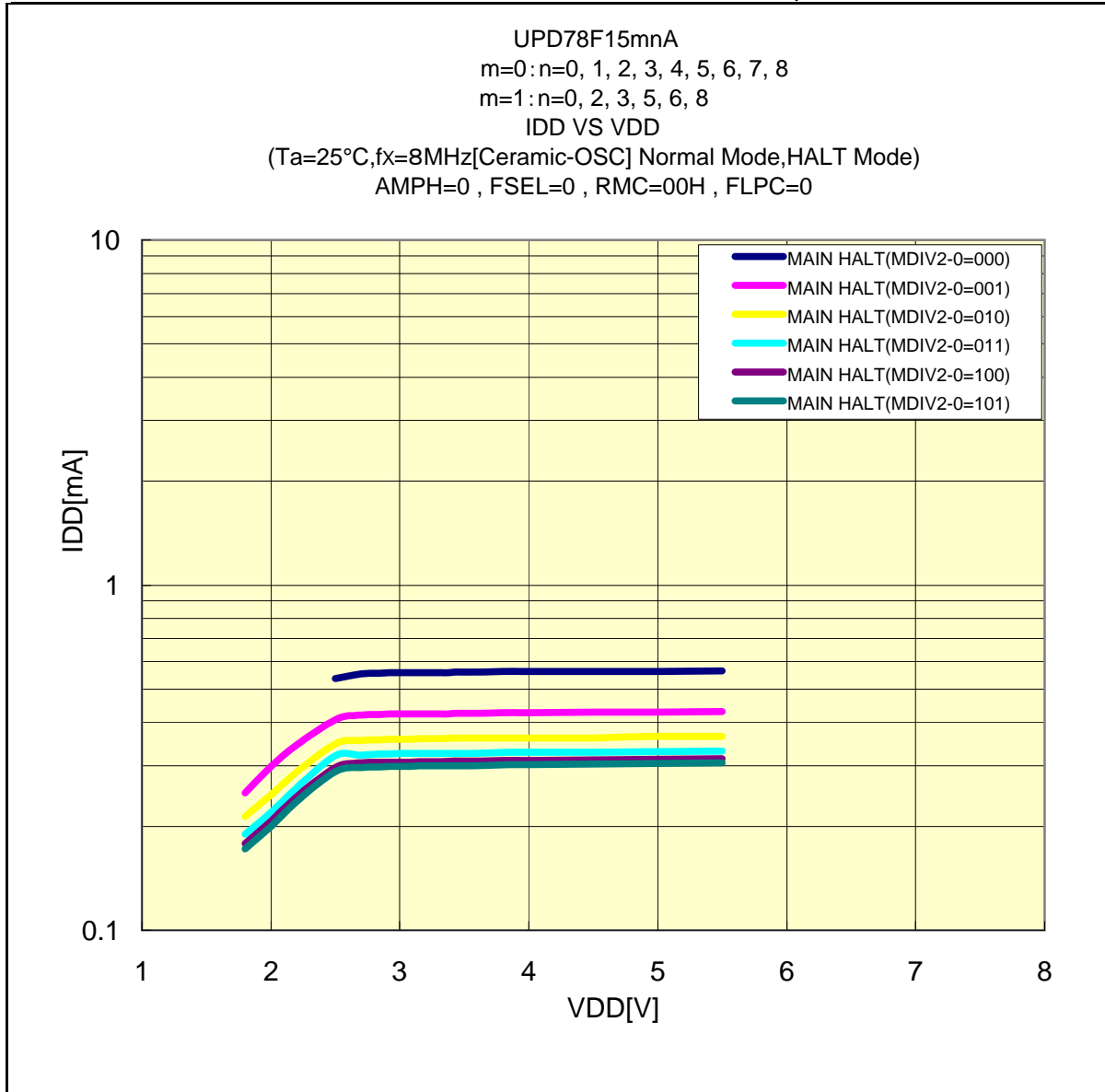
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/8MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

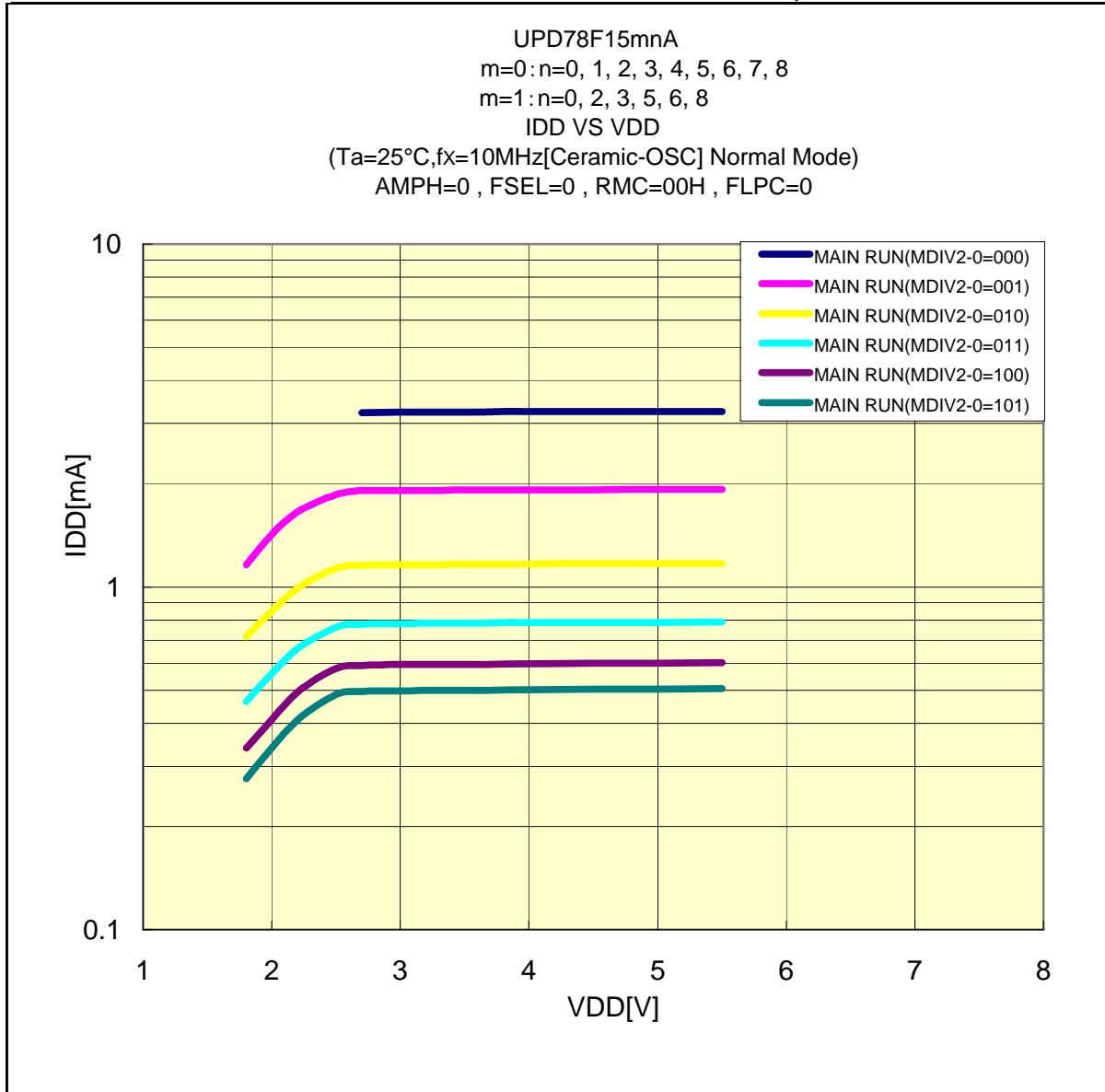
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/10MHzCeramic-OSC) Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

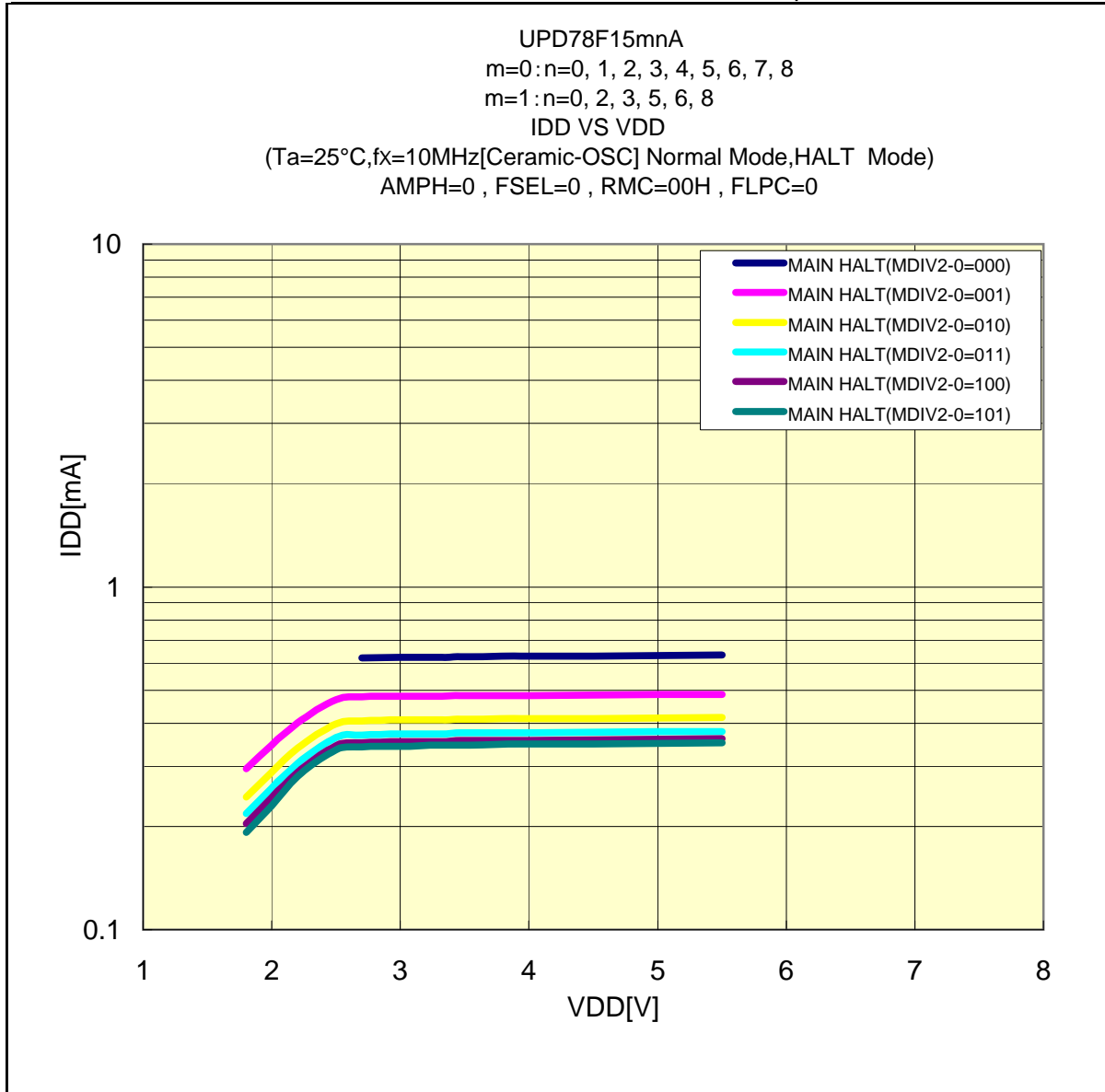
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/10MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

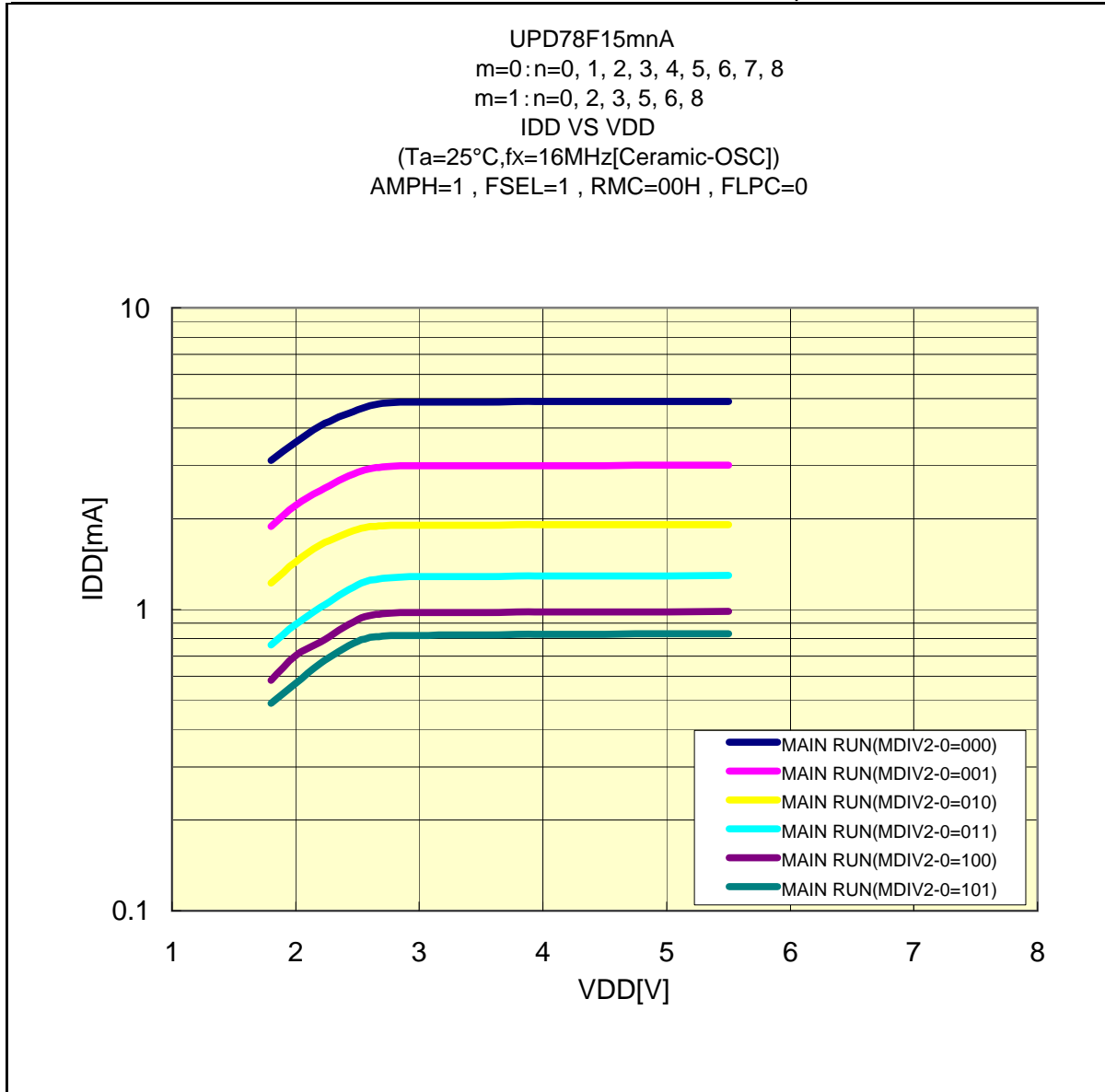
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/16MHzCeramic-OSC)

AMPH=1,FSEL=1

Prepared on Oct. 11th, 2011



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UPD78F15mA

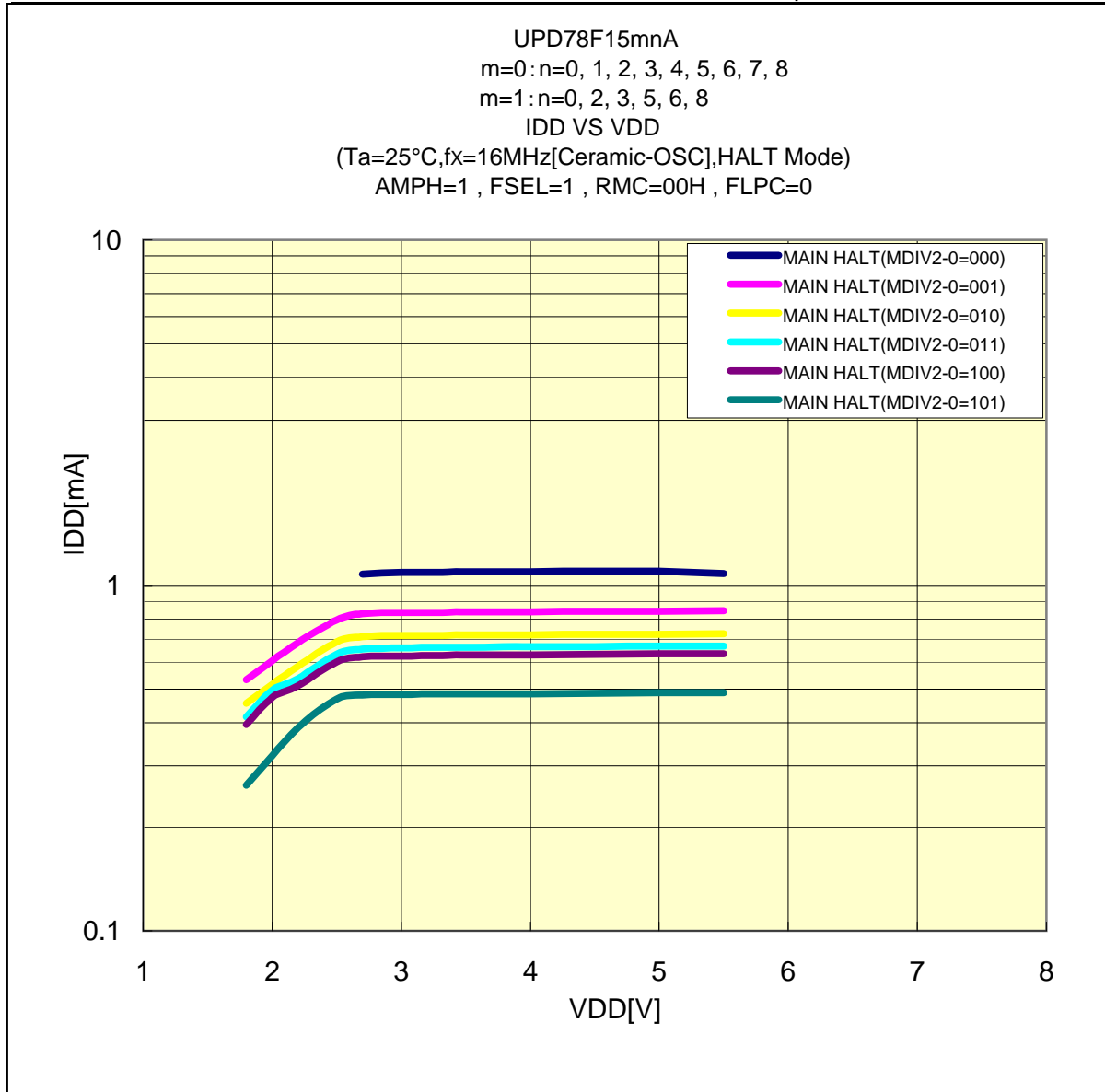
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/16MHzCeramic-OSC)

AMPH=1,FSEL=1(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

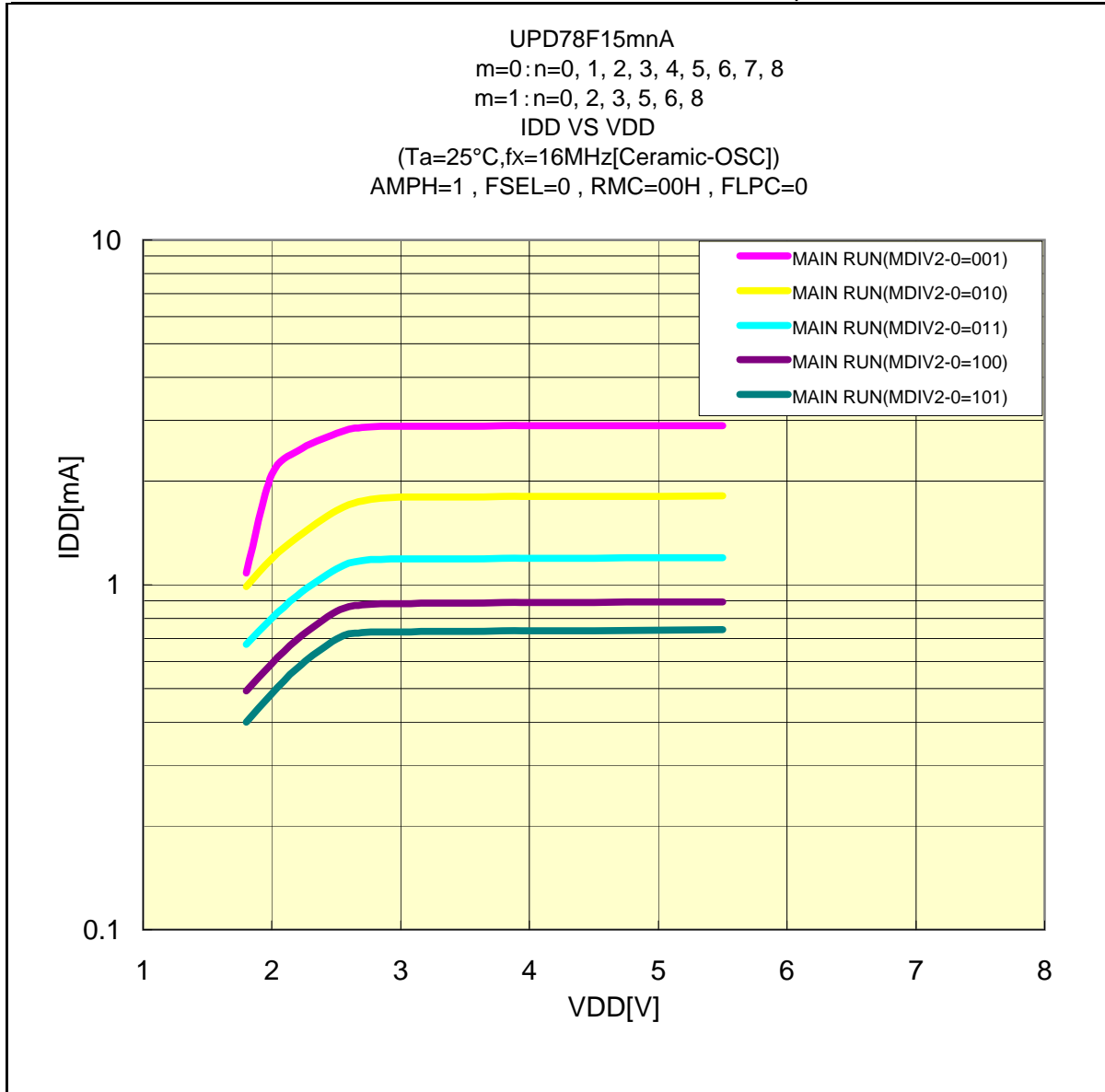
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/16MHzCeramic-OSC)

AMPH=1,FSEL=0

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

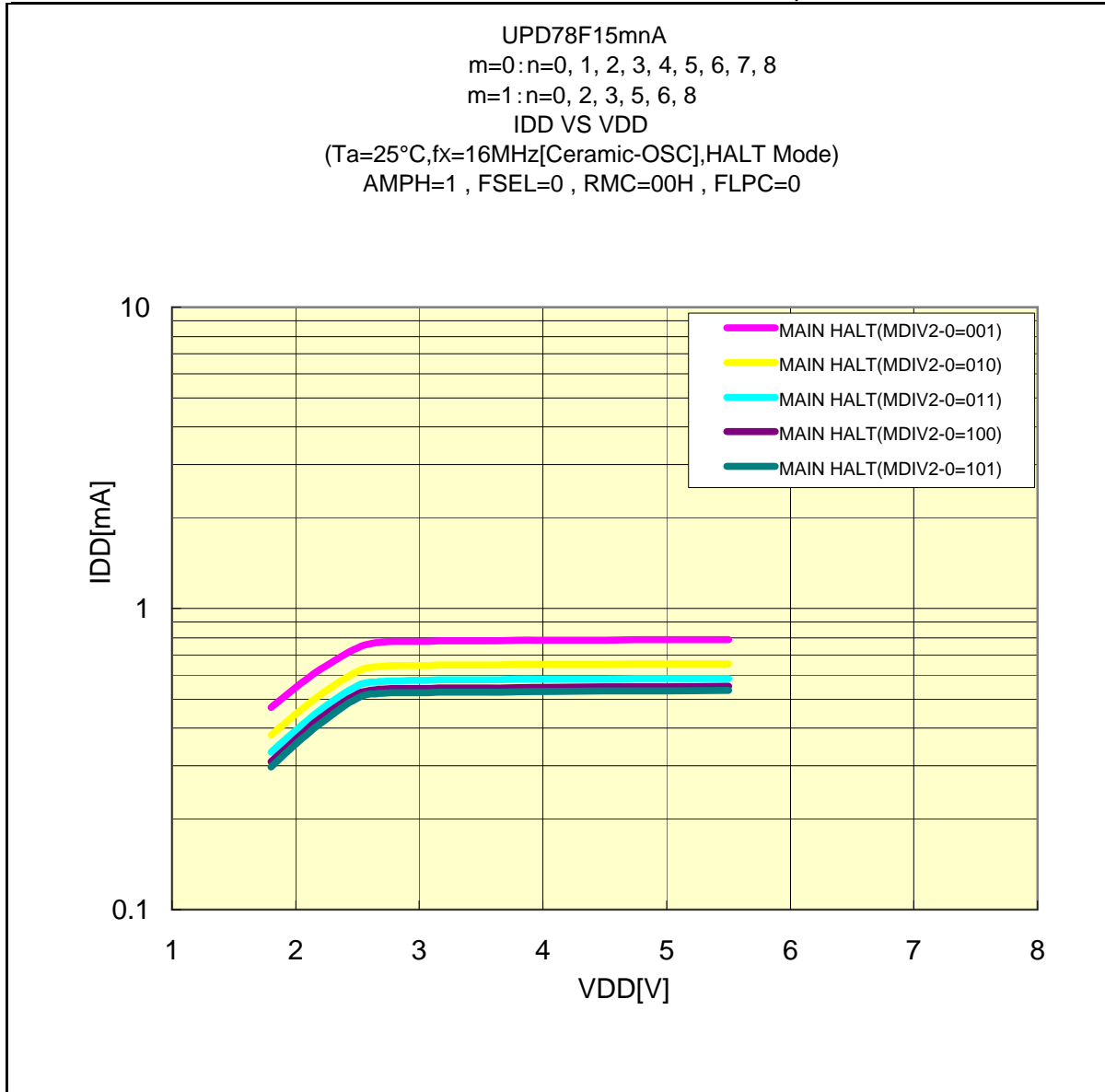
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/16MHzCeramic-OSC)

AMPH=1,FSEL=0(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

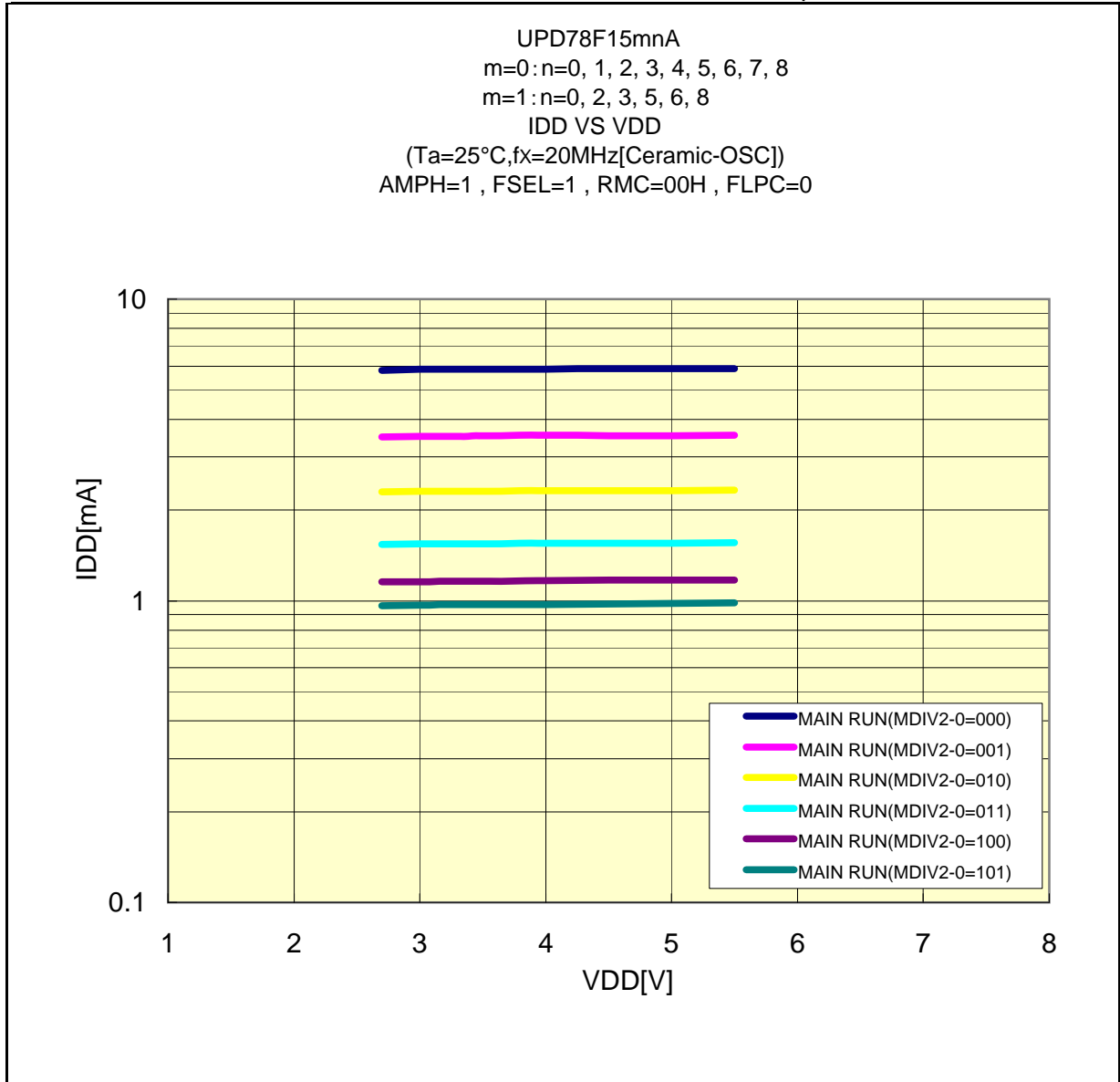
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/20MHzCeramic-OSC)

AMPH=1,FSEL=1

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

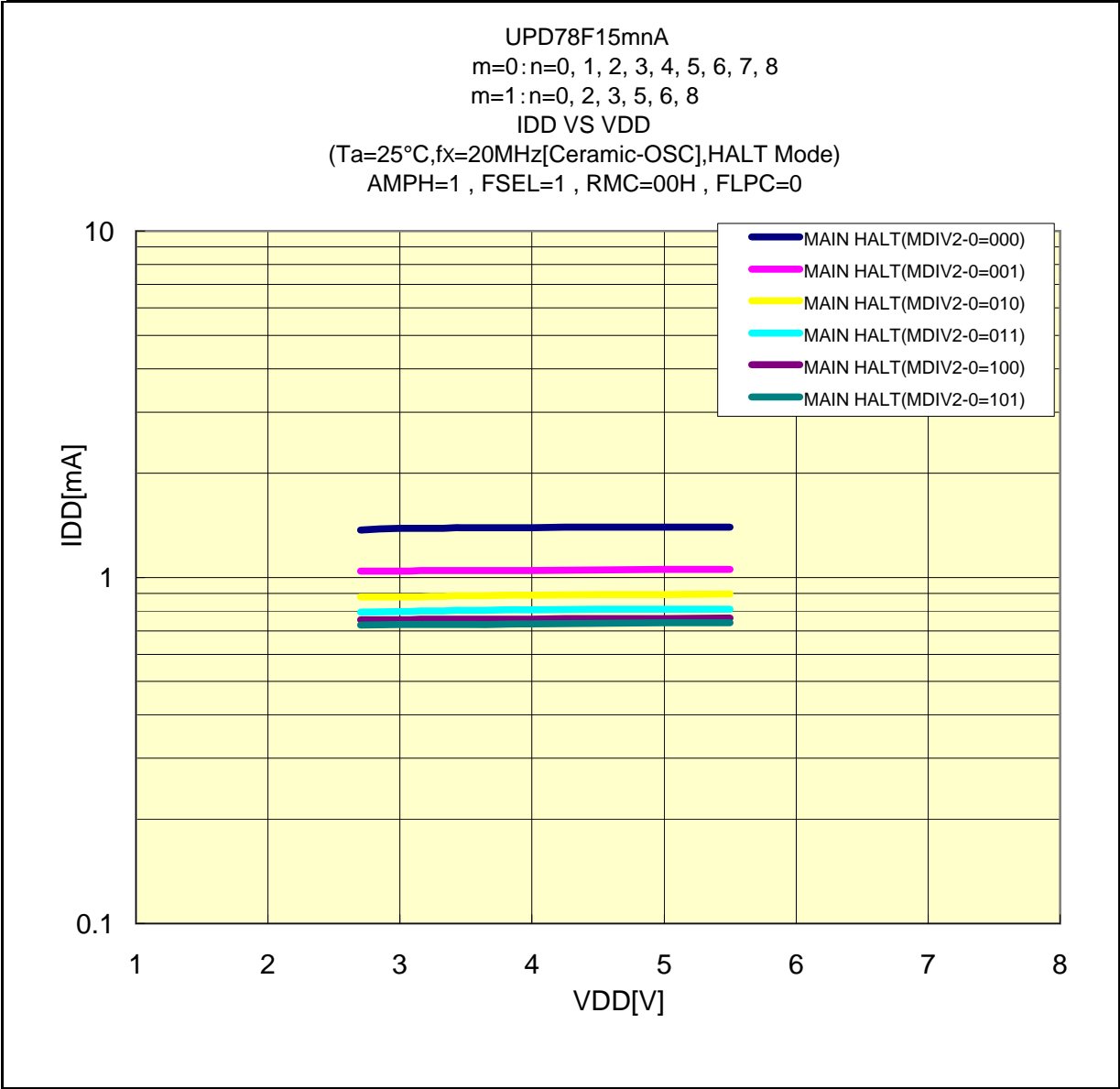
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/20MHzCeramic-OSC)]
AMPH=1,FSEL=1(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

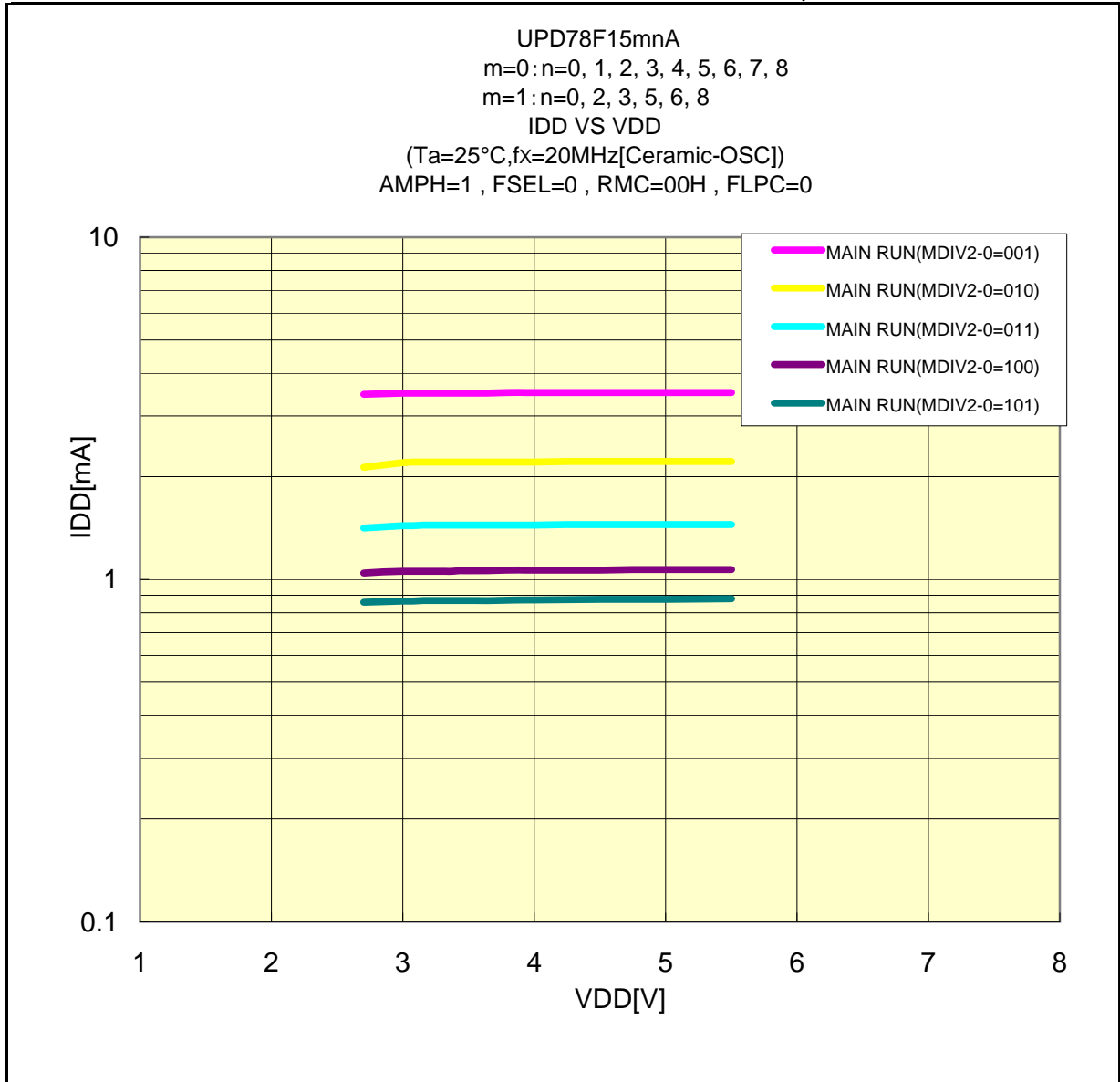
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/20MHzCeramic-OSC)

AMPH=1,FSEL=0

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

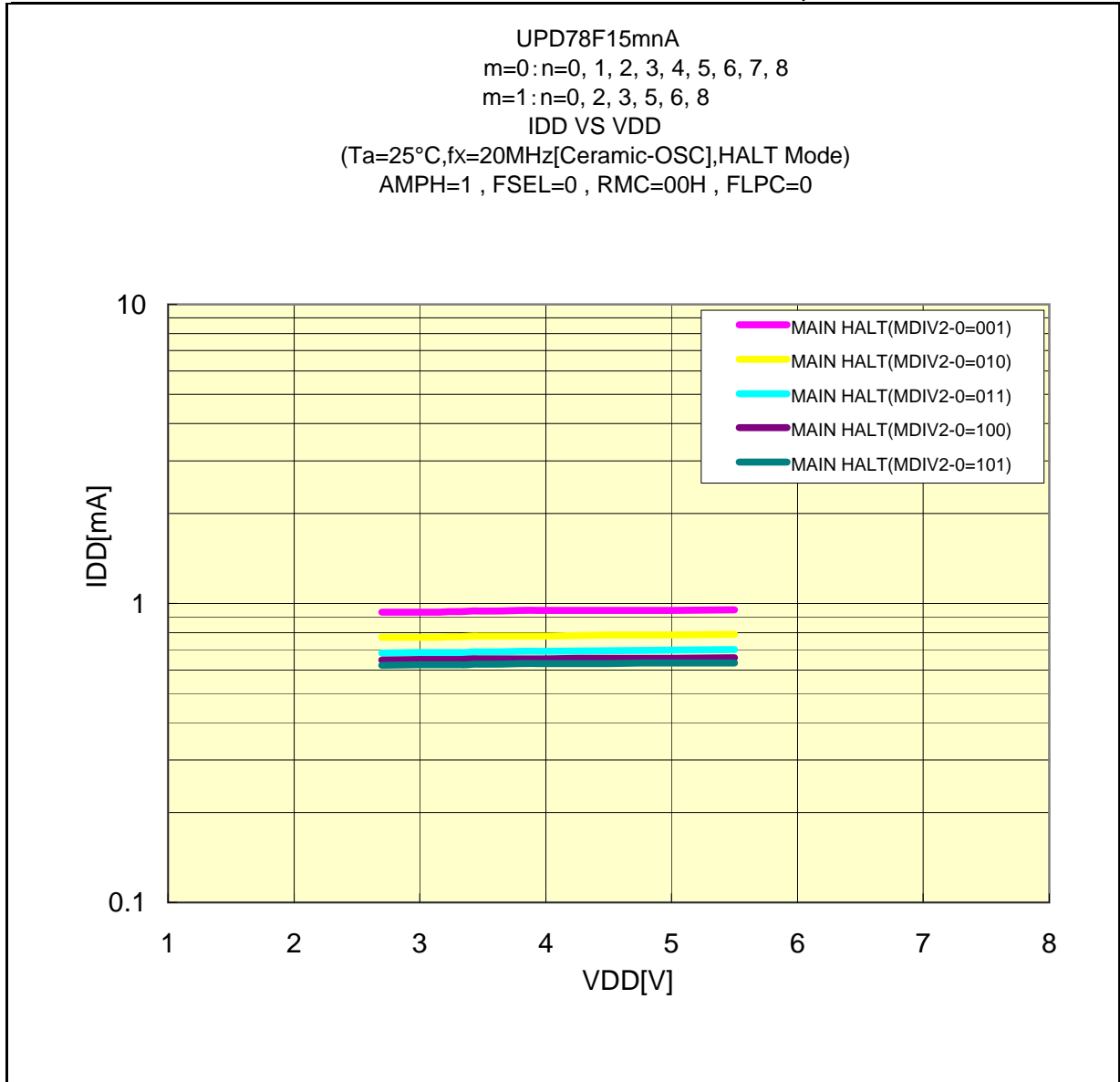
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/20MHzCeramic-OSC)

AMPH=1,FSEL=0(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

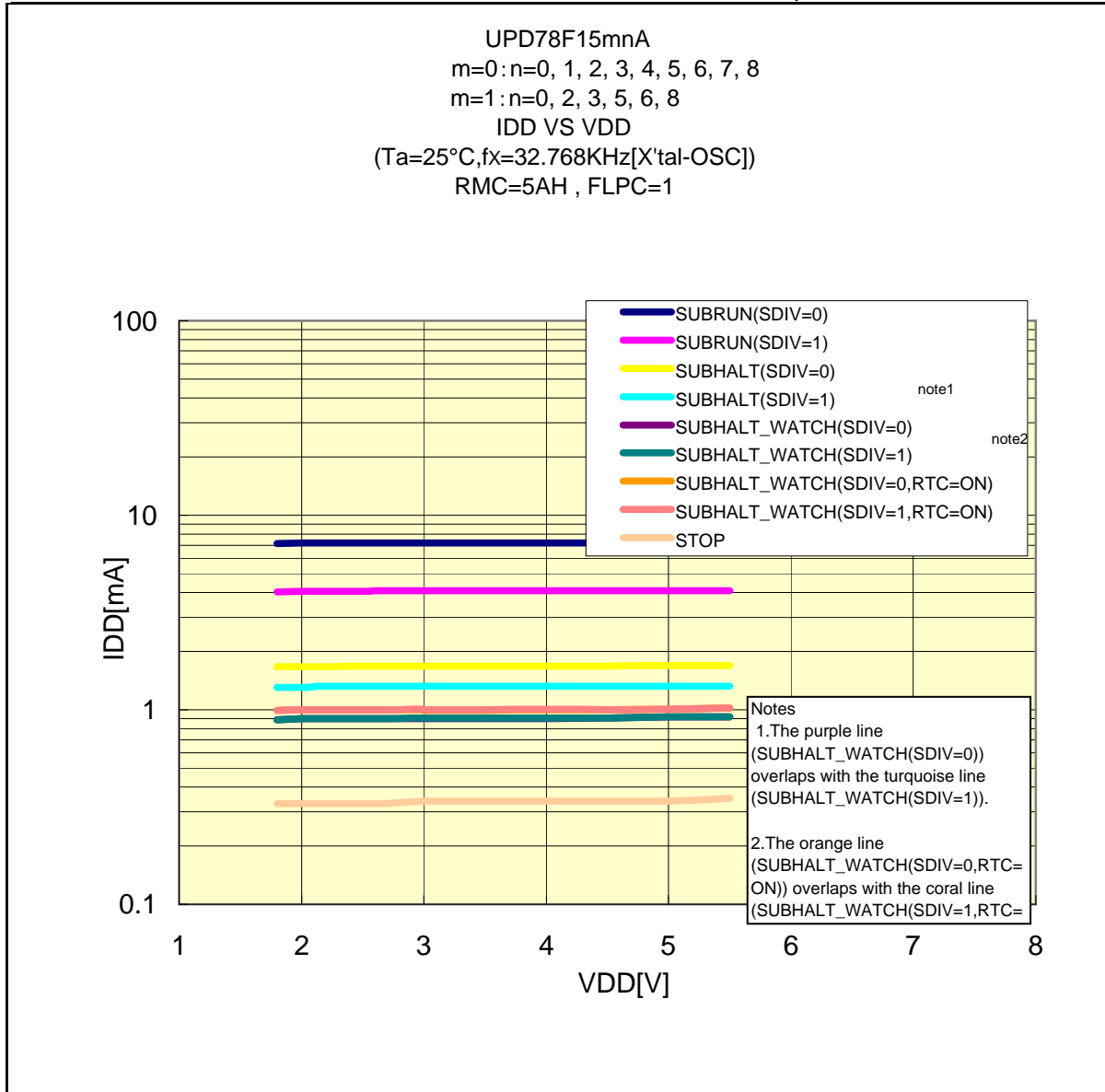
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/32.768KHz[X'tal-OSC])

Prepared on Oct. 11th, 2011



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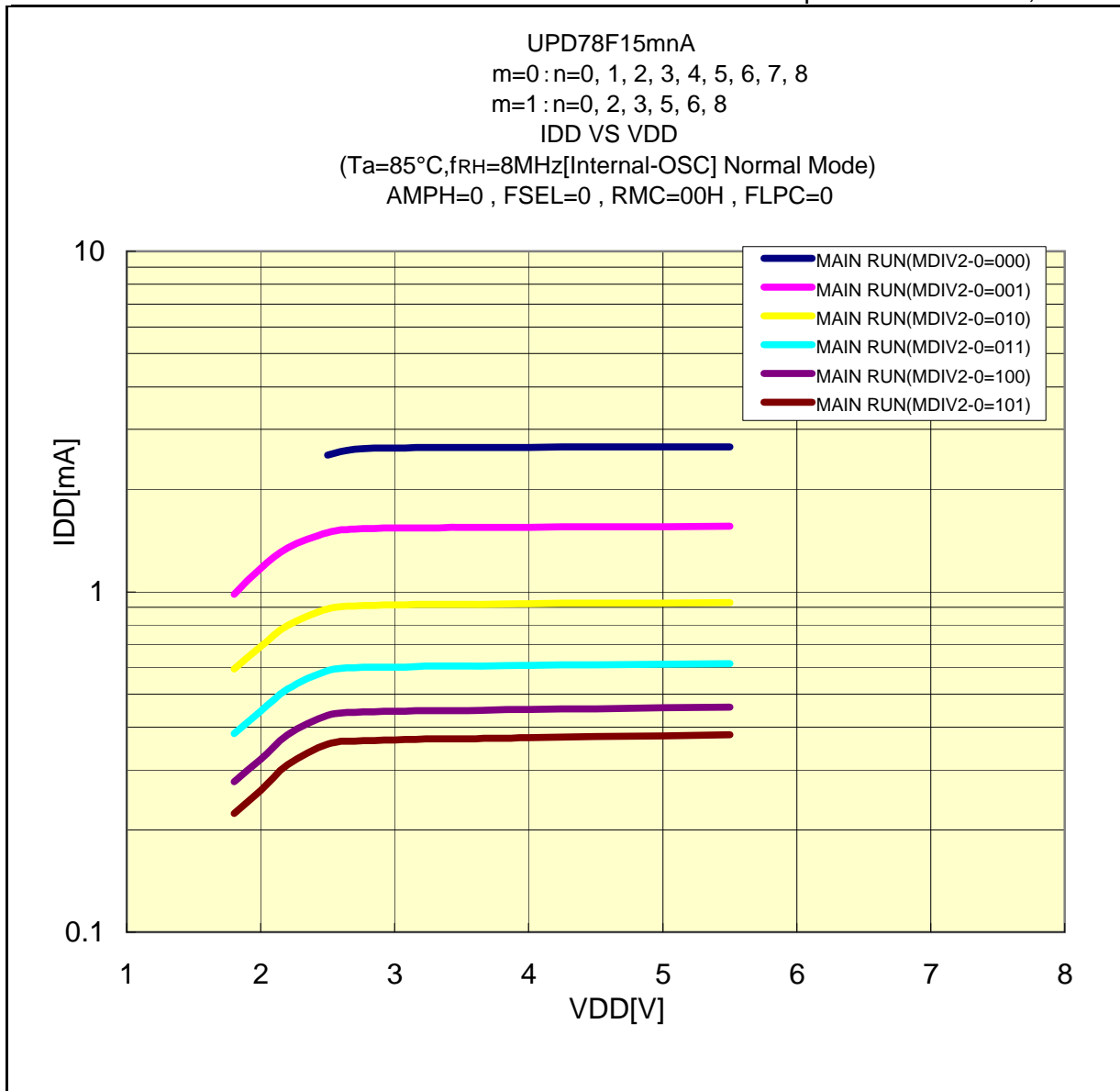
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/8MHz[Internal-OSC]) Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

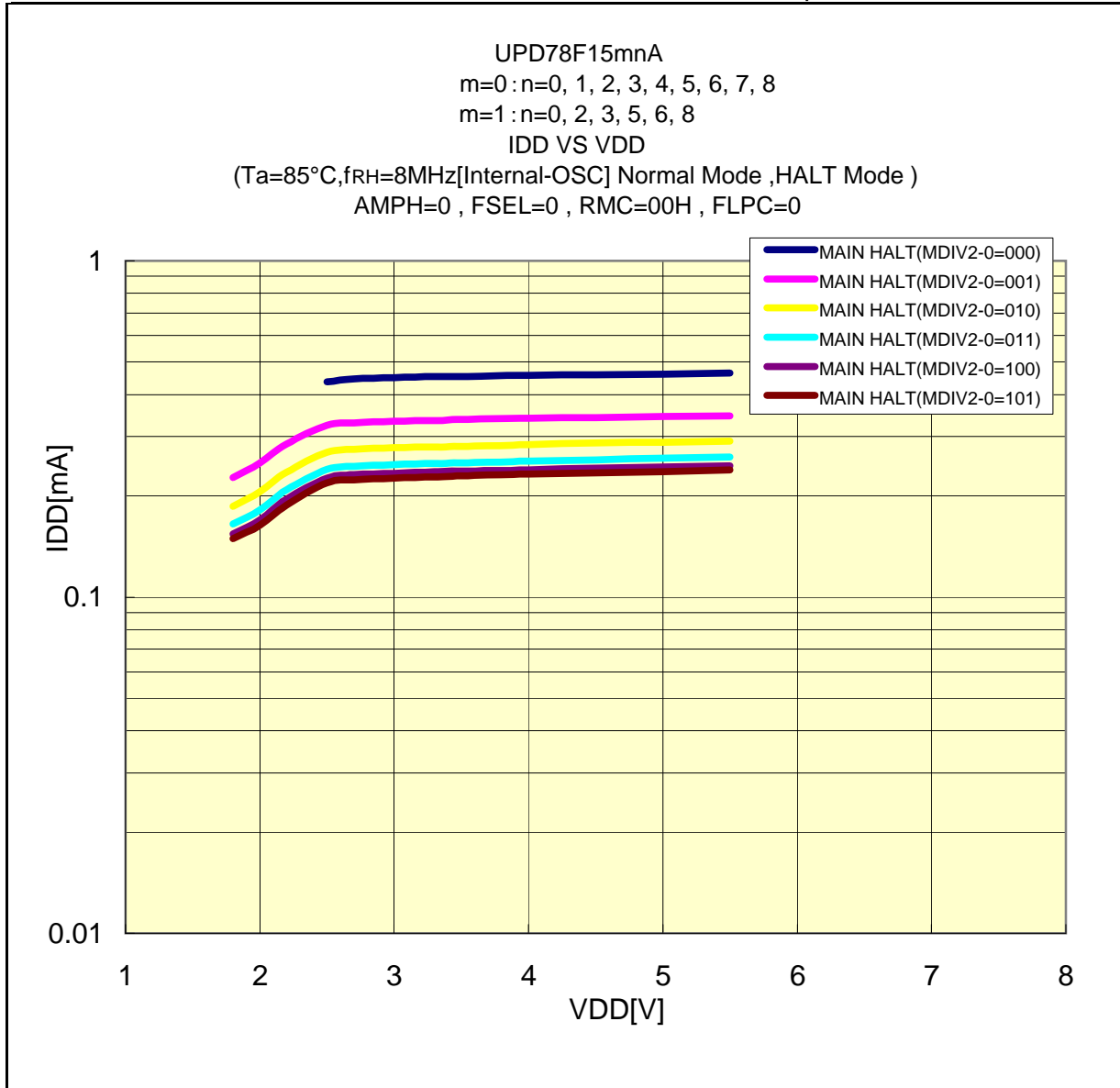
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/8MHz[Internal-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

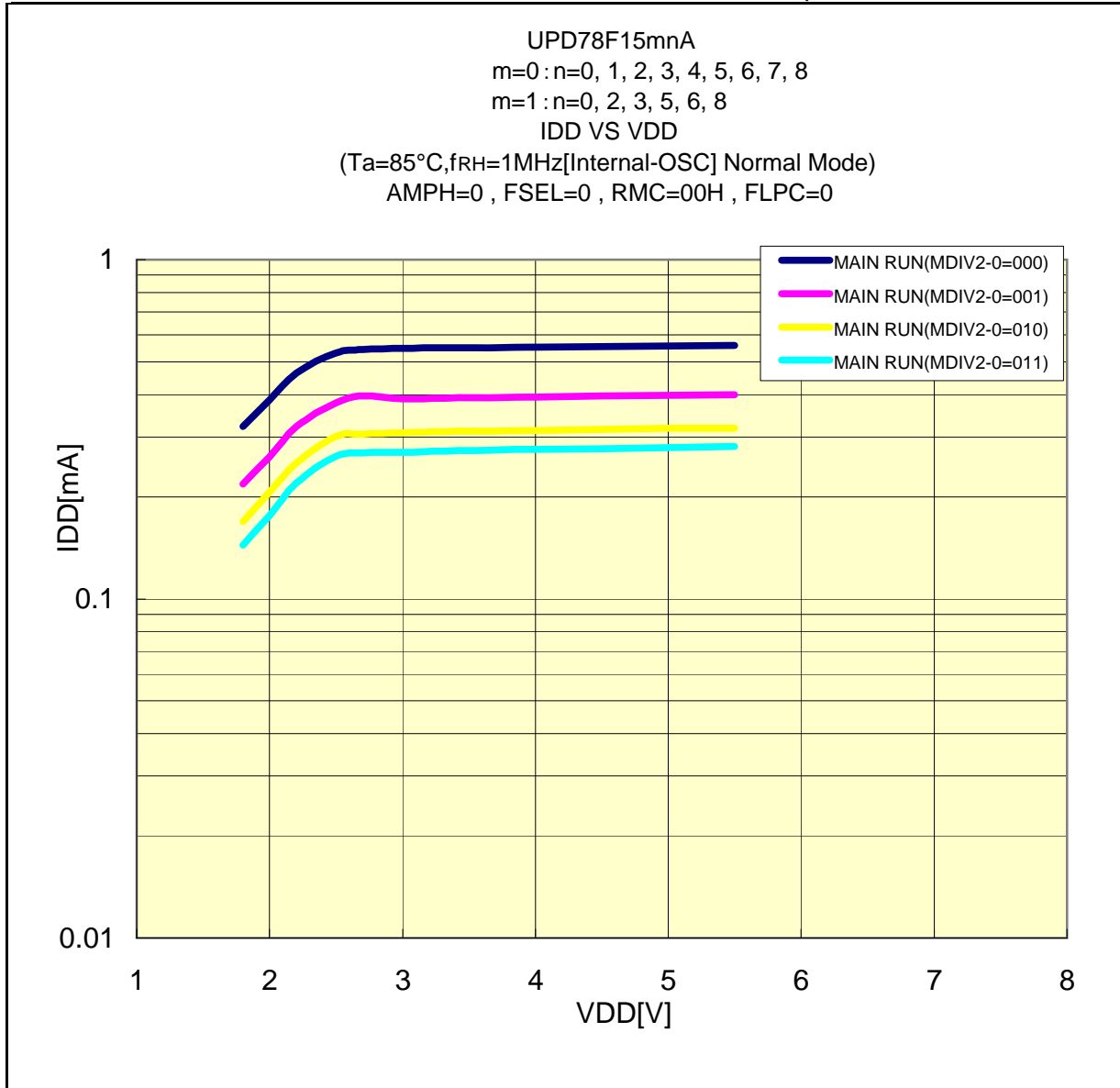
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/1MHz[Internal-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

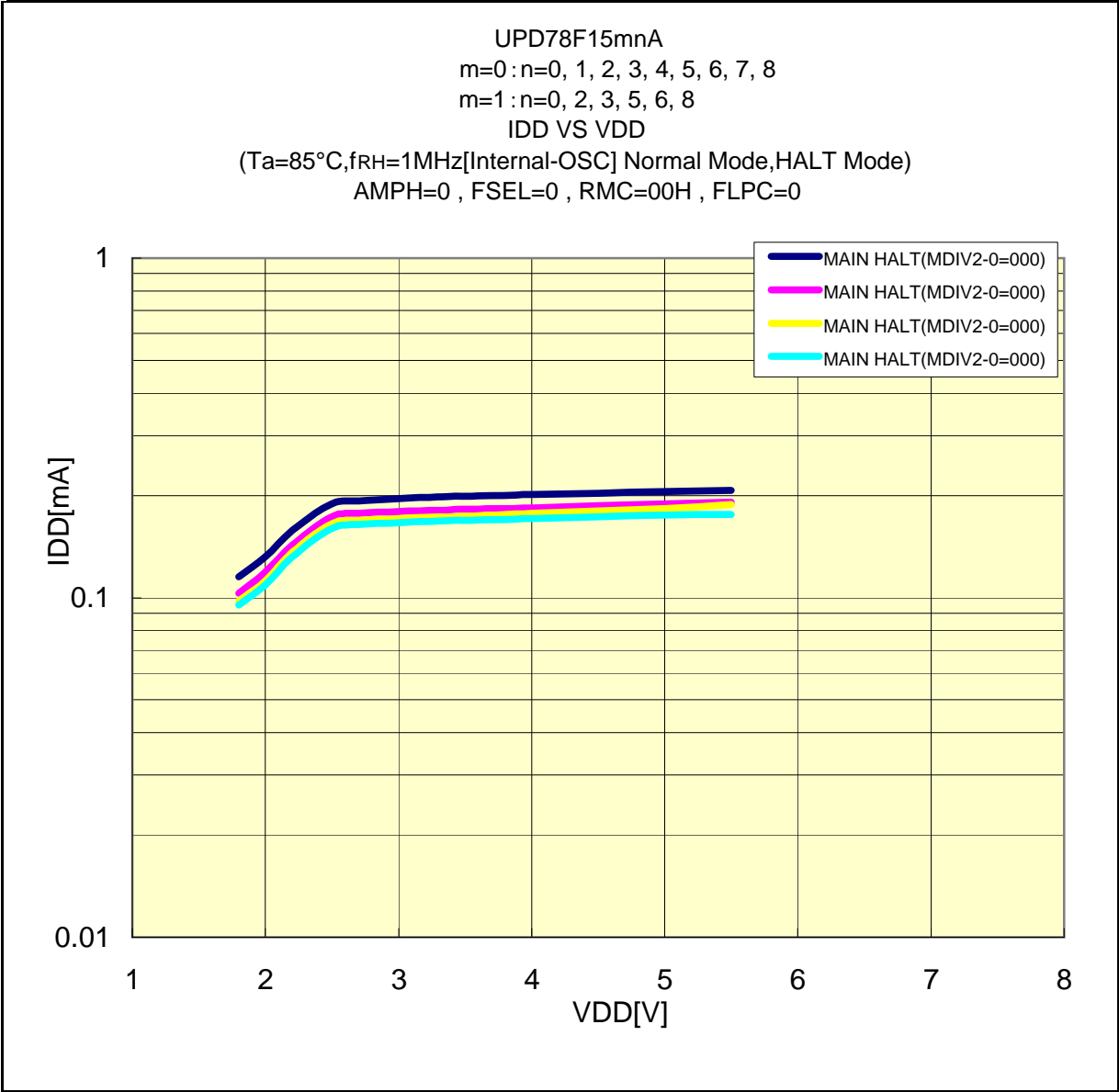
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(25°C/1MHz[Internal-OSC])
Normal Power Mode(HALT)**

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

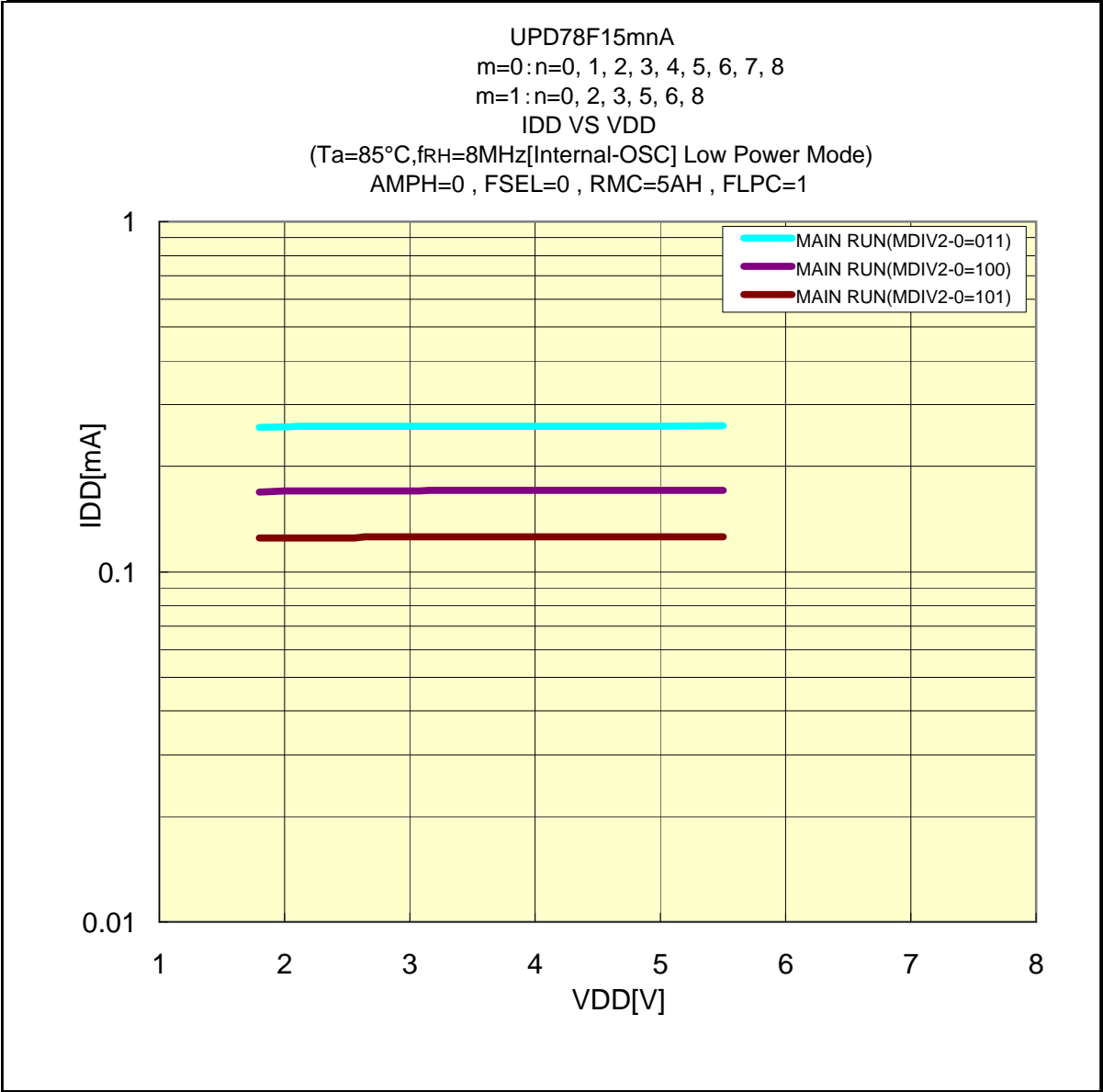
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(85°C/8MHz[Internal-OSC])
Low Power Mode**

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

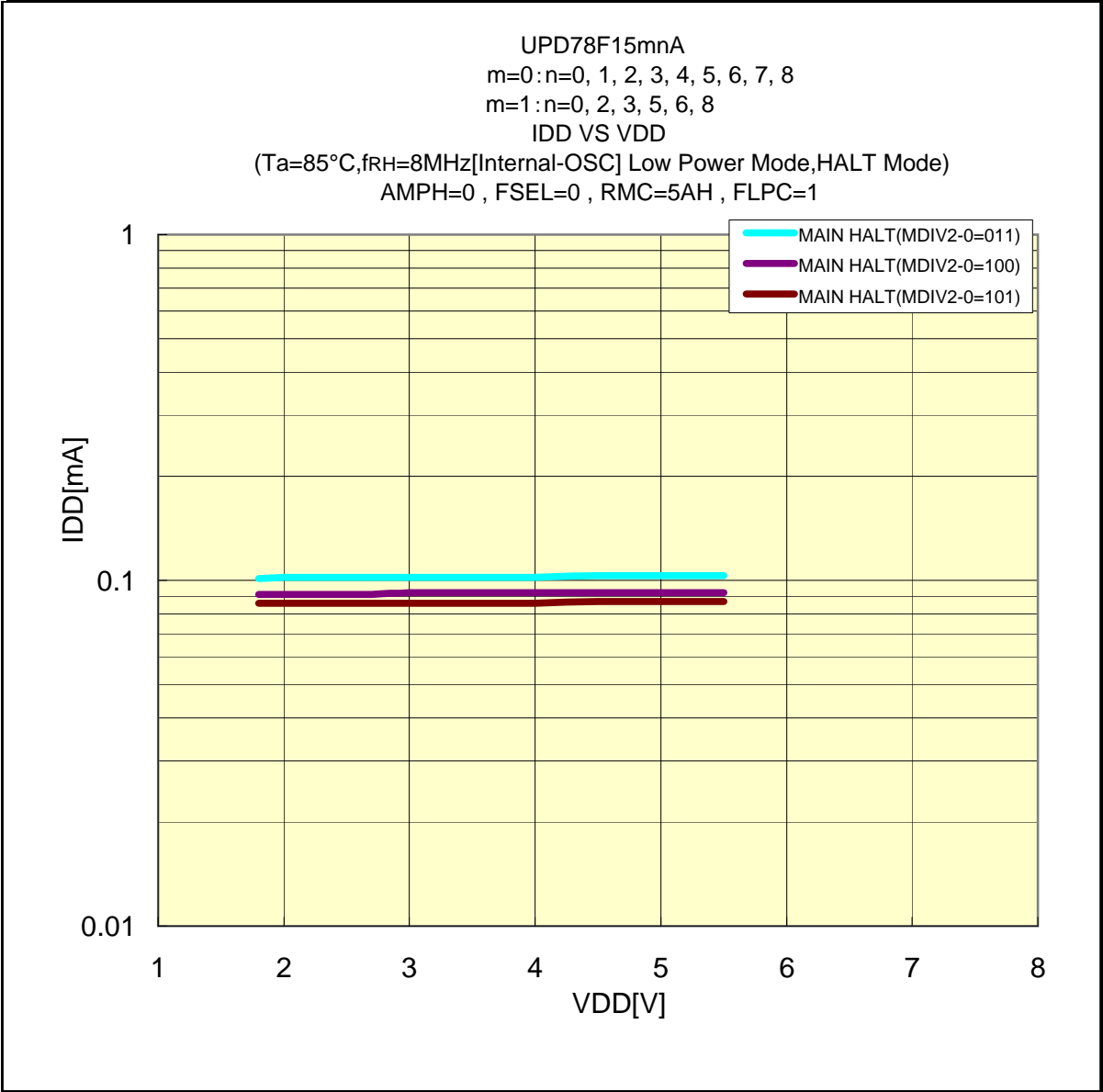
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(85°C/8MHz[Internal-OSC])
Low Power Mode(HALT)**

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

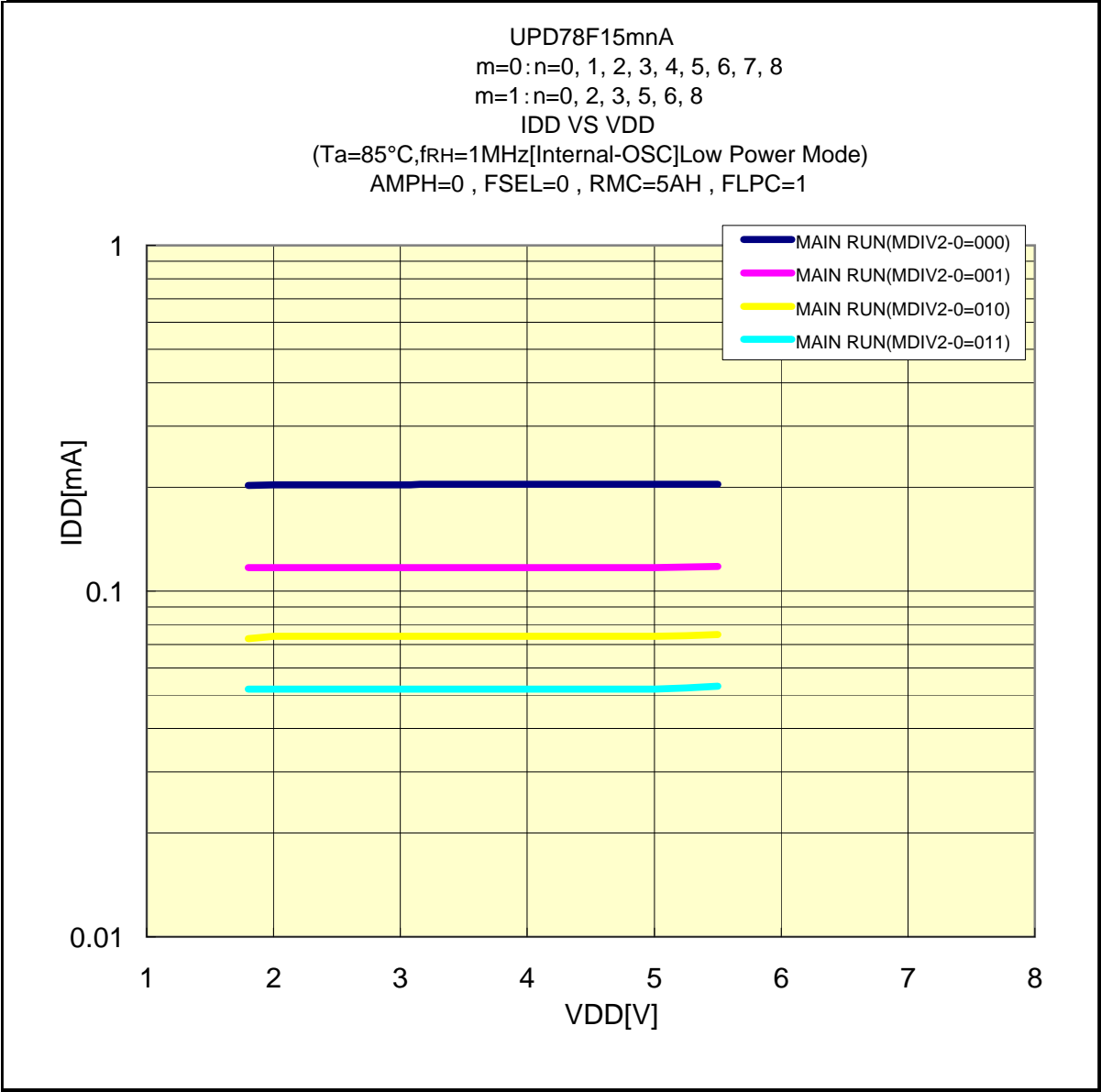
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

**IDD VS VDD(85°C/1MHz[Internal-OSC])
Low Power Mode**

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

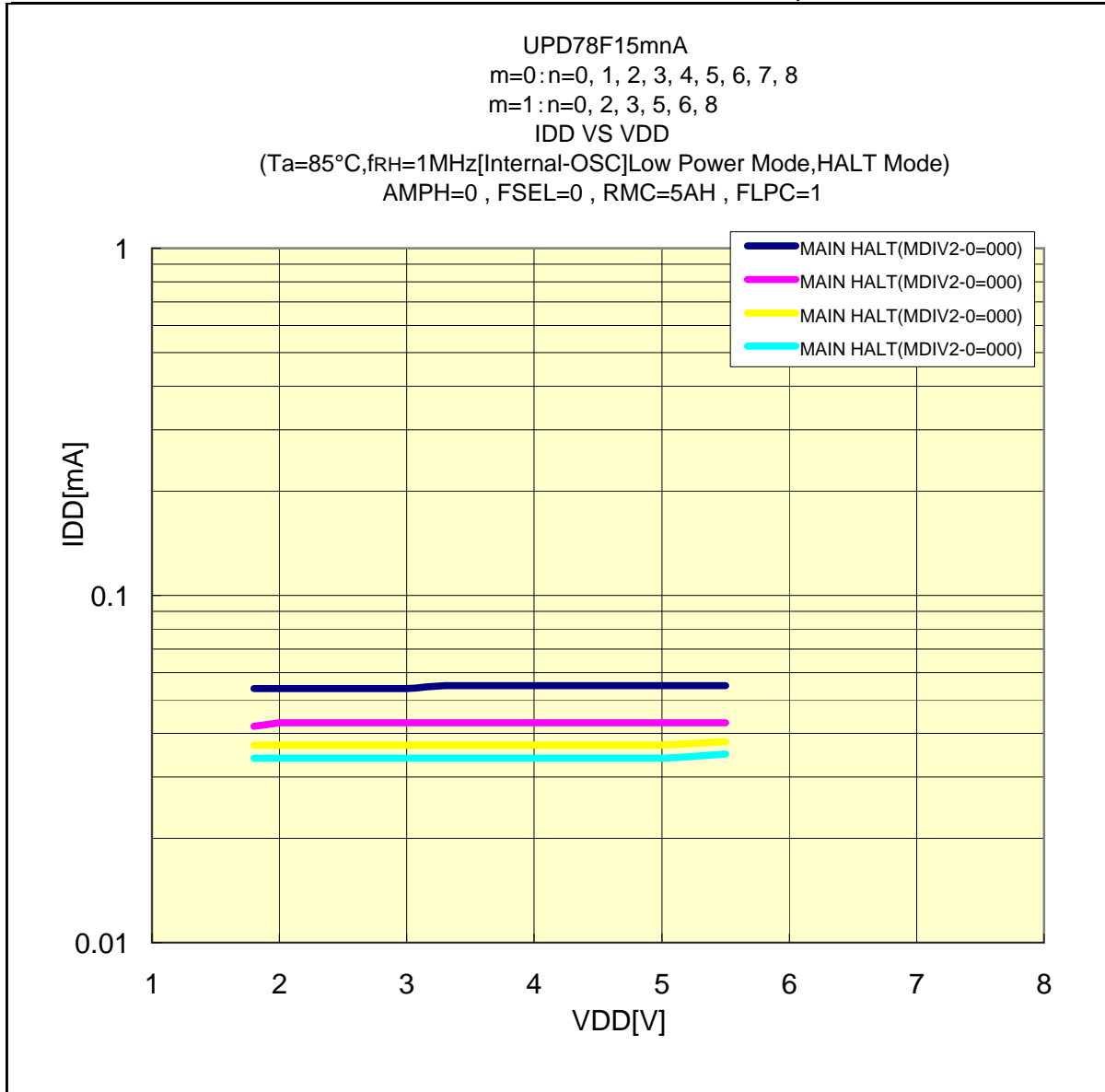
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/1MHz[Internal-OSC]) Low Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

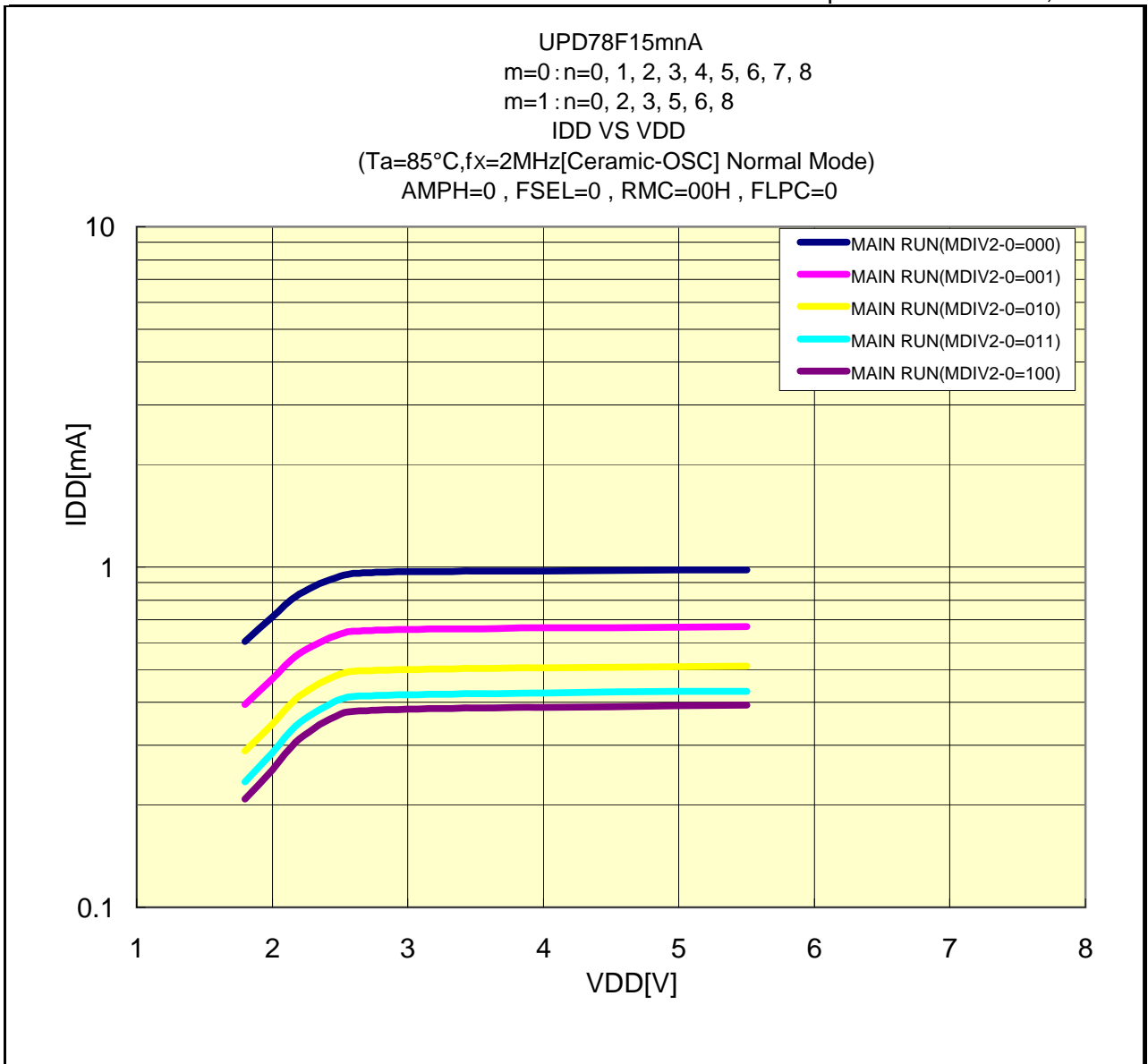
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/2MHz[Ceramic-OSC]) Normal Power Mode

Prepared on Oct. 11th, 2011



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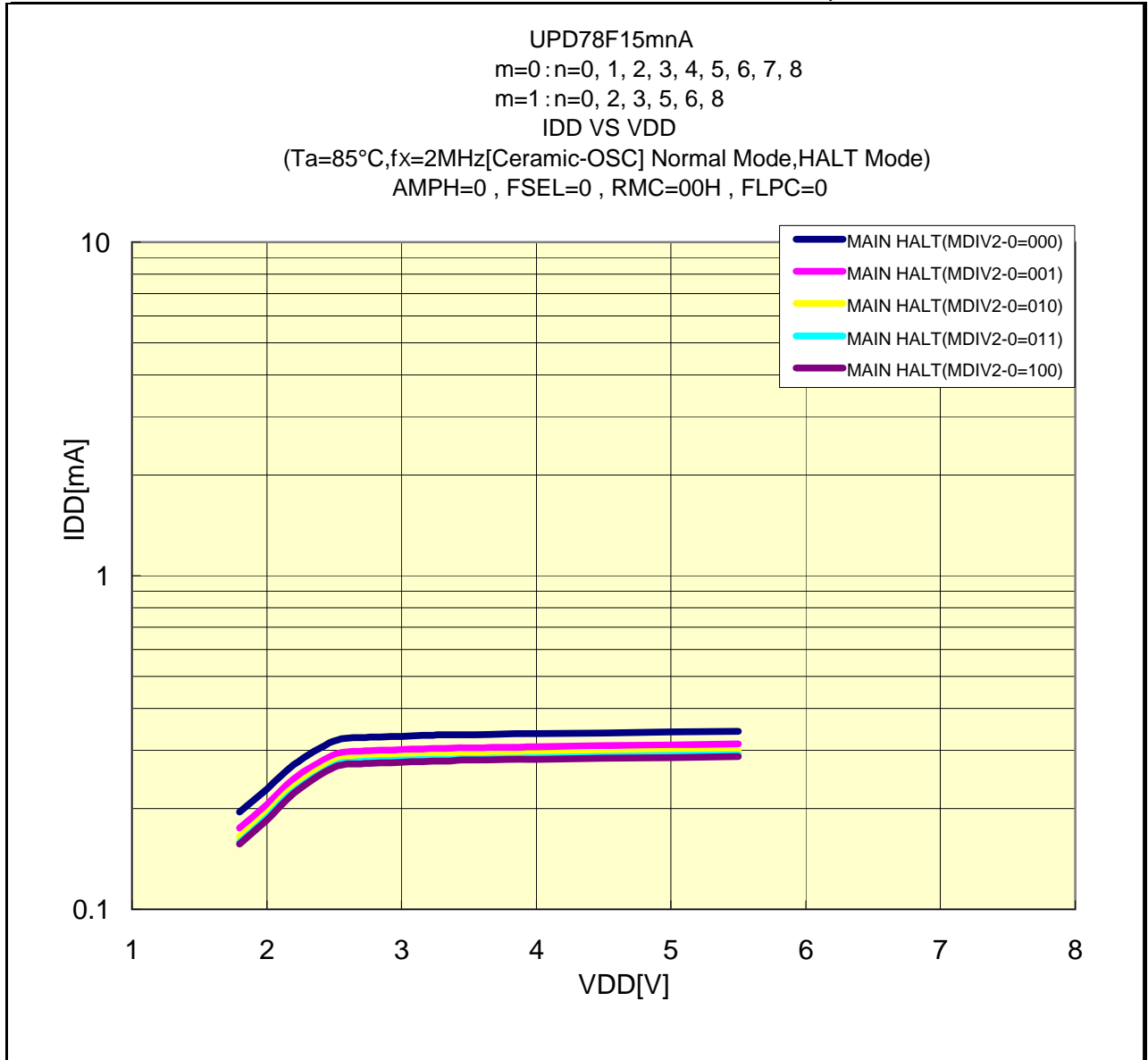
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/2MHz[Ceramic-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

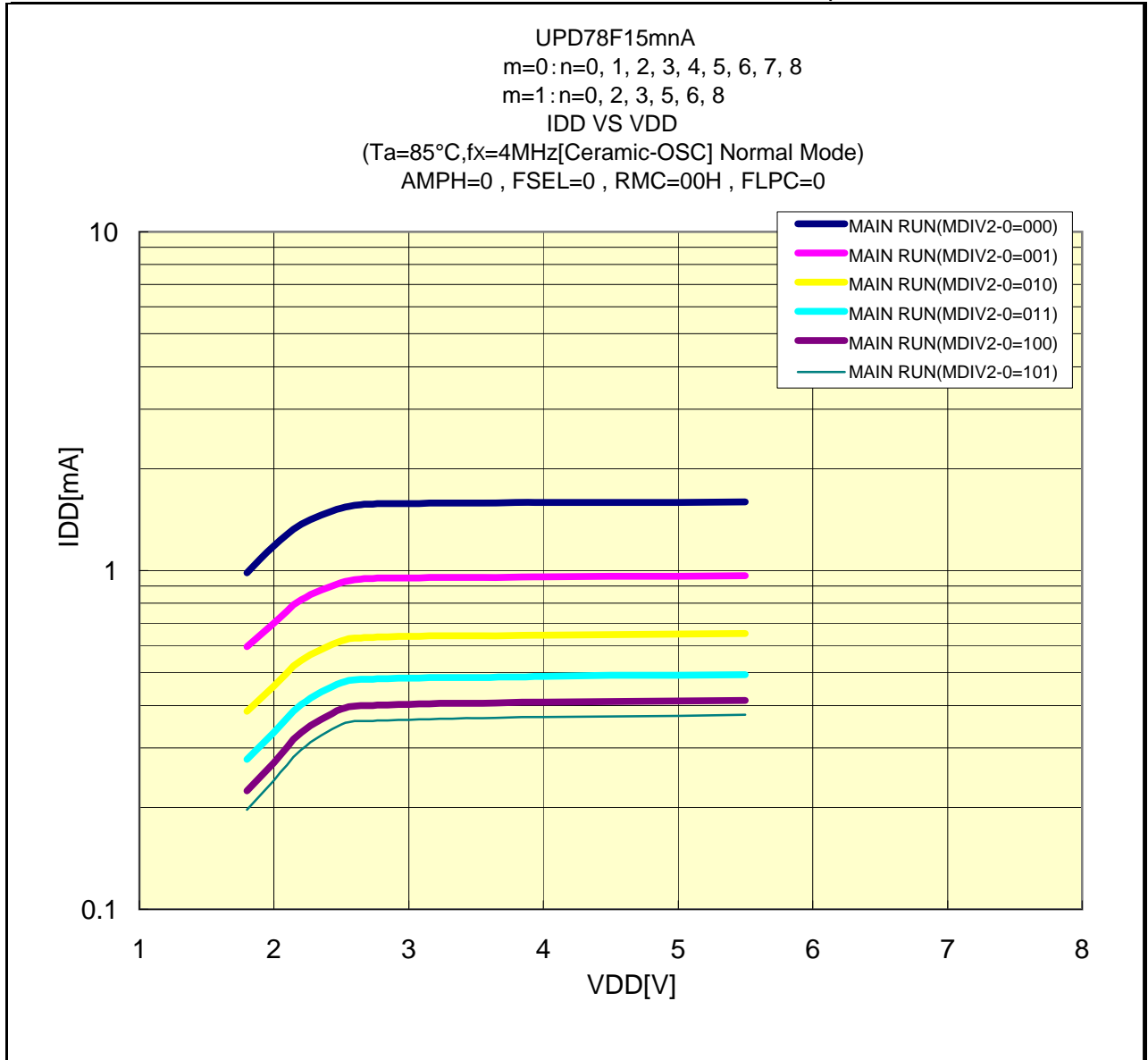
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/[4MHzCeramic-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



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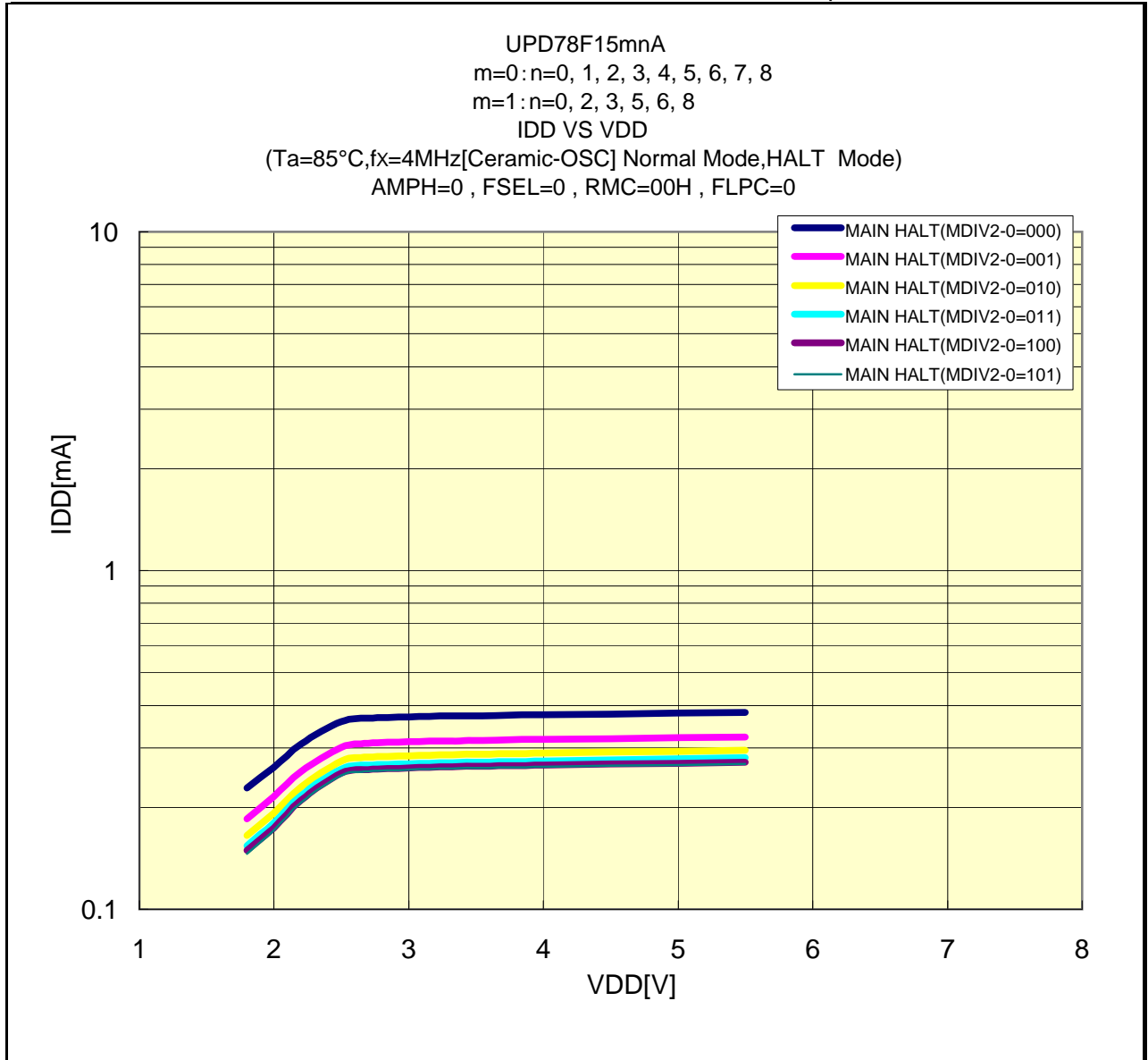
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/4MHz[Ceramic-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



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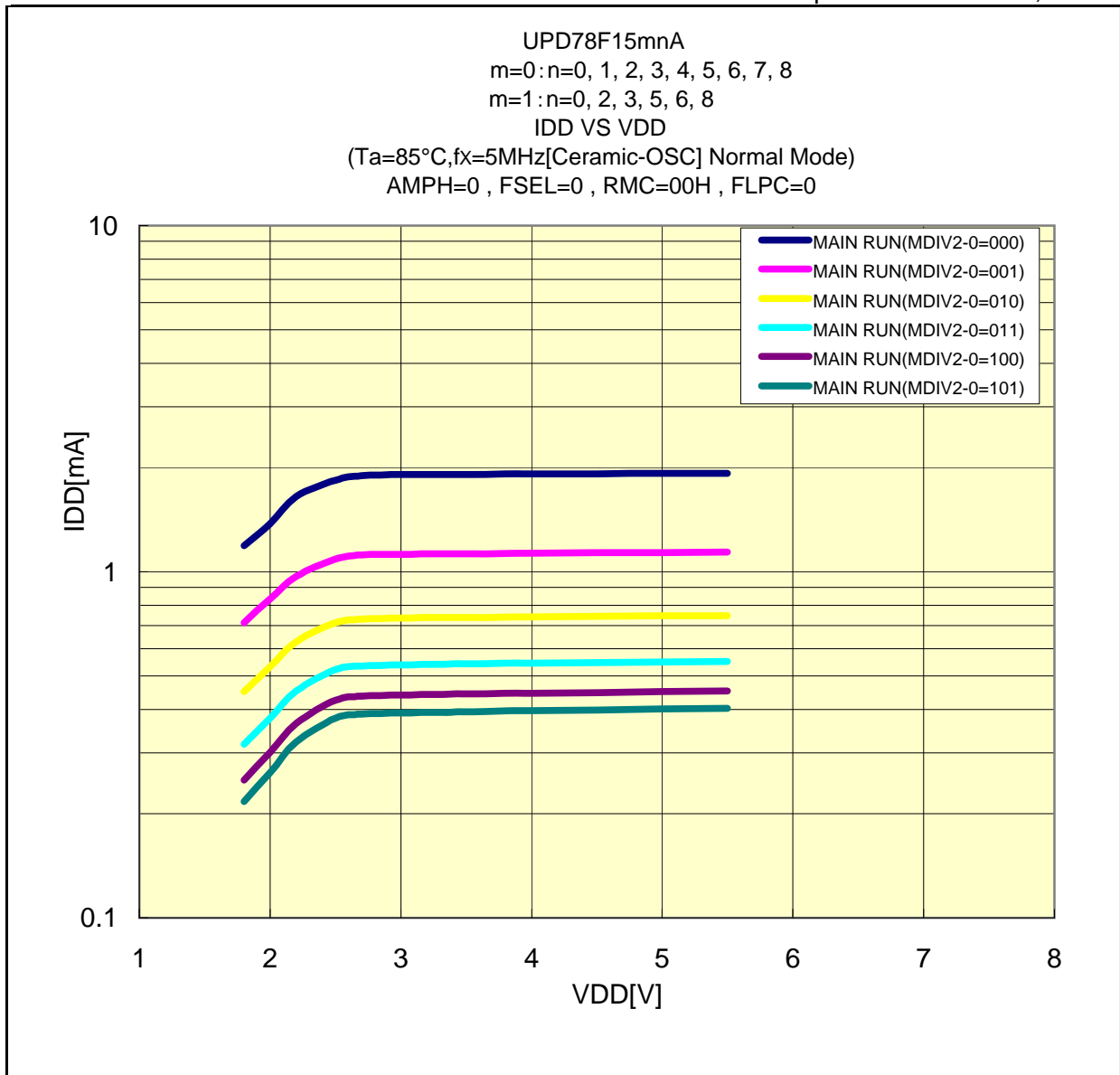
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/5MHz[Ceramic-OSC]) Normal Power Mode

Prepared on Oct. 11th, 2011



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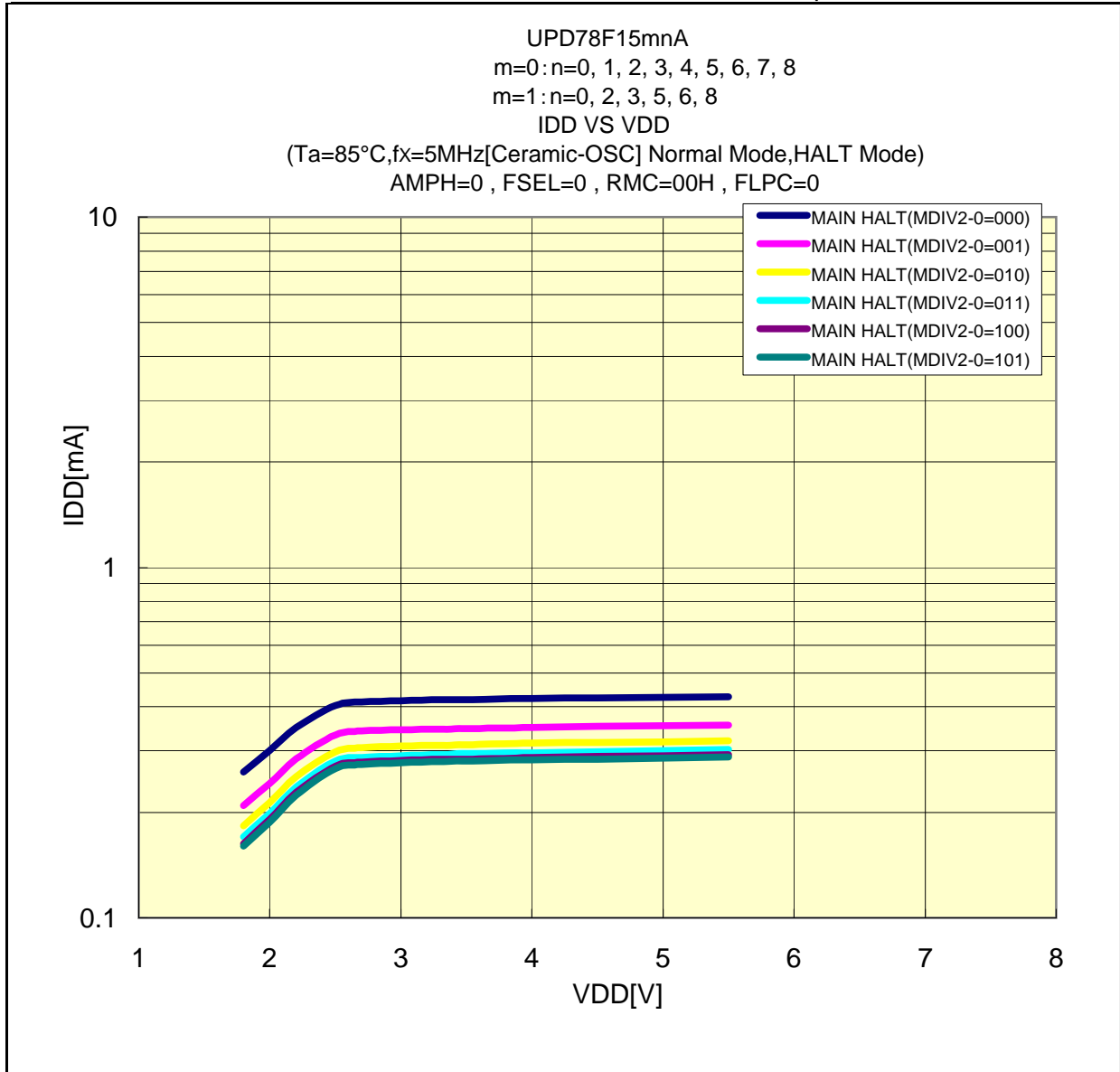
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/5MHz[Ceramic-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

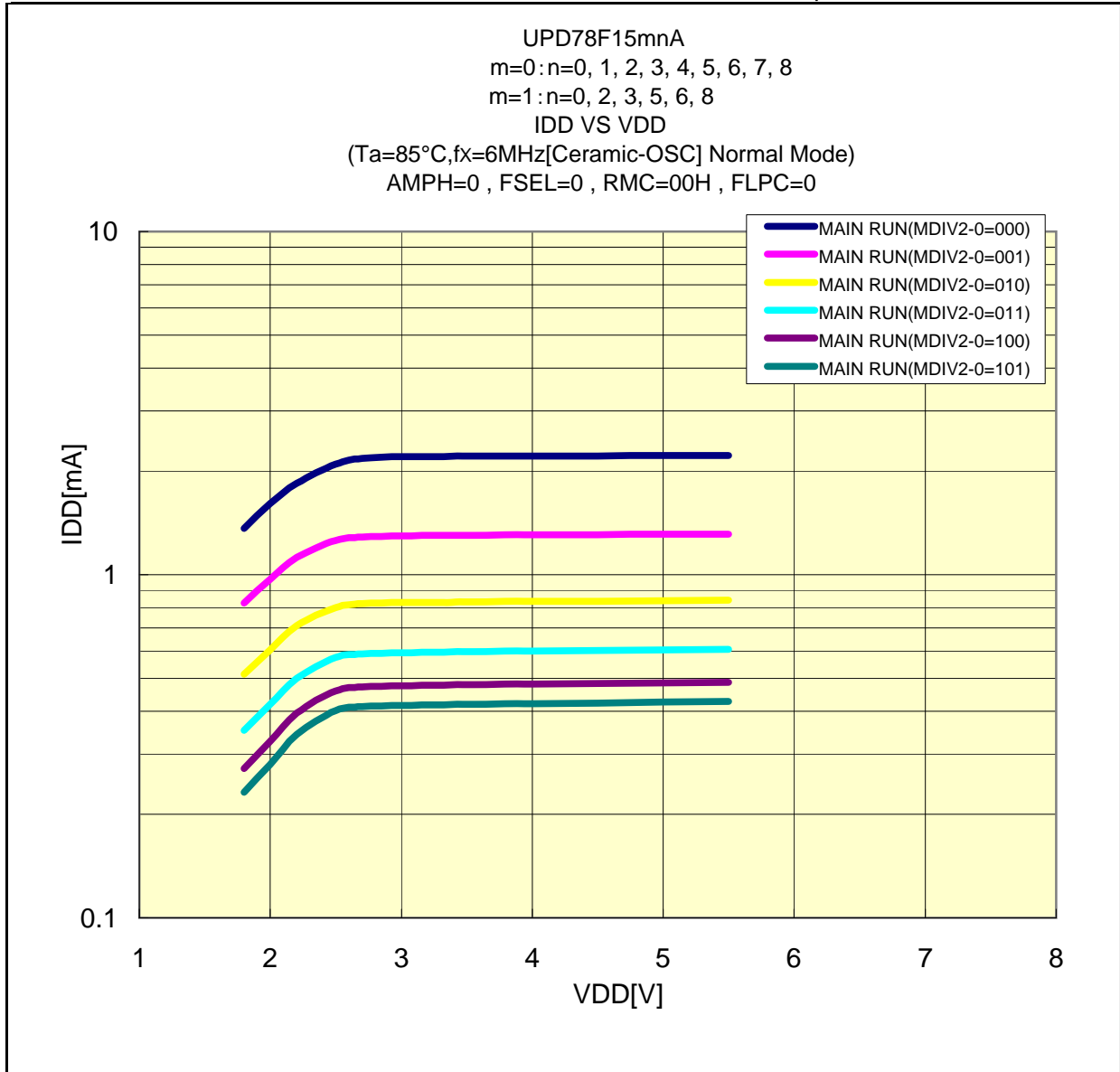
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/6MHz[Ceramic-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

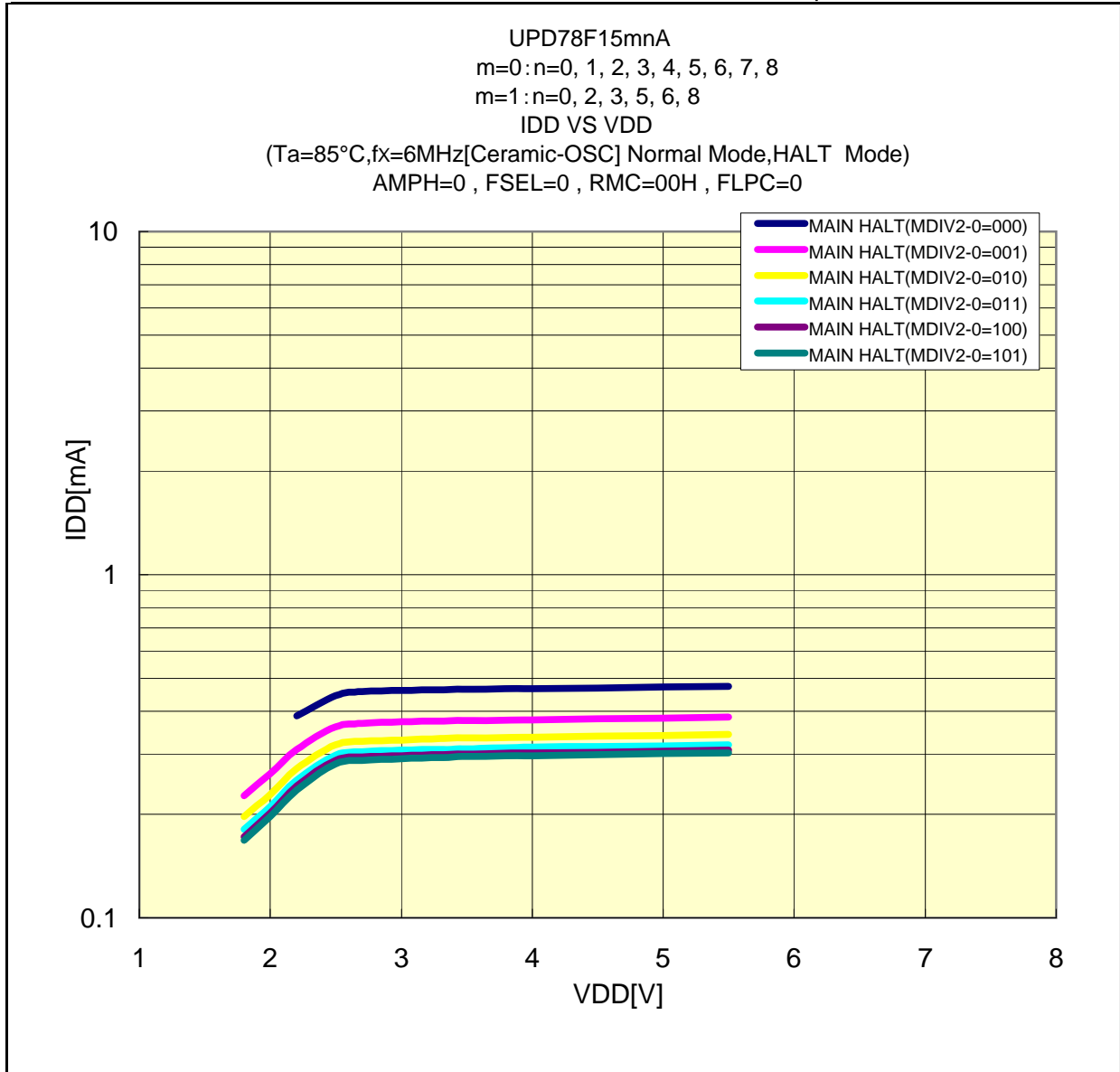
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/6MHz[Ceramic-OSC]) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

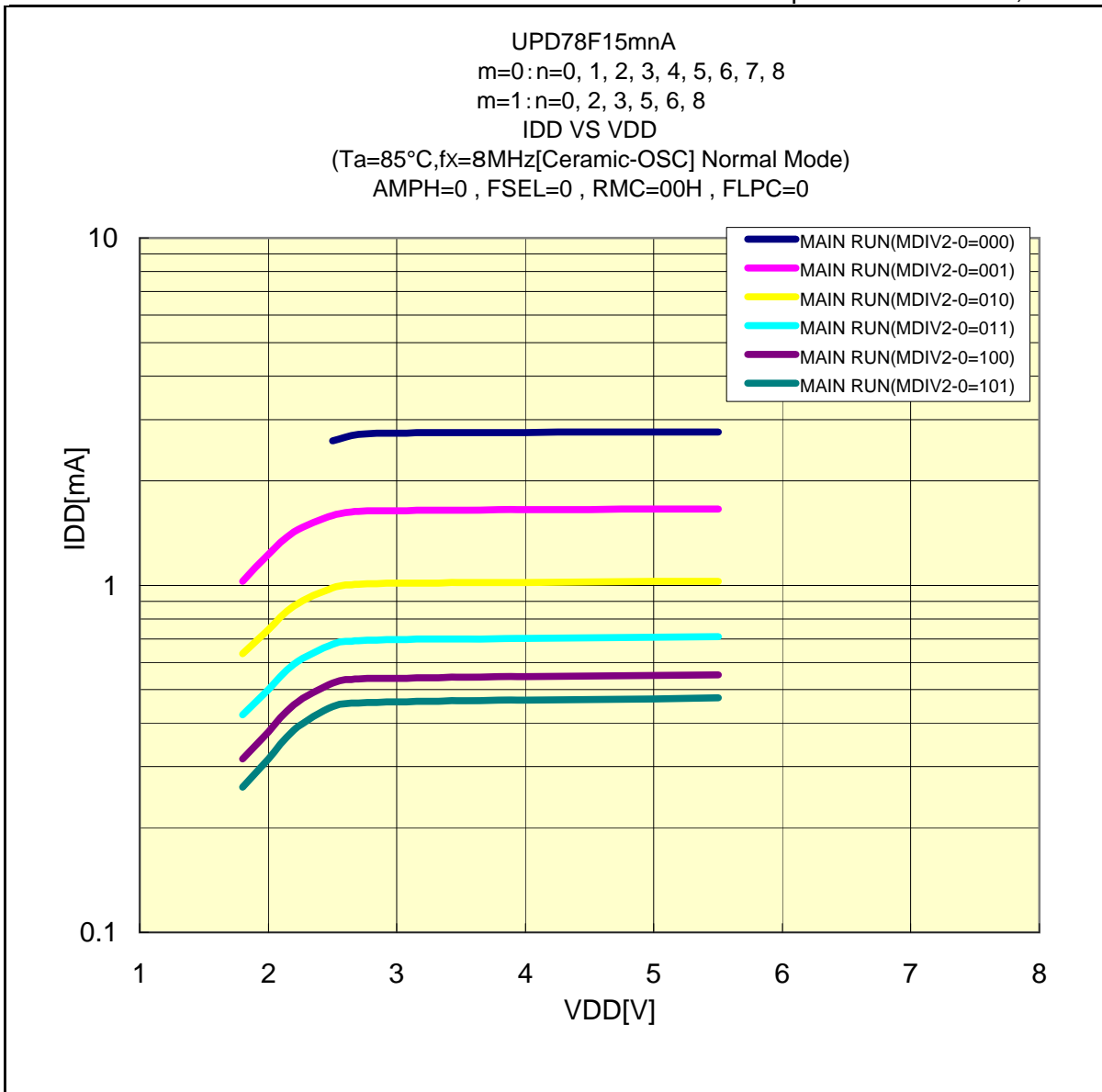
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/8MHz[Ceramic-OSC]) Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

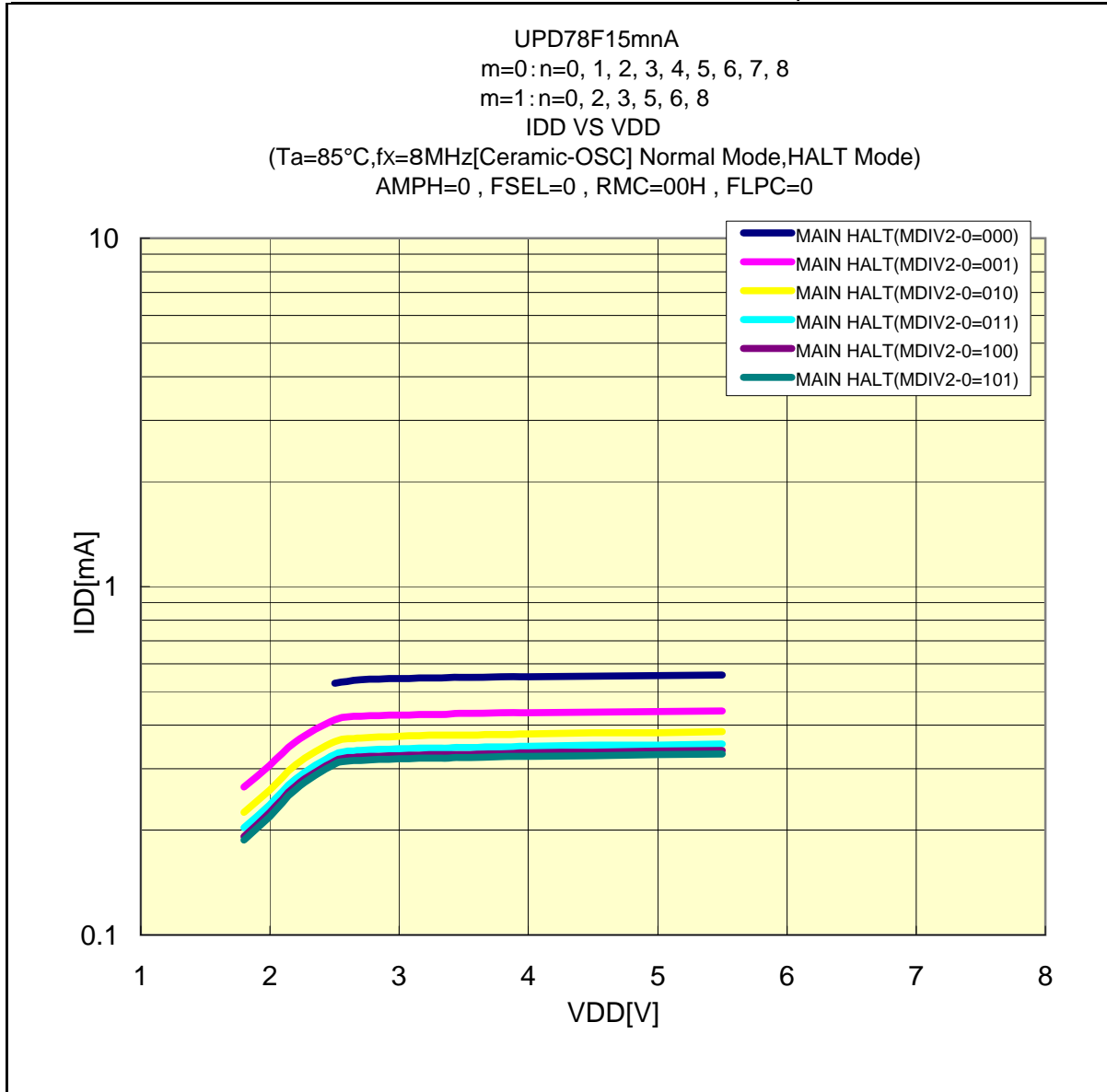
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/8MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

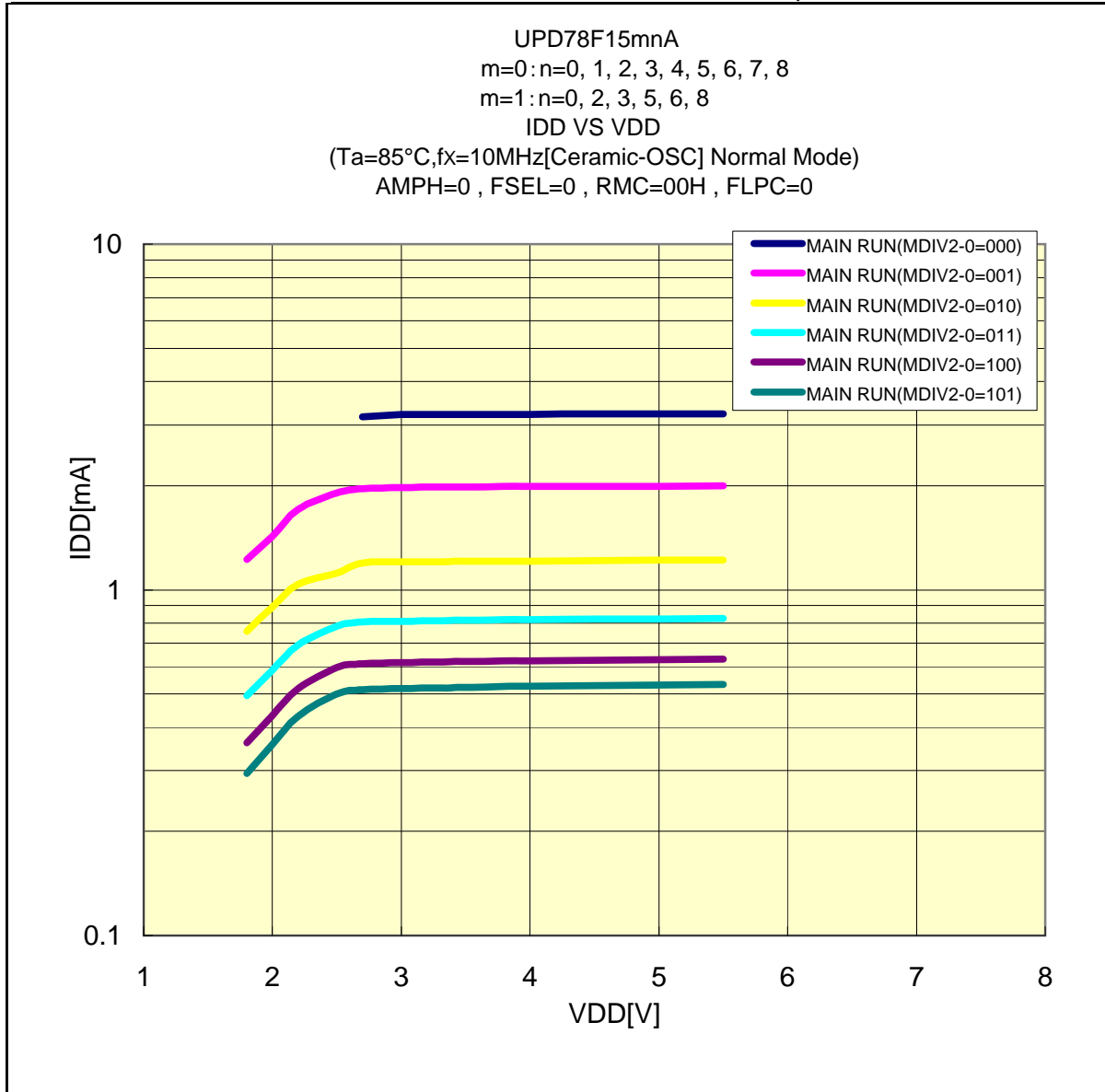
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/10MHz[Ceramic-OSC])

Normal Power Mode

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

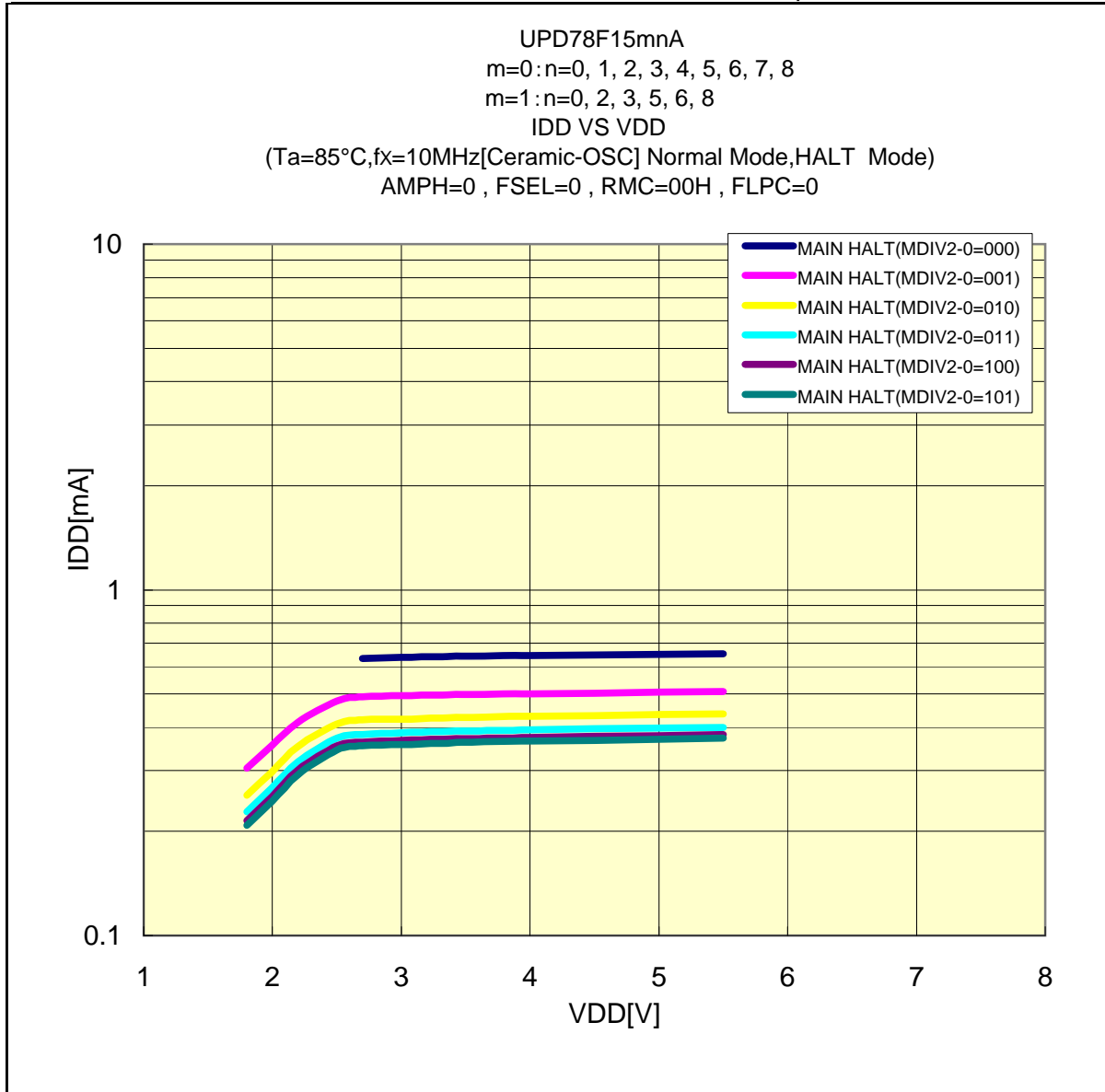
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/10MHzCeramic-OSC) Normal Power Mode(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

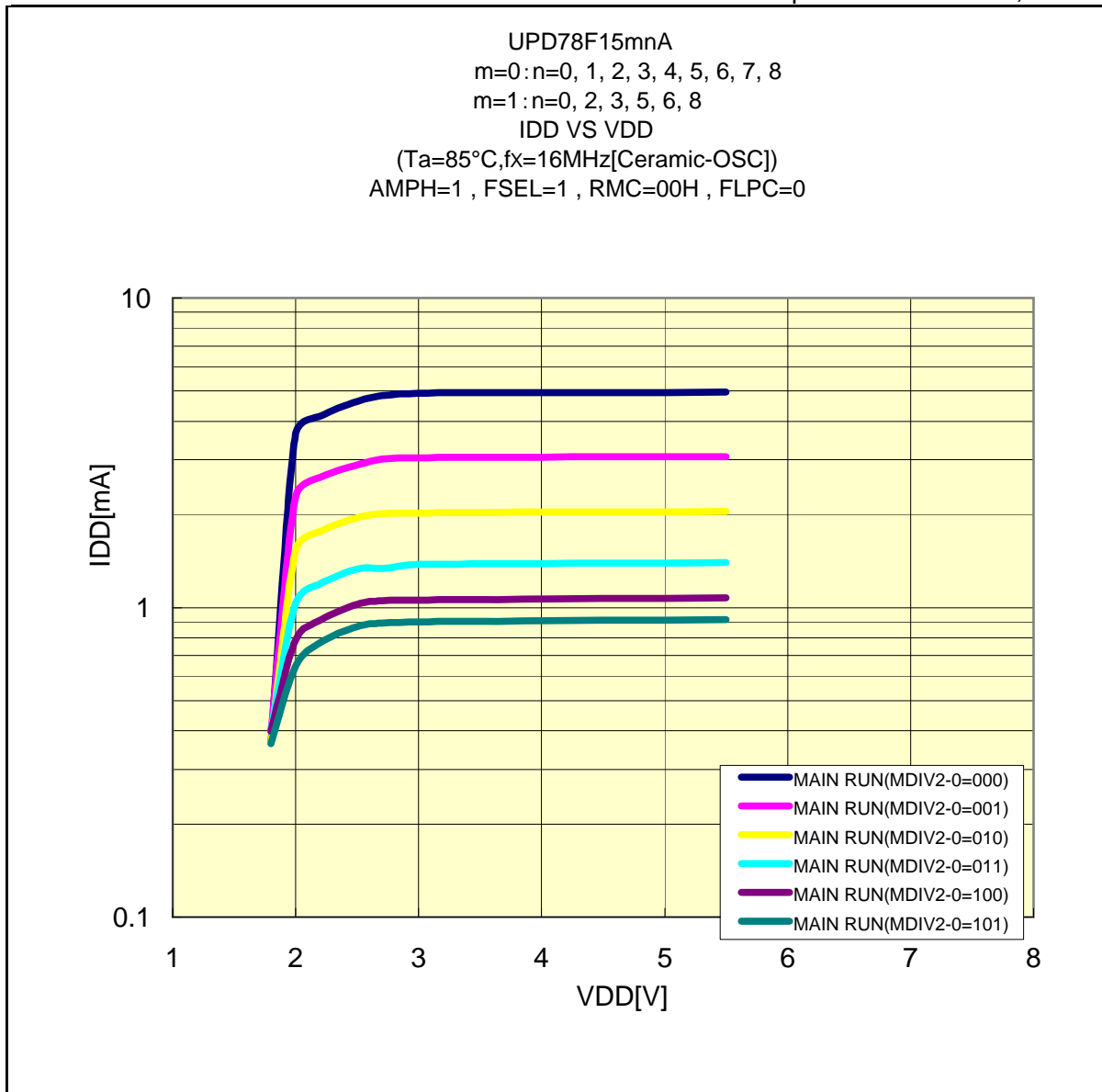
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/16MHz[Ceramic-OSC])

AMPH=1,FSEL=1

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

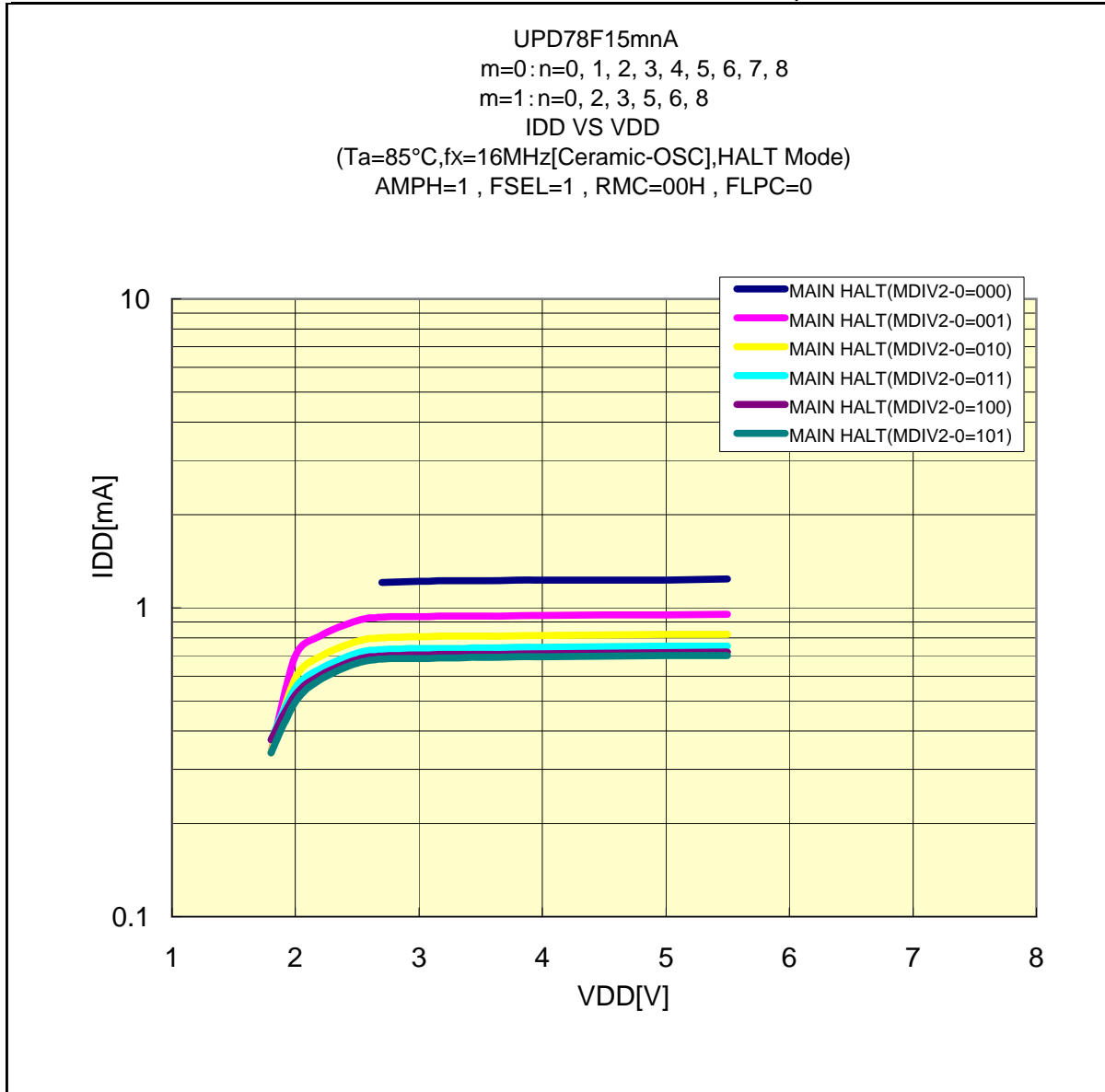
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/16MHz[Ceramic-OSC])

AMPH=1,FSEL=1(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

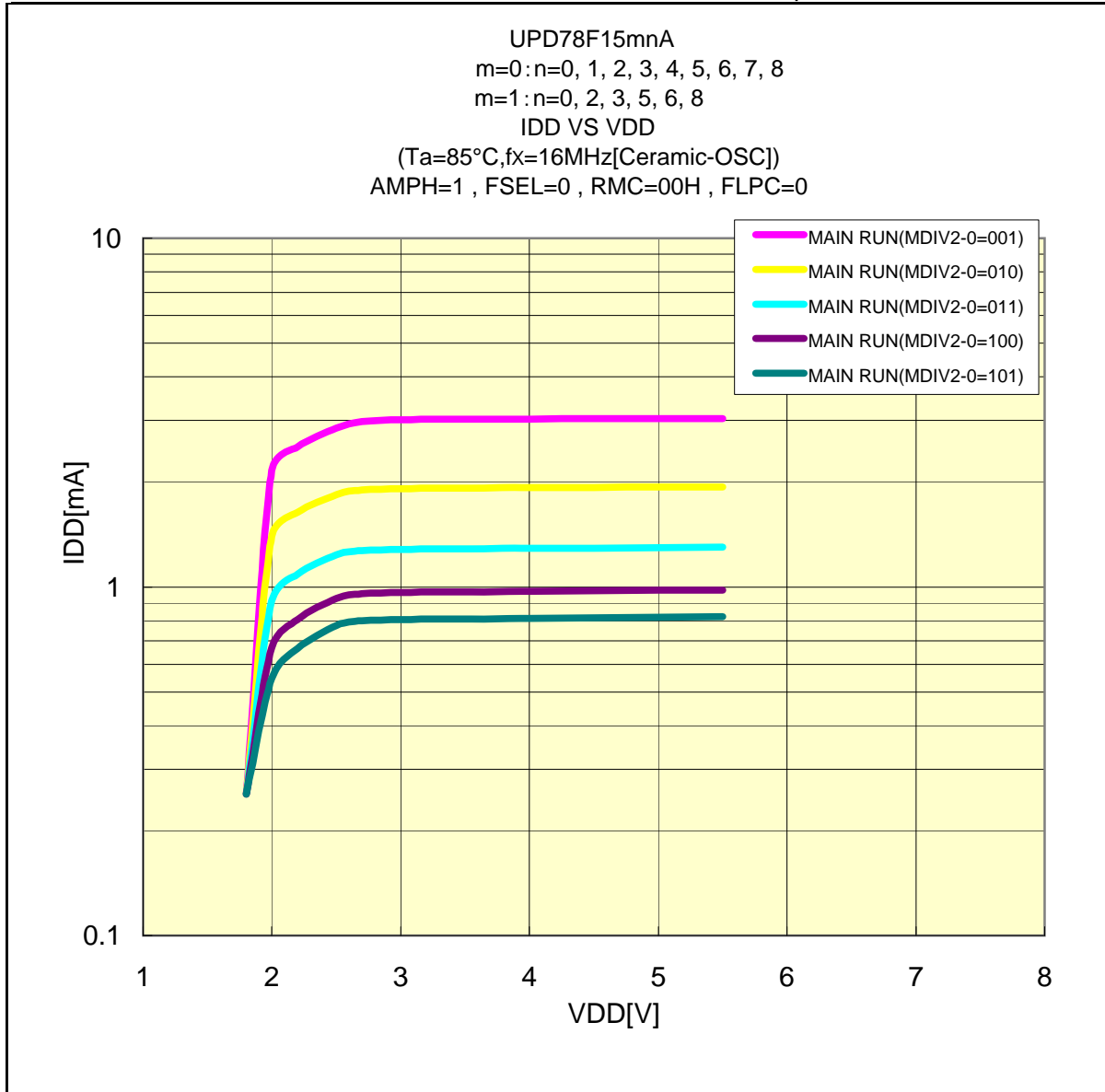
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/16MHz[Ceramic-OSC])

AMPH=1,FSEL=0

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

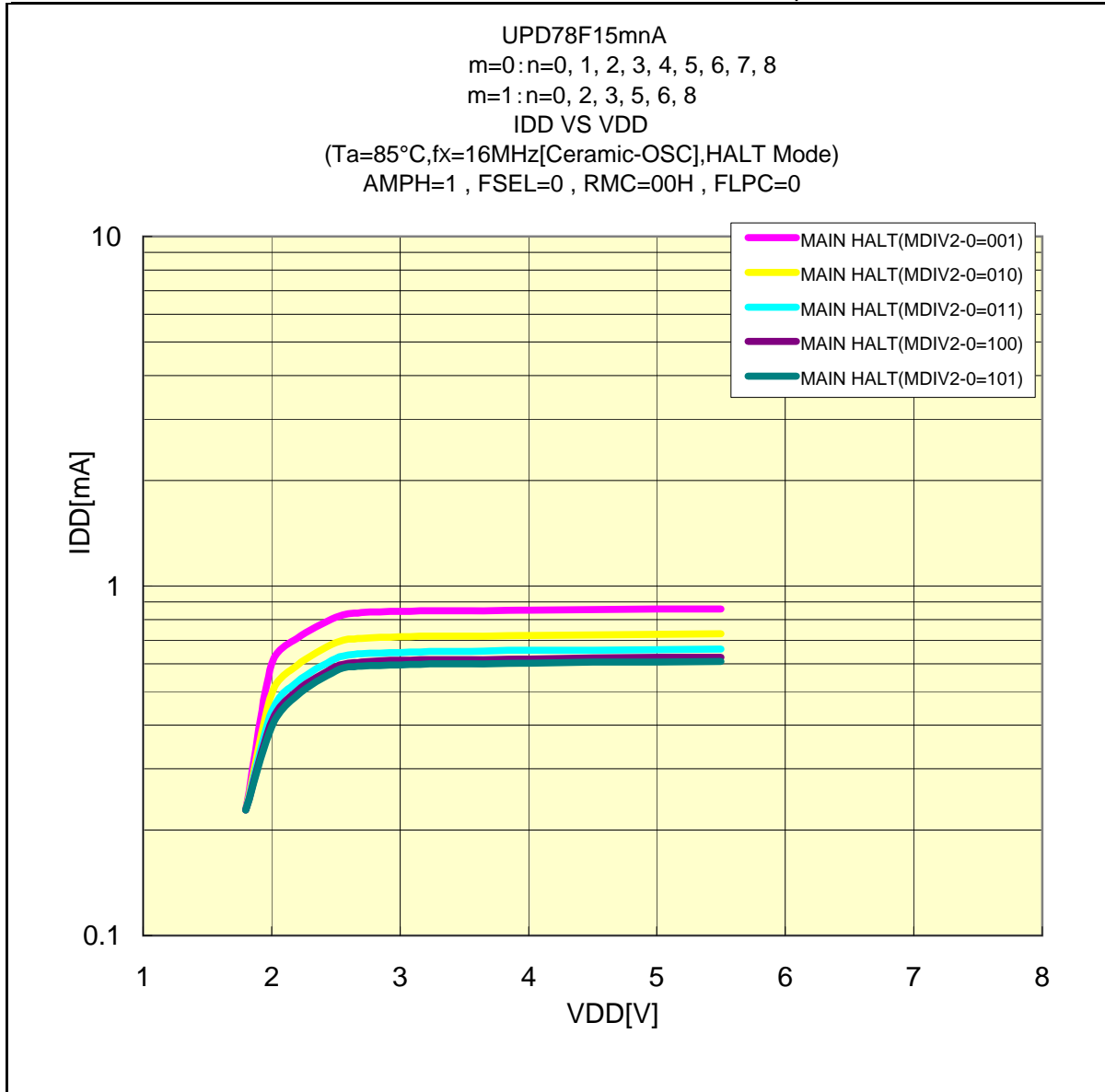
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(25°C/16MHz[Ceramic-OSC])

AMPH=1,FSEL=0(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

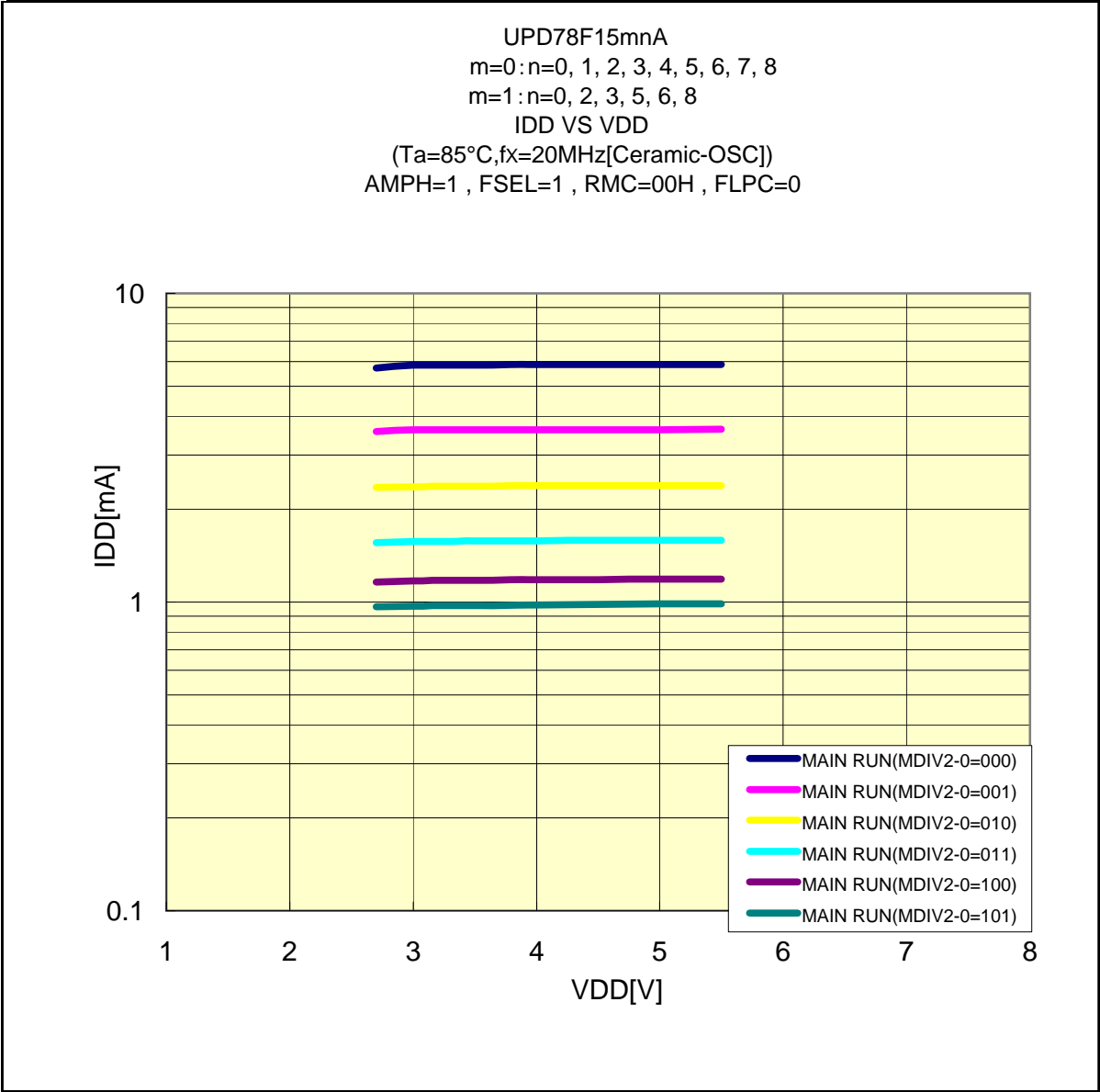
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/20MHz[Ceramic-OSC])
AMPH=1,FSEL=1

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

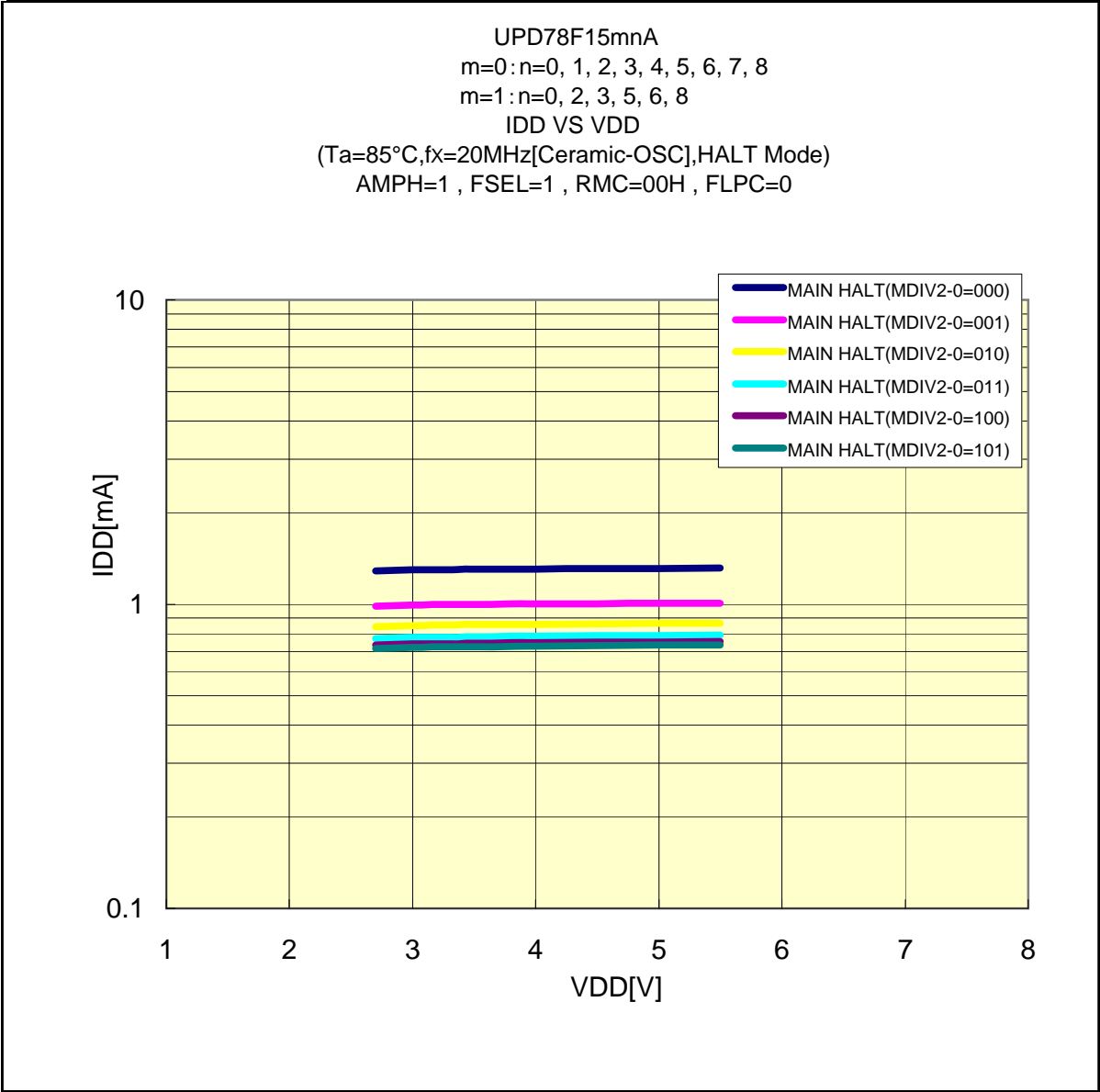
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/20MHz[Ceramic-OSC])
AMPH=1,FSEL=1(HALT)

Prepared on Oct. 11th, 2011



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UPD78F15mA

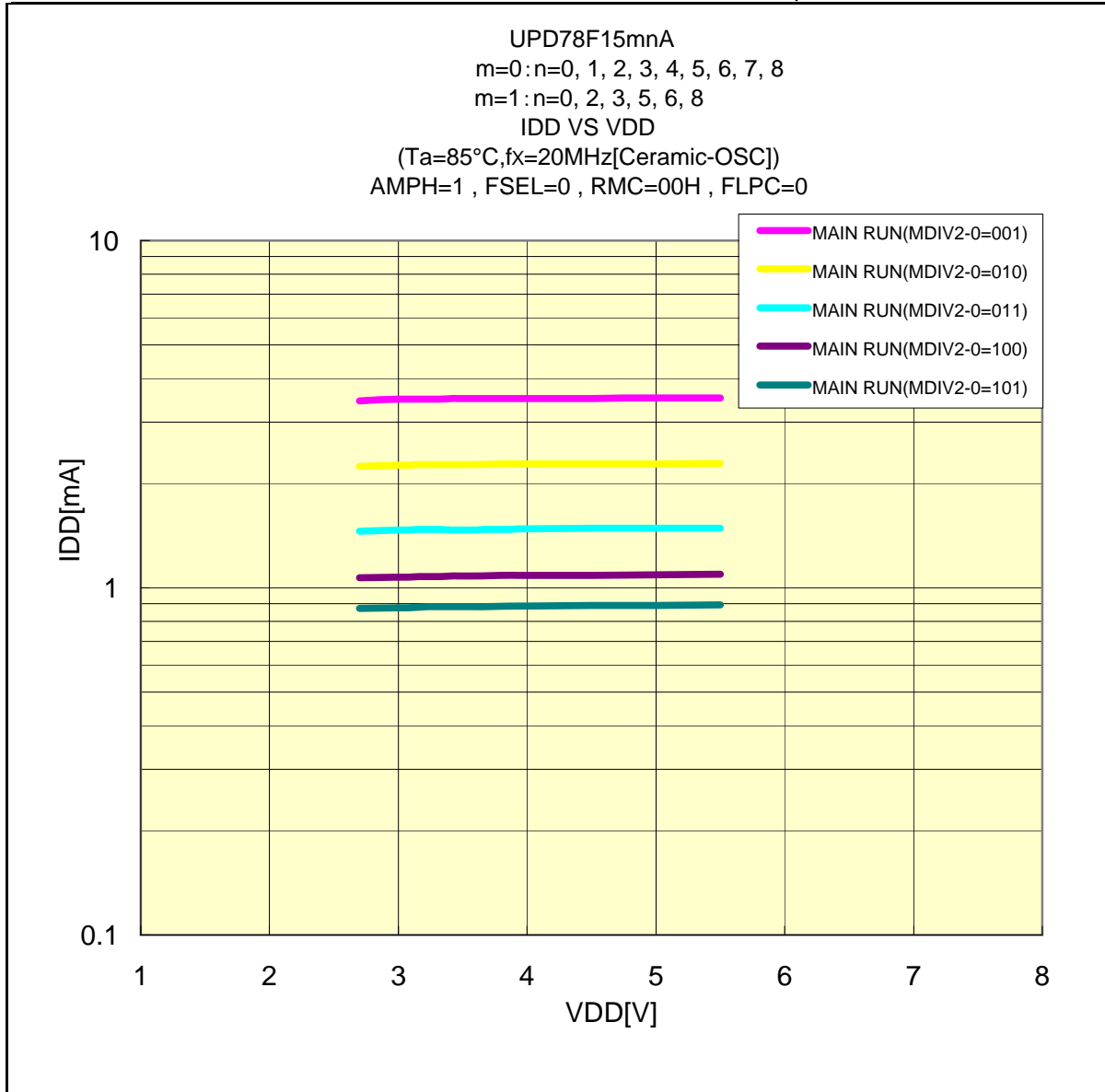
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/20MHz[Ceramic-OSC])

AMPH=1,FSEL=0

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

UPD78F15mA

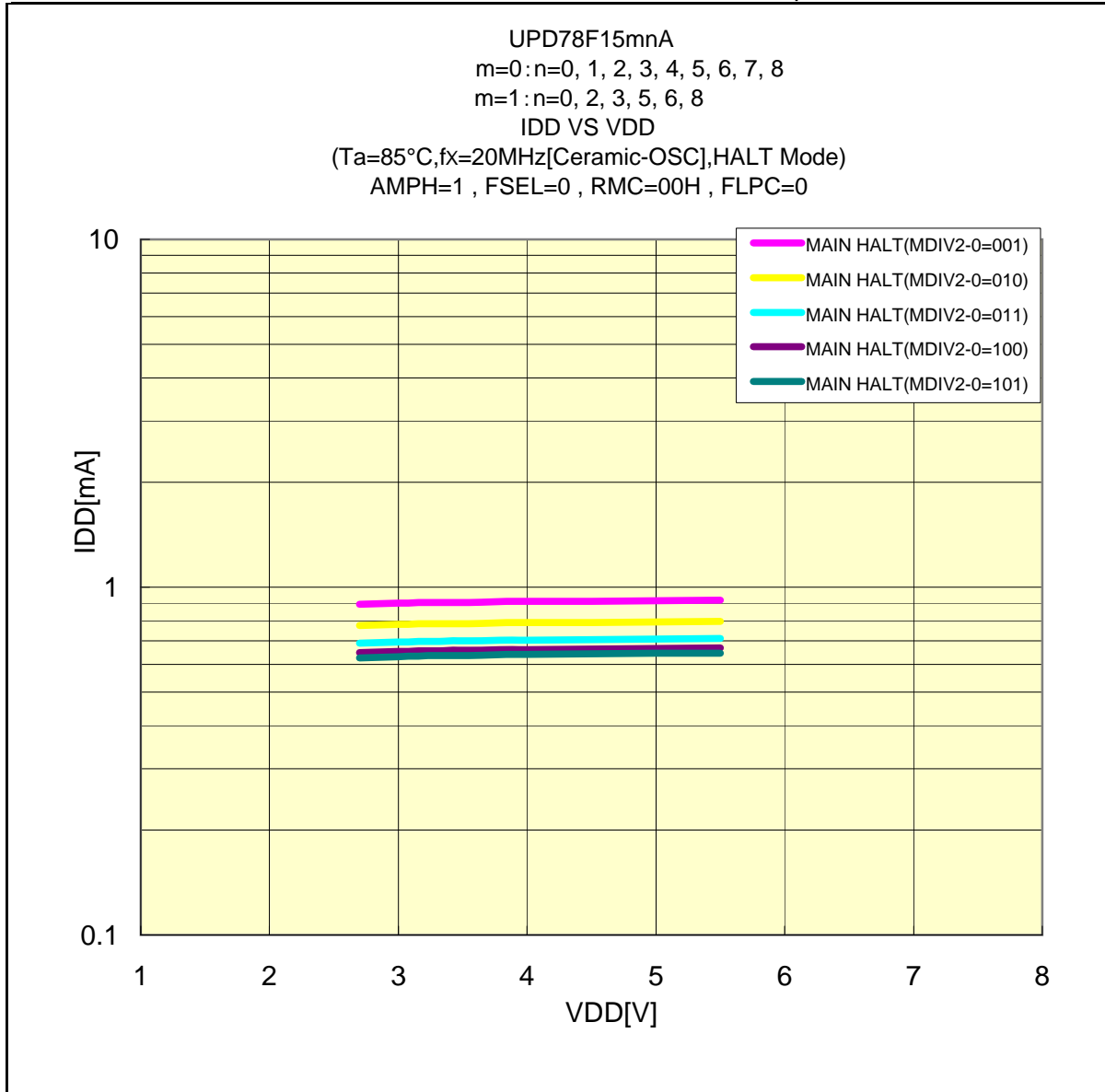
m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/20MHz[Ceramic-OSC])

AMPH=1,FSEL=0(HALT)

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.

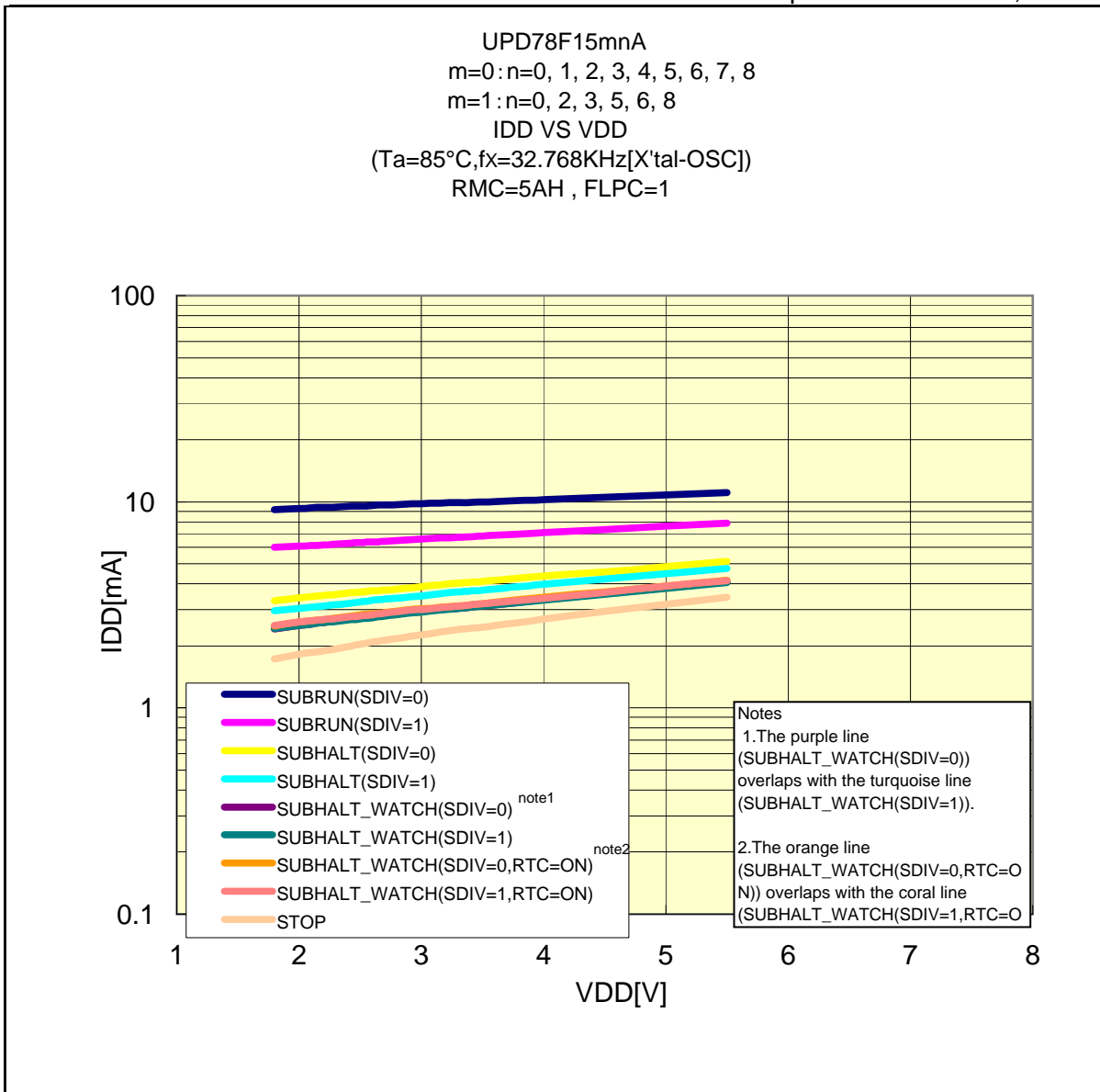
UPD78F15mA

m=0 : n=0, 1, 2, 3, 4, 5, 6, 7, 8

m=1 : n=0, 2, 3, 5, 6, 8

IDD VS VDD(85°C/32.768KHz[X'tal-OSC])

Prepared on Oct. 11th, 2011



The above mentioned value is only for your reference. The value was measured under certain conditions and does not guarantee the product's characteristics.