

To our customers,

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Notice

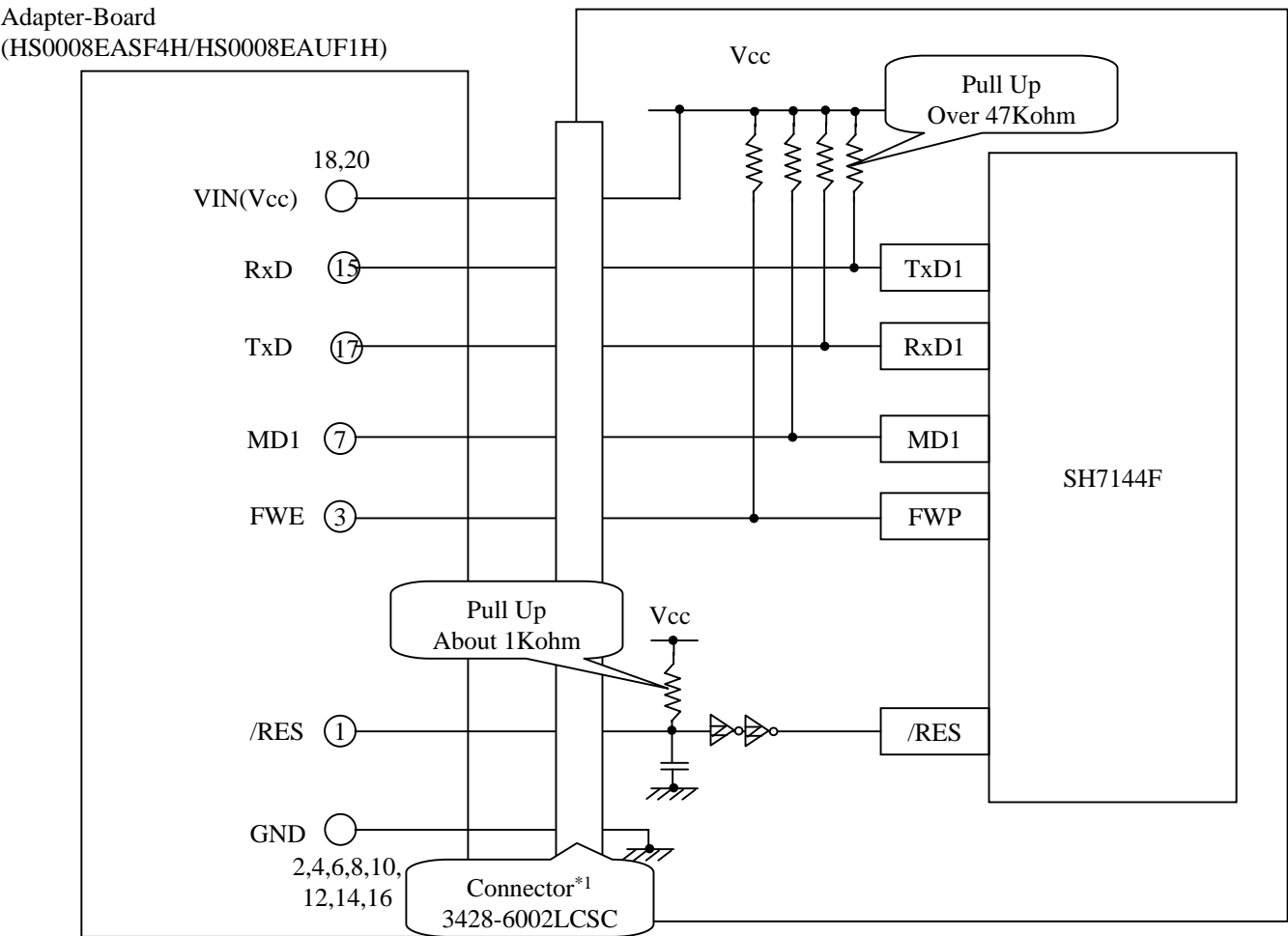
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The interface circuit between SH7144F and F-ZTAT programming adapter board

This figure shows you the interface circuit between SH7144F and F-ZTAT adapter board (HS0008EASF4H/HS0008EAUF1H). The value of pull-up and pull down resistors are for your reference.



Setting the switches on the HS0008EASF4H on using SH7144F *1:Sumitomo 3M,Ltd

No.	Adapter-Board Signal	Number On Cable	Low/High Switches (S3)	Control-Signal Enable/Disable Switches (S4)	Vcc and PVcc Switches (S5)	Vcc and PVcc Switches (S6)
1	FWE	3	Low(0)	Enable(E)	Vcc(C)	——
2	MD0	5	——	Disable(D)	Vcc(C)	——
3	MD1	7	Low(0)	Enable(E)	Vcc(C)	——
4	I/O1	9	——	Disable(D)	Vcc(C)	——
5	I/O2	11	——	Disable(D)	——	Vcc(C)
6	I/O3	13	——	Disable(D)	——	Vcc(C)

Setting the switches on the HS0008EAUF1H on using SH7144F
FLASH Development Toolkit Pin Settings

FDM Pin Settings

Please select the pin settings for the FDM

BOOT Mode using Clock Mode **3**

Operating Mode: **U: User Defined**

WARNING:
Incorrect settings could damage your hardware

	FWx	SCK	MD4	MD3	MD2	MD1	MD0	
BOOT Mode Outputs	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	= 0x82
BOOT Mode Setting	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	= 0x00

(coloured pins are clock mode pins)

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