

## ZMID4200

## Contents

<b>1. Introduction</b> .....	<b>1</b>
<b>2. Inductive Sensing Technology Introduction</b> .....	<b>2</b>
2.1 Device Block Diagram .....	4
<b>3. Getting Started</b> .....	<b>5</b>
3.1 Device Initialization .....	6
3.1.1. Output Mode Selection.....	7
3.2 Offset Check for Rx Coils .....	8
3.3 Gain Stage Setting .....	9
3.4 Input Amplitude Offset Compensation.....	10
3.5 Output Calibration.....	11
3.6 Output Linearization .....	13
<b>4. Internal Memory</b> .....	<b>15</b>
<b>5. Programming the ZMID4200 EEPROM</b> .....	<b>17</b>
<b>6. Revision History</b> .....	<b>17</b>

## Figures

Figure 1. Tx Loop Magnetic Field .....	2
Figure 2. Current Paths and Shapes for Coils – Example for Linear Position Sensing .....	3
Figure 3. Geometrical Illustration for Coils and Target – Example for Linear Position Sensing .....	3
Figure 4. Main Internal Functional Block Diagram .....	4
Figure 5. ZMID4200 Transmitter LC Tank.....	5
Figure 6. TX Oscillation and LF-U 5 Probe.....	6
Figure 6. Output Mode Selection .....	6
Figure 7. ZMID4200EEPROM Memory Values –Example for the Linear Output Mode Settings .....	7
Figure 8. ZMID4200 EEPROM Gain Stage Setting – Example using the SENT output. ....	8
Figure 9. Coil Offset Reading – Example using the ZMID4200.....	9
Figure 10. Magnitude for Gain Selection – Example for the ZMID4200.....	10
Figure 11. ZMID4200 Coil Amplitude Offset Compensation .....	11
Figure 12. Output Calibration.....	12
Figure 13. Output Calibration Verification.....	12
Figure 14. Correction Mode Selection .....	13
Figure 15. Measured Value Readings for 9 Linearization Points .....	14
Figure 16. Input Fields for the 9 Linearization Points Measured Values .....	15
Figure 17. ZMID4200 Internal Memory.....	15

# 1. Introduction

This document describes the procedures for setting up the ZMID4200 internal memory and transmitter (Tx) front-end in order to start measurements with the device in user applications. It describes the settings needed to prepare the device for operation and then gives an internal memory overview.

The steps provided here are needed when configuring a new device in a user application. These procedures can also be applied to any of the ZMID4200 application modules provided by Renesas; however this is typically not needed because the sensor has been fully configured before the module is shipped.

Recommendation: Read the *ZMID4200 Datasheet* before using this document for a better understanding of the ZMID4200: [www.renesas.com/ZMID4200](http://www.renesas.com/ZMID4200)

Information on the OWI interface can be found in the *ZMID4200 Technical Brief – One Wire Interface (OWI)*.

# 2. Inductive Sensing Technology Introduction

This section provides some basic principles for inductive position sensing using the ZMID4200 family of products, and it covers how the required magnetic fields are generated.

In the application, an LC oscillator generates a magnetic field in the transmit wire loop. The frequency of the oscillation is tuned by the capacitance Ct. The polarity of the magnetic field depends on the direction of the current in the loop.

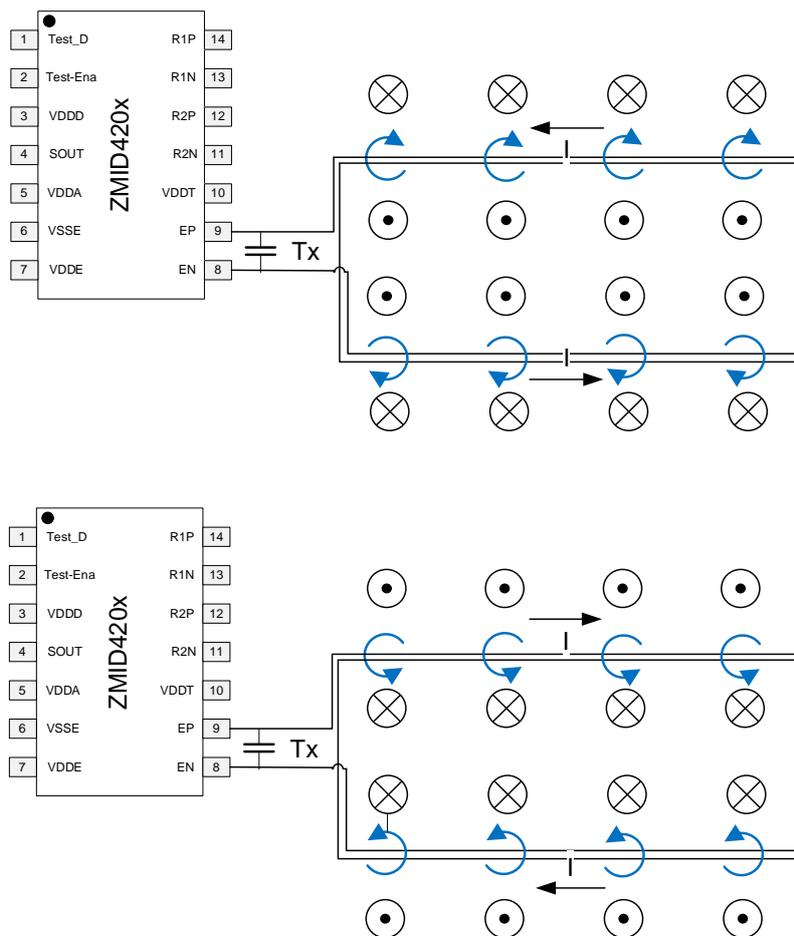


Figure 1. Tx Loop Magnetic Field

The signal that is generated by the magnetic field of transmitter coil (Tx) is received by two receiver coils.

If a metallic target is placed over the coils, the transmitted energy below the target is dissipated as eddy currents in the target and does not induce a secondary voltage in the receiver coils in that area.

The two receiver coils are designed with a 90° phase shift, and the transmitter coil surrounds them. The target is placed on top of them and moves over a plane parallel to the plane containing all the coils, as shown in Figure 2 below. Depending on the coil's shape, movement can be linear, arc, or rotary. The same physical principle is valid for different coils (and target) shapes.

The position of the target will be indicated through the differential phase and the amplitude of the signals measured on the Rx coils by the ZMID4200.

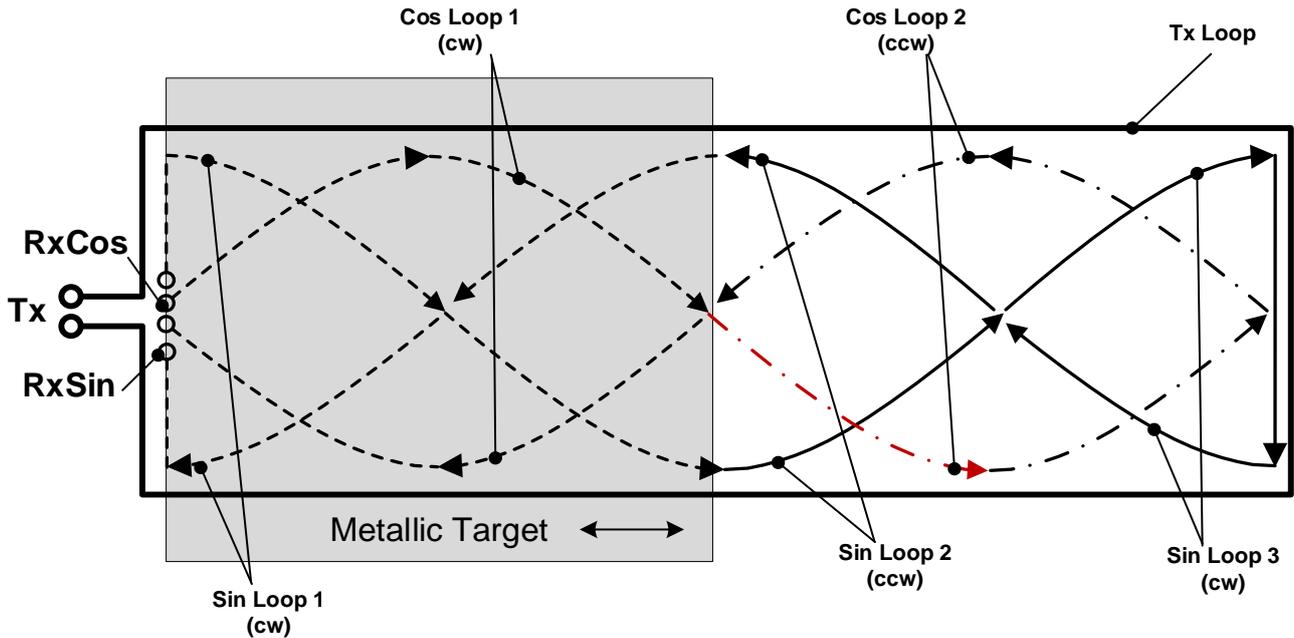
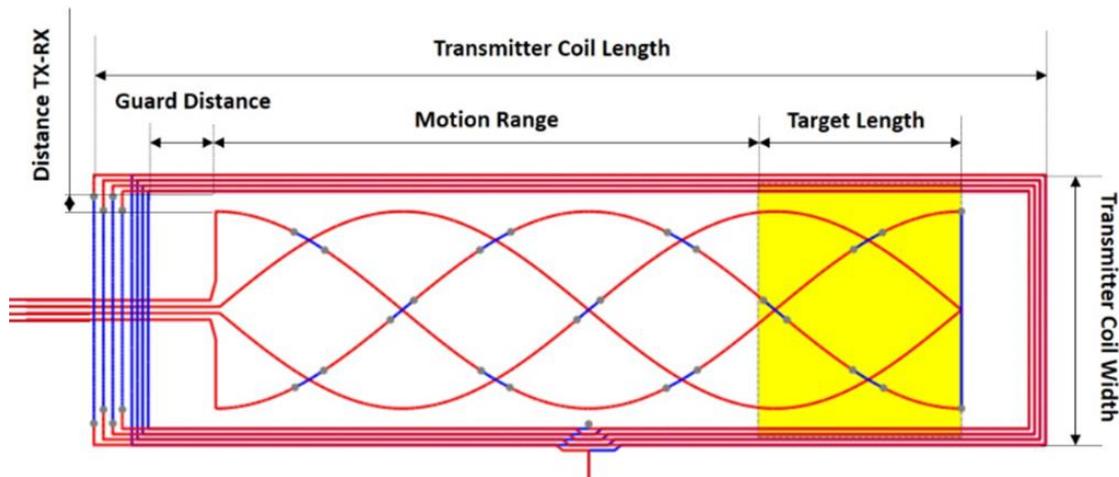


Figure 2. Current Paths and Shapes for Coils – Example for Linear Position Sensing

The key parameters influencing the proper operation of coils with the ZMID4200 are the following: the length and width of the Tx and Rx coils, the size of the target, and the airgap between the target and the printed circuit board (PCB) where the coils are integrated. Figure 3 provides an illustration of the length and width of the coils for linear position sensing.



Note: The display was created by the ZMID4200 Inductive Coil Design Tool Software.

Figure 3. Geometrical Illustration for Coils and Target – Example for Linear Position Sensing

Renesas provides a software tool to support coil design as illustrated in Figure 3:

- *ZMID4200 Inductive Coil Design Tool Software*: <http://www.renesas.com/ZMID4200>
- *ZMID4200 User Guide – Inductive Coil Design Tool*: Reference designs for linear, arc and rotary position sensors are shown in the “Layout” section of the applicable *Application Modules User Manual* available on the following application module product pages, which also provide relevant Gerber design files:
  - ZMID4200MLINApplication Module
  - ZMID4200MARC Application Module
  - ZMID4200MROT Application Module

All modules are available on the <http://www.renesas.com/ZMID4200> page.

## 2.1 Device Block Diagram

Refer to the *ZMID4200 Datasheet* for the latest block diagram information.

The main building blocks include the following:

- Power management: power-on-reset (POR) circuit and low drop-out (LDO) regulators for internal supplies.
- Oscillator: generation of the transmit coil signal.
- Analog front-end: demodulator and gain control for the receive signals.
- Analog-to-digital converter (ADC): conversion into digital domain.
- Digital signal processing: offset correction; conversion of sine and cosine signals into angle and magnitude; angle range adjustment; and linearization.
- EEPROM: nonvolatile storage of factory and user-programmable settings.
- One-wire interface (OWI): programming of the chip through the output pin.
- Interface options:
  - Analog output
  - PWM output
  - Sent output
- Protection: overvoltage, reverse polarity, short circuit.

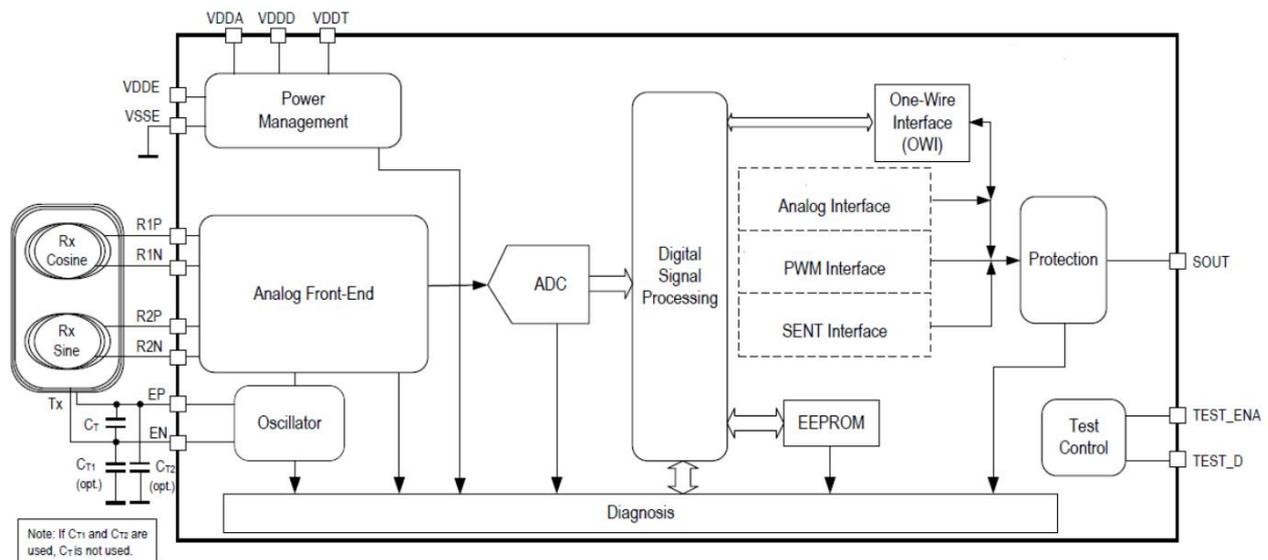


Figure 4. Main Internal Functional Block Diagram

### 3. Getting Started

The following procedures require the *ZMID4200 EVK Application Software*, which includes the graphical user interface (GUI) provided for the ZMID4200 Application Modules.

A complete description of the GUI is given in the *ZMID4200 Evaluation Kit User Manual*, and it includes instructions for downloading and installing the GUI: LC Tank – Tx Front-End Tuning

The transmit circuitry for ZMID4200 applications consists of an LC tank that is formed from the inductance of the transmitting coil and a capacitor on the circuit board as shown in Figure 5.

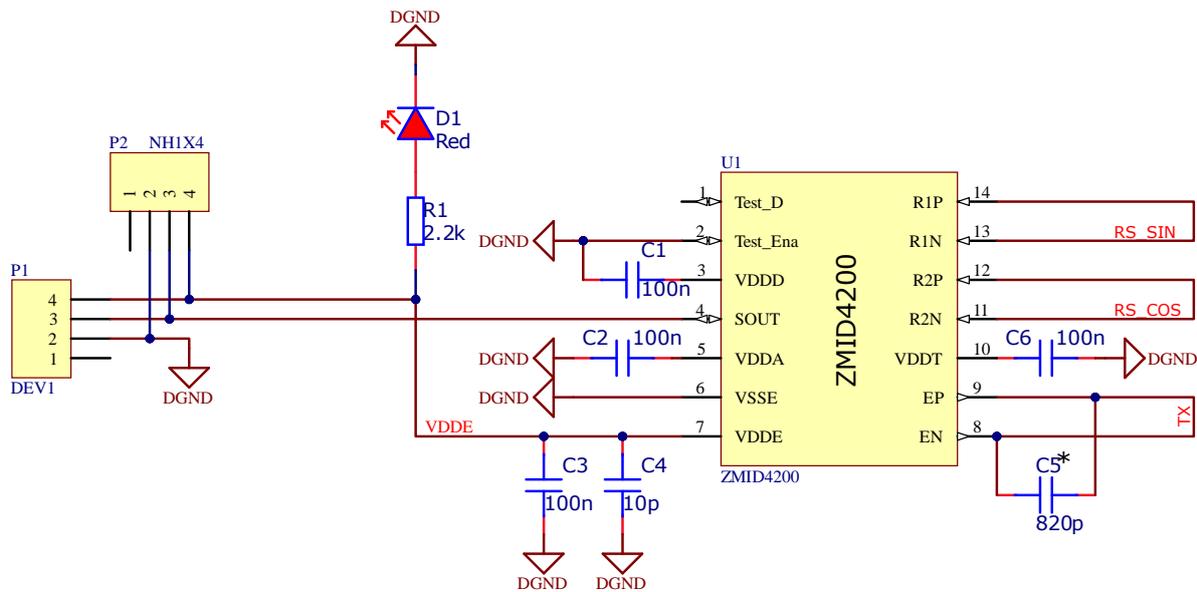


Figure 5. ZMID4200 Transmitter LC Tank

The objective of the Tx front-end tuning is to set the oscillation frequency in the range of operation specified in the *ZMID4200 Datasheet*; a typical value is approximately 3.5MHz.

The first step is to measure the inductance value of the Tx coil and verify that the value is within the limits specified in the *ZMID4200 Datasheet*. Once the inductor value is known, the capacitor value (Ct in Figure 5) can be calculated using Equation 1.

$$f = \frac{1}{2\pi\sqrt{LC}} \tag{Equation 1}$$

Direct measurement of the frequency will confirm the exact value of the oscillation frequency.

If the measurement of the inductance of the printed circuit board coil (Lt in Figure 5) is not an option, a successive approximation approach can be used, i.e., testing multiple Ct values until the resulting oscillation frequency is as close as possible to 3.5MHz. A capacitor value of 560pF is generally a good starting point.

Recommendation: To ensure a good quality factor and low temperature drift for the LC tank circuit, use ceramic capacitors class C0G (C-zero-G) ceramics also known as NP0 (negative-positive-zero). The capacitor must be placed close to the EP and EN pins on the ZMID4200.

Figure 6 shows the TX pin oscillation detected with a LF-U 5 Near-Field Probe from Langer EMV-Technik.



Figure 6. TX Oscillation and LF-U 5 Probe

### 3.1 Device Initialization

It is possible to set the Output of the sensor by bit configuration to either Analog, PWM, or SENT. Select the preferred Output by the *Output* dropdown on the Main tab of the GUI, see Figure 7.

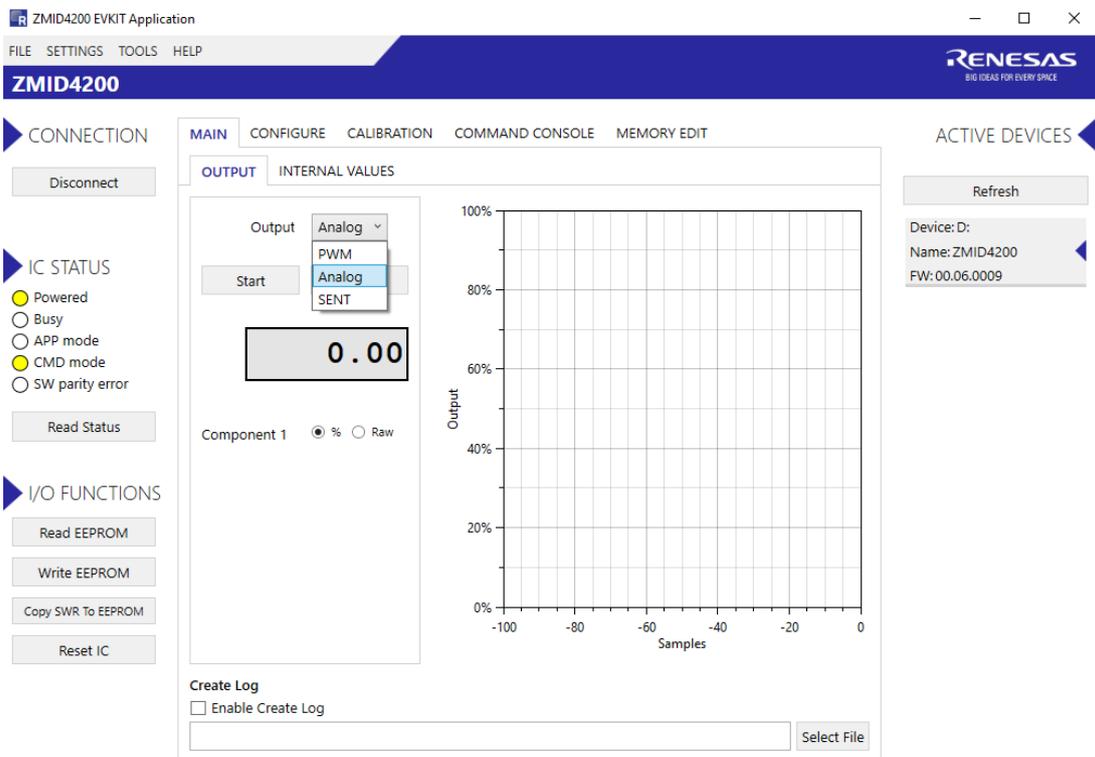


Figure 7. Output Mode Selection

### 3.1.1. Output Mode Selection

The ZMID allows selecting one of two output modes: Linear or Modulo 360.

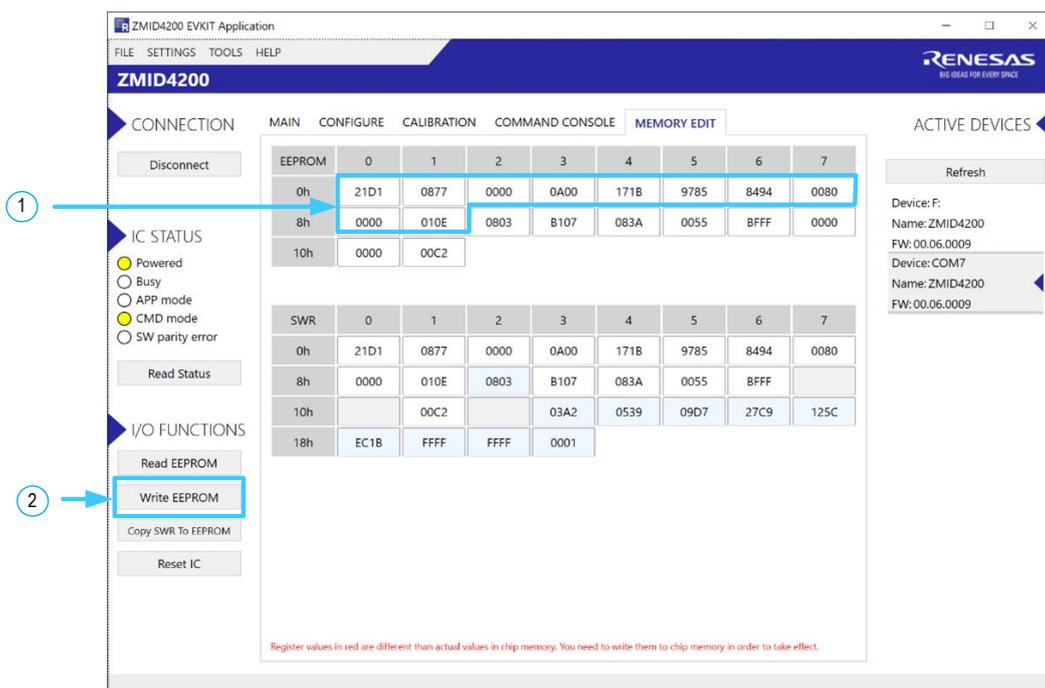
- Linear: The Linear Output Mode is a non-repeating output mode in which the sensor output signal is clamped at the mechanical end points.
- Modulo 360: The Modulo 360 Output (Sawtooth Output) Mode is a repeating output mode in which the sensor output signal is not clamped at the mechanical end points, but it is switched back to its origin.

See the *ZMID4200 Datasheet* and *ZMID4200 EVK User Manual* for further details.

In most linear and arc applications, the Linear Output Mode is recommended. For rotary applications, the Modulo 360 Mode is recommended.

To ensure a smooth and successful procedure, use the GUI and the instructions in the *ZMID4200 EVK User Manual* to set the ZMID4200 EEPROM registers from 00<sub>HEX</sub> to 09<sub>HEX</sub> to the following values. Note: Ensure that the new values are stored in EEPROM using the “Write EEPROM” function.

- If using the Linear Output Mode, set the following register values as shown in Figure 8: register 00<sub>HEX</sub> = 2400<sub>HEX</sub>; register 01<sub>HEX</sub> = 0400<sub>HEX</sub>; and registers 02<sub>HEX</sub> through 09<sub>HEX</sub> = 0000<sub>HEX</sub>.
- If using the Modulo 360 Output Mode, set register 00<sub>HEX</sub> = 0000<sub>HEX</sub>, register 01<sub>HEX</sub> = 0400<sub>HEX</sub>, registers 02<sub>HEX</sub> through 08<sub>HEX</sub> = 0000<sub>HEX</sub>, and register 09<sub>HEX</sub> = 1000<sub>HEX</sub>.



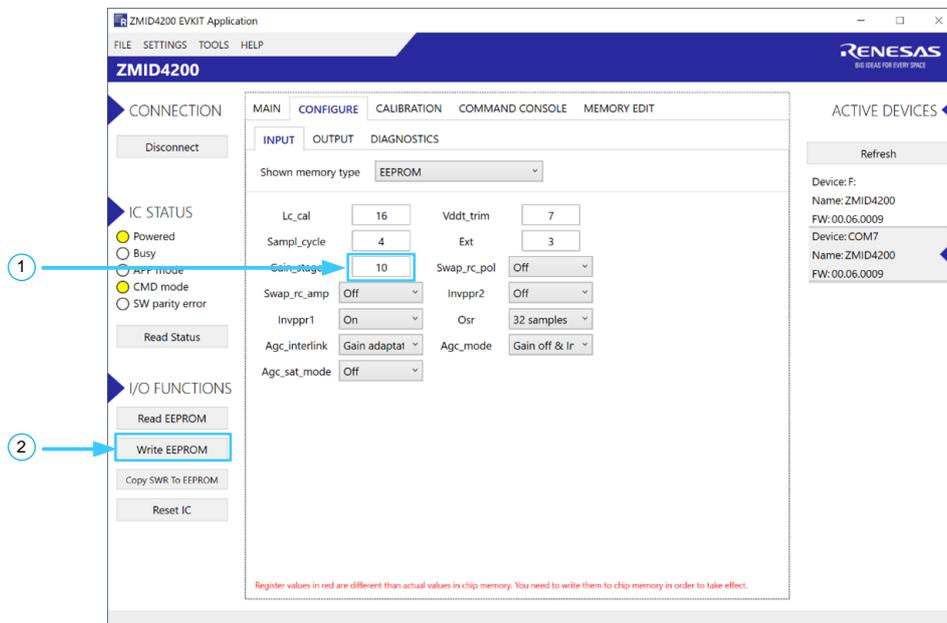
- 1 Set standard values for registers 00<sub>HEX</sub> to 09<sub>HEX</sub>. Other register values can differ.
- 2 Write new values to the ZMID4200 EEPROM.

Figure 8. ZMID4200EEPROM Memory Values –Example for the Linear Output Mode Settings

### 3.2 Offset Check for Rx Coils

The ZMID4200 has a selectable input gain, which can be set via the GUI using the *Gain\_stage* entry field found on the “INPUT” subtab under the “CONFIGURE” tab as described in section 3.3. Before checking the offset of the Rx coils, set the *Gain\_stage* value to a preliminary setting of 6, see Figure 9.

Save the new value in the ZMID4200 EEPROM memory by clicking the “Write EEPROM” button, which updates register 0C<sub>HEX</sub>.



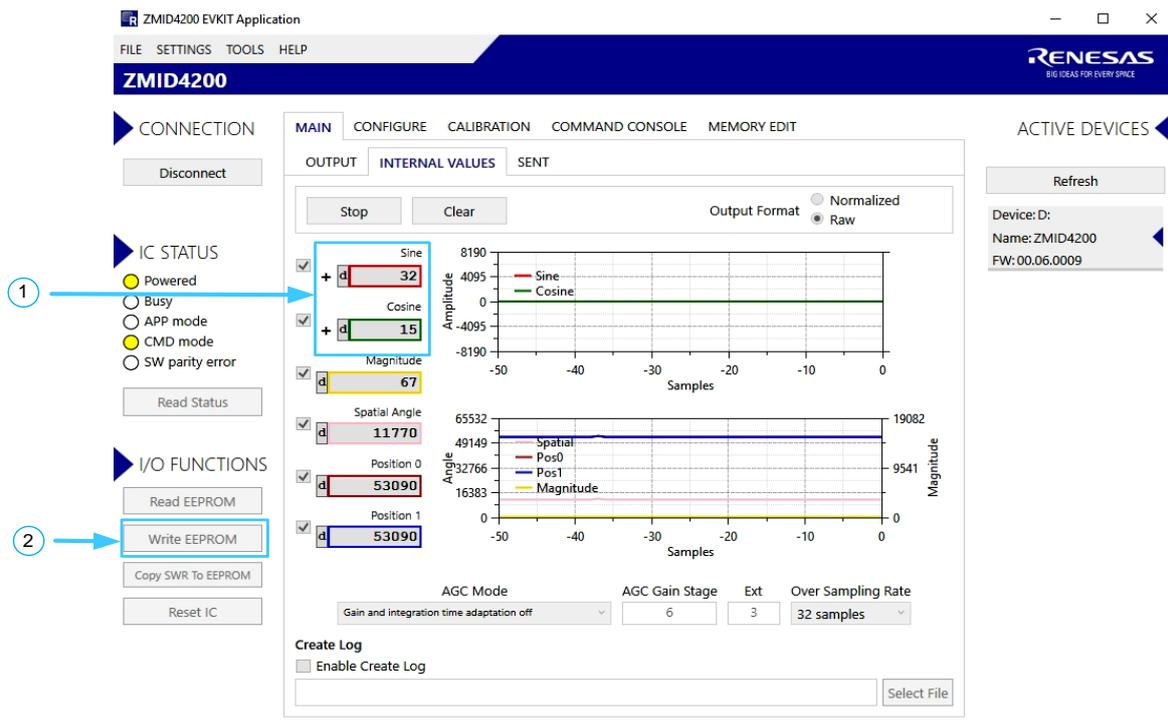
- ① Set *Gain\_stage* to 6.
- ② Write new values to the ZMID4200 EEPROM.

**Figure 9. ZMID4200 EEPROM Gain Stage Setting – Example using the SENT output.**

Next, remove the target from the sensor board (distance between the target and the Rx coils must be greater than 20mm). With this condition, use the GUI to read the “Sine” and “Cosine” values on the “INTERNAL VALUES” subtab under the “MAIN” tab as shown in the ZMID4200 example given in Figure 10.

For properly designed coils, typical offset values are below 100<sub>DEC</sub> for “Sine” and “Cosine” values as shown in the example in Figure 10.

The coil design should meet the criteria of having a maximum symmetry for the two Rx coils. See section 2 for the links for software and documentation for the *ZMID4200 Inductive Coil Design Tool Software* provided by Renesas for addressing this requirement.



- ① Check that offset values are within requirements.
- ② Write new values to the ZMID4200 EEPROM.

Figure 10. Coil Offset Reading – Example using the ZMID4200.

### 3.3 Gain Stage Setting

Select the value of the *Gain\_stage* setting so that the value of the “Magnitude” parameter is in the range of 6000<sub>DEC</sub> to 10000<sub>DEC</sub> when the target is positioned at the nominal air gap over the sensor board. The GUI displays the “Magnitude” parameter tab on the “INTERNAL VALUES” subtab under the “MAIN” tab for checking that this requirement has been met. In the ZMID4200 example given in Figure 11, *Gain\_stage* has been set to 7<sub>DEC</sub> resulting in a “Magnitude” value within the required range.

For most applications, the automatic gain control (ACG) functionality can be disabled (see the “AGC Mode” setting in Figure 11).

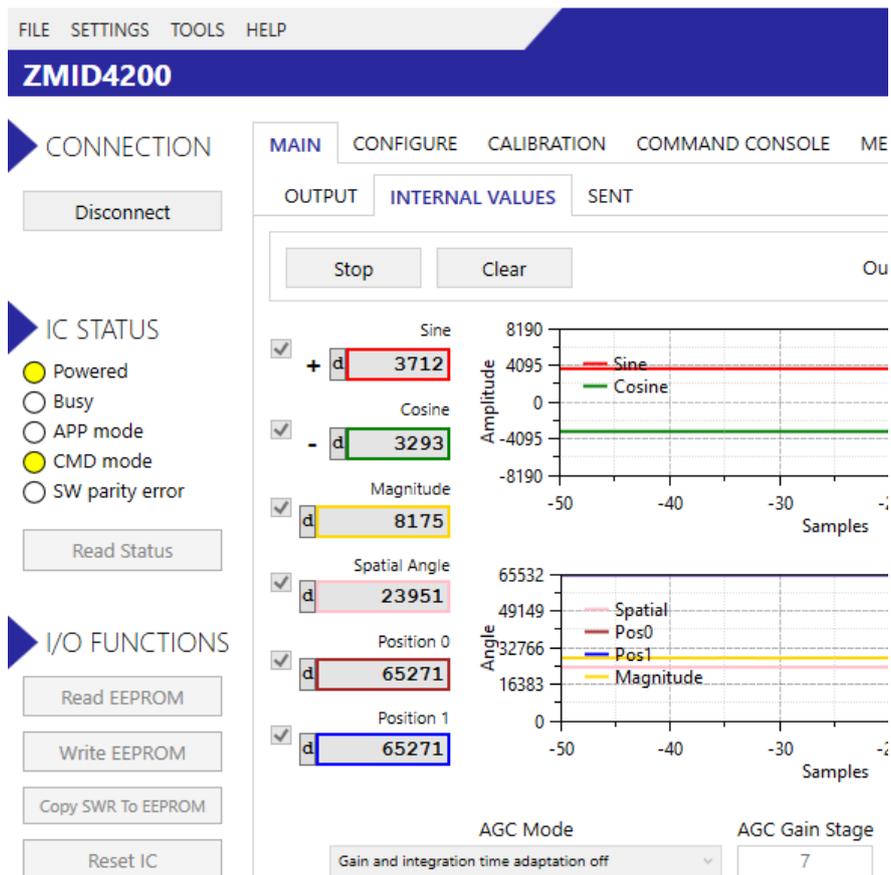
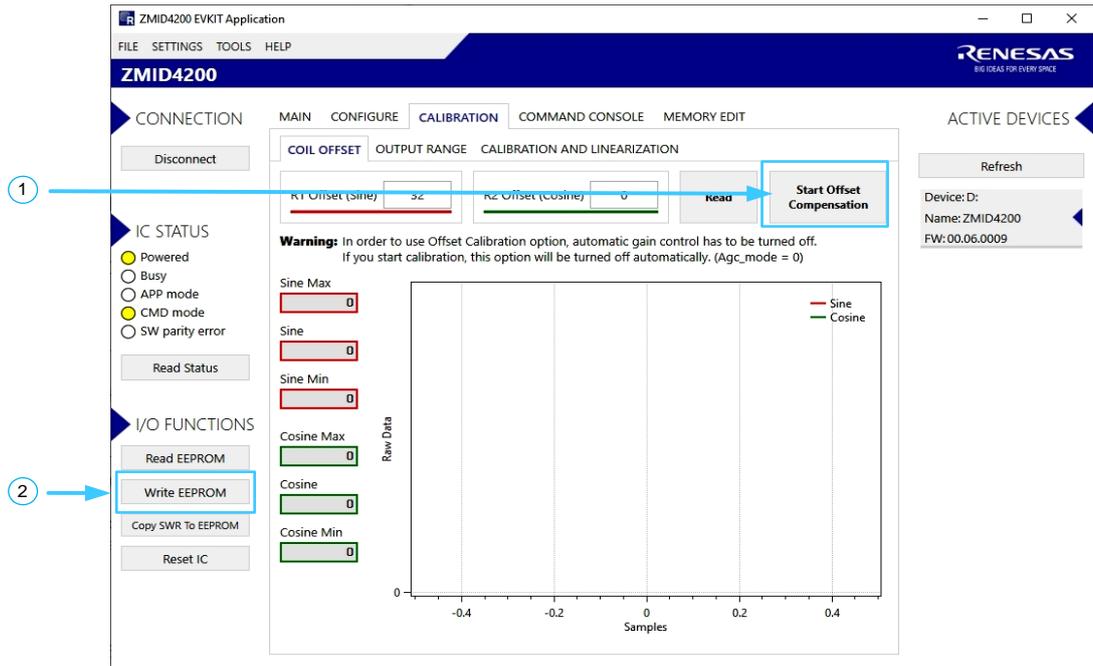


Figure 11. Magnitude for Gain Selection – Example for the ZMID4200

### 3.4 Coil Amplitude Offset Compensation

The GUI offers an automated procedure for performing the Coil offset compensation with the target in place. For this procedure, the target must be positioned at the operational distance from the receiving coils (air gap).

1. Start the procedure by clicking the “Start Calibration” button located on the “AMPLITUDE OFFSET” subtab under the “CALIBRATION” tab as shown in the example given in Figure 12, which applies to all ZMID4200 ICs. Follow instructions in the resulting dialog windows to complete the calibration.
2. When the calibration is finished, click the “Write to EEPROM” button before moving to the next steps. This procedure modifies the contents of register 08<sub>HEX</sub>.



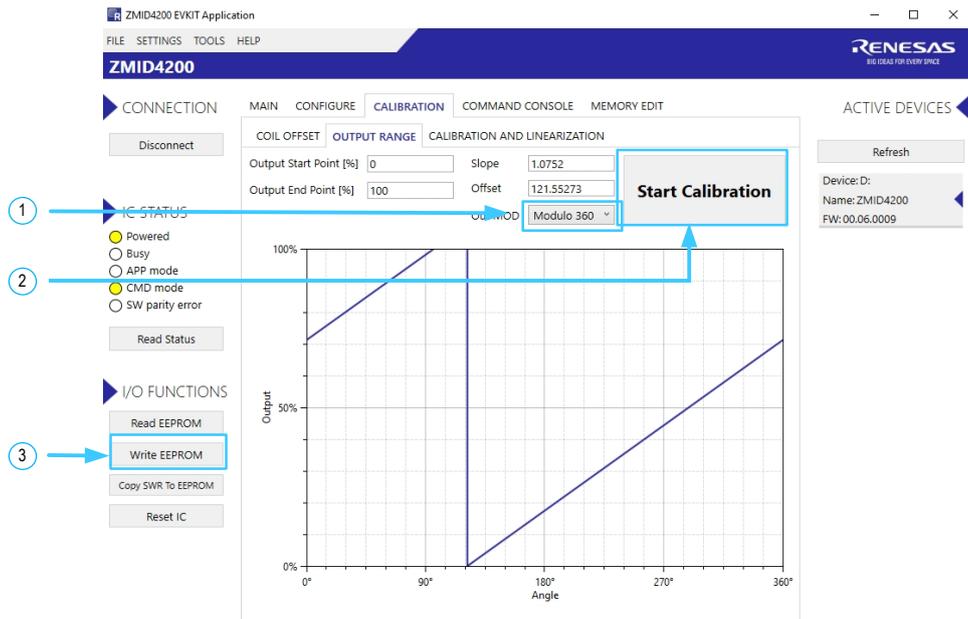
- 1 Click to start automatic offset compensation.
- 2 When the automatic calibration is completed, write new values to the ZMID4200 EEPROM.

Figure 12. ZMID4200 Coil Amplitude Offset Compensation

### 3.5 Output Calibration

The GUI offers an automated procedure for performing output calibration with the target in place at the operational distance from the receiving coils (air gap).

1. Select the “Out MODE” setting as described in section 3.1.1.
2. Start the procedure by clicking the “Start Calibration” button on the “OUTPUT CONFIG” subtab under the “CALIBRATION” tab as shown in the ZMID4200 example given in Figure 13.
3. Click the “Write to EEPROM” button before moving to the next steps. This procedure modifies the contents of registers 00<sub>HEX</sub>, 01<sub>HEX</sub>, and 09<sub>HEX</sub>.
4. Verify that the calibration was successful by checking the “Position 1” field on the “INTERNAL VALUES” subtab under the “MAIN” tab at the start and end positions of the measurement range via the “INTERNAL VALUES” subtab under the “MAIN” tab as shown in the example given in Figure 14, which applies to all ZMID4200 ICs.



- 1 Select the output mode:
  - “Linear” for arc and linear applications
  - “Modulus 360” for rotary applications
- 2 Click to start automatic offset compensation.
- 3 When the automatic calibration is completed, write new values to the ZMID4200 EEPROM.

Figure 13. Output Calibration



Figure 14. Output Calibration Verification

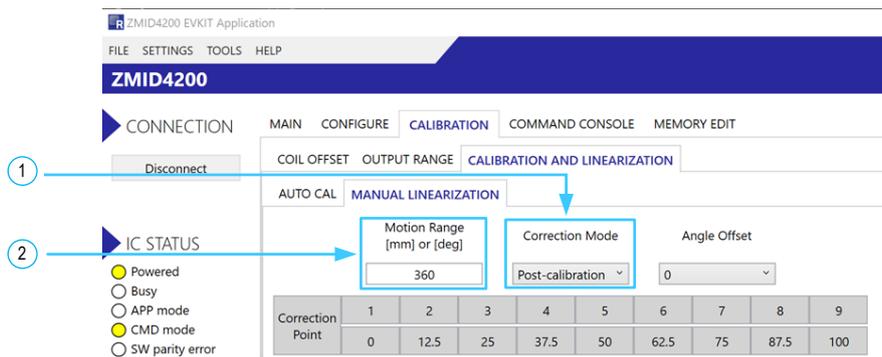
### 3.6 Output Linearization

This section provides a basic output linearization procedure for the ZMID4200. Using a target positioning system for these procedures is strongly recommended.

For additional information on calibration and linearization, refer to the *ZMID4200 Evaluation Kit User Manual*. For the Analog Output, also see the *Manual for Calibration and Linearization Using the Analog Output* available on the product web page. <http://www.renesas.com/ZMID4200>

Set the “Correction Mode” drop-down menu to “Post-calibration” on the “LINEAR INTERPOLATION” subtab under the “CALIBRATION” tab (see Figure 15). This modifies register 09<sub>HEX</sub>.

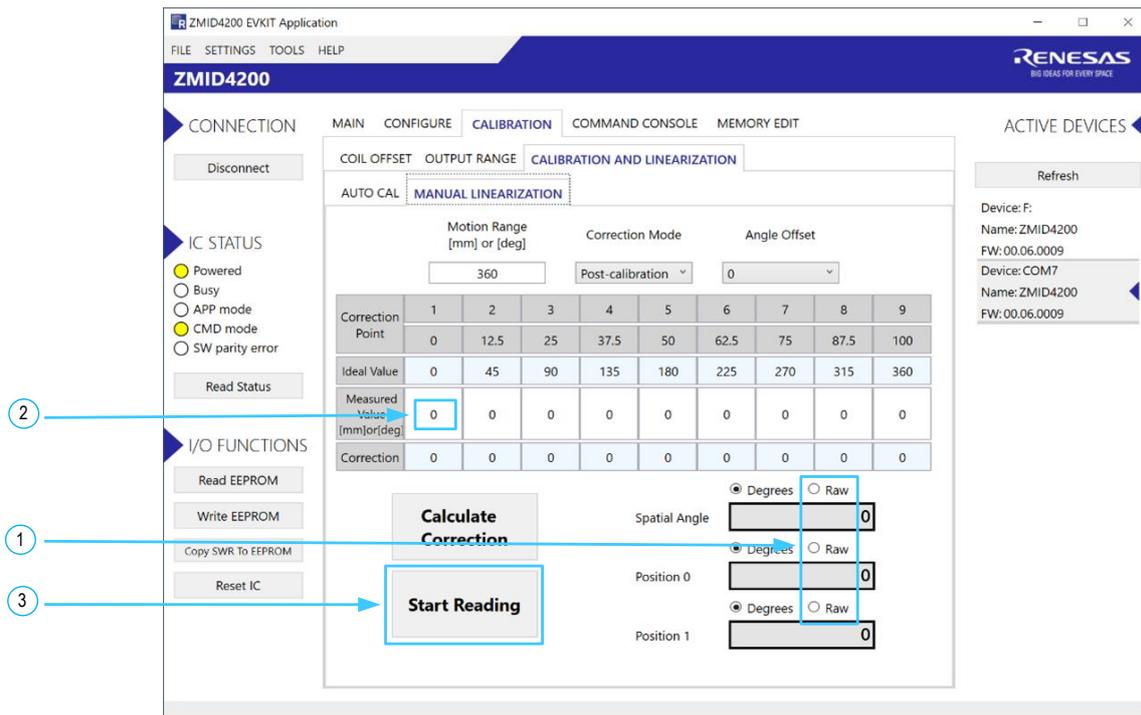
1. Set the value for the “Motion Range” entry field according to the receiver coil shape.



1. Set “Correction Mode” to “Post-calibration”.
2. Set “Motion Range” value according to the application.

**Figure 15. Correction Mode Selection**

2. Select the “Raw” mode via the radio buttons above the fields for “Spatial Angle,” “Position 0,” and “Position 1” as shown in Figure 16.
3. Click the “Start Reading” button to start reading position values via the ZMID4200. The button changes to “Stop,” and the adjacent readings should update to show the position readings.
4. Move the actual target position until the value in the “Position 0” field is 0000<sub>HEX</sub>. Note: The points identified by “Position 0” = 0000<sub>HEX</sub> and “Position 0” = FFFF<sub>HEX</sub> are the start and end points identified with the calibration procedure.
5. Then physically measure the actual position or read the measured value from the positioning system (in mm or degrees) and enter the value in the first column in the “Measured Value” row in the matrix on the “LINEAR INTERPOLATION” tab. Figure 16 shows an example of the matrix for a 360 rotary position sensing application for the ZMID4200.

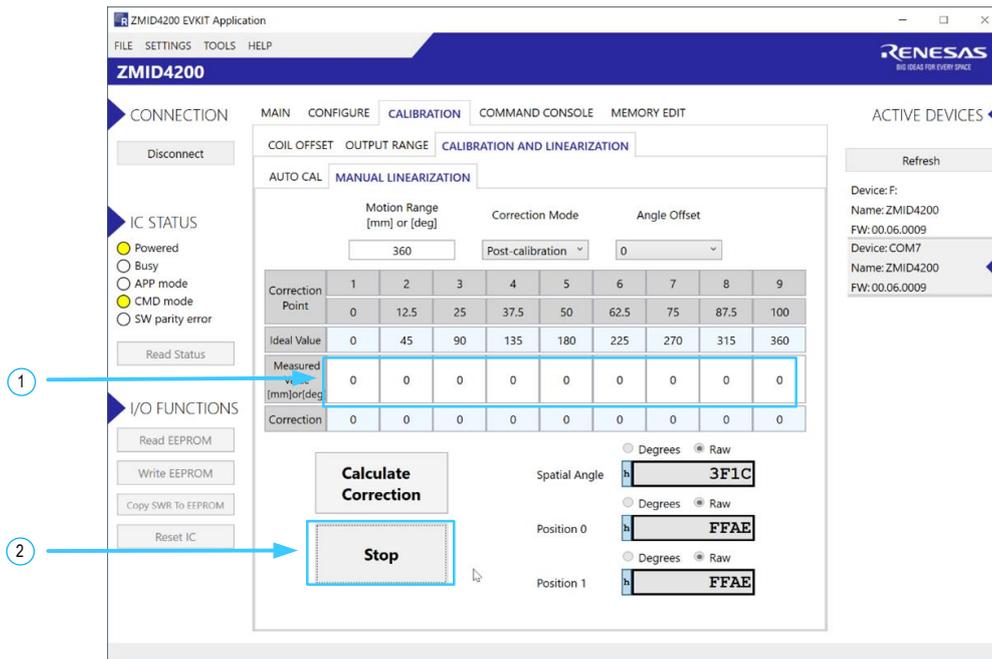


- 1 Set "Raw" mode
- 2 Enter the physical measurement in mm or degrees; for example, from the positioning system.
- 3 Click "Start Reading" to start reading the measured values from the ZMID4200.

**Figure 16. Measured Value Readings for 9 Linearization Points**

6. Move the target again (e.g., with the positioning system) until the value in the "Position 0" field is 1FFF<sub>HEX</sub>. Then physically measure the new actual position or read the measured value from the positioning system and enter the value in the second column in the "Measured Value" row in the matrix.
7. Repeat the previous step for "Position 0" = (1FFF x n) + n – 1 where n = 2 to 8 to obtain actual measurements for the remaining positions in the matrix so that all 9 linearization points identified in Figure 17 have measured values entered in the "Measured Value" row. The software immediately calculates the correction values, and the resulting values are displayed below the applicable "Measured Values" entry.
8. Click on "Write EEPROM" to save the new values in registers 03<sub>HEX</sub> to 07<sub>HEX</sub>.

The linearization procedure is completed. The values in the *Pos1* register will differ from those in *Pos0* register, reflecting the linearization correction performed (see Table 1). The device is ready for operation in the application environment.



- 1 Advance the target and enter actual measured position values (for example, from the positioning system) in each column.
- 2 Click “Stop” to read measured values.

Figure 17. Input Fields for the 9 Linearization Points Measured Values

## 4. Internal Memory

Figure 18 shows the internal memory structure of the ZMID4200 products, which is divided into a nonvolatile EEPROM and a volatile shadow RAM (SWR) section. After the ZMID4200 start up, the EEPROM content is copied into the SWR. During ZMID4200 operation in OWI mode, changes in the SWR will take immediate effect; whereas changes in the EEPROM require a memory WRITE command or a ZMID4200 power cycle (power off / power on).

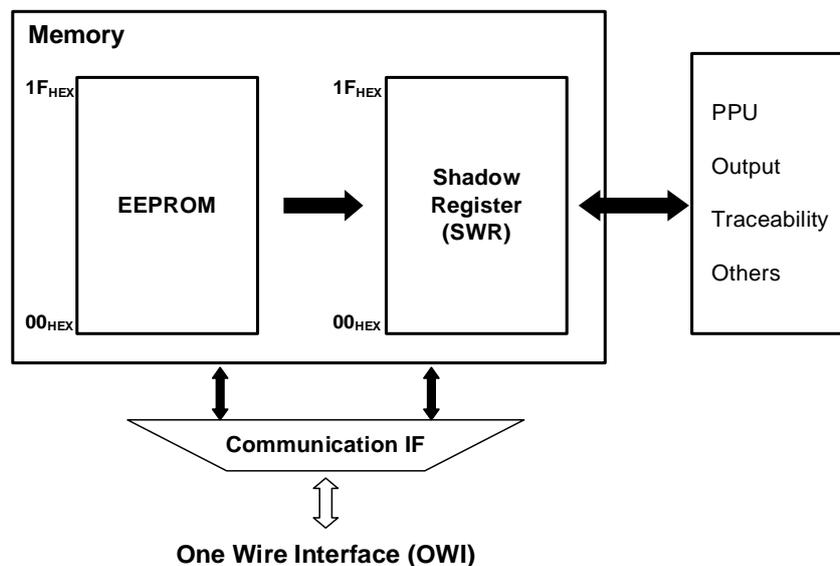


Figure 18. ZMID4200 Internal Memory

**Table 1. EEPROM and Shadow RAM Contents**

Address	Type	Location	Function
00 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is the 14-bit zero-angle offset of output signal, which is used for device calibration and sets the offset value of the output transfer function.
01 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is the 13-bit slope value of output signal, which is used for device calibration and sets the slope value of the output transfer function.
02 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used with the analog and PWM output protocols in order to clamp the upper and lower output levels of the transfer function respectively below 95% and above 5%.
03 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used in linearization. The position transfer function can be modified by a correction curve over the whole position range. The correction curve is defined by 9 linearization points. This register contains the correction factor for points n°2 and n°1. Bits [15:8] are the correction factor for the position at 12.5% (45°), and bits [7:0] are the correction factor for the position at 0% (0°).
04 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used in linearization (see register 03 <sub>HEX</sub> for description). This register contains the correction factor for points n°4 and n°3. Bits [15:8] are the correction factor for the position at 37.5% (135°), and bits [7:0] are the correction factor for the position at 25% (90°).
05 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used in linearization (see register 03 <sub>HEX</sub> for description). This register contains the correction factor for points n°6 and n°5. Bits [15:8] are the correction factor for the position at 62.5% (225°), and bits [7:0] are the correction factor for the position at 50% (180°).
06 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used in linearization (see register 03 <sub>HEX</sub> for description). This register contains the correction factor for points n°8 and n°7. Bits [15:8] are the correction factor for the position at 87.5% (315°), and bits [7:0] are the correction factor for the position at 75% (270°).
07 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used in linearization (see register 03 <sub>HEX</sub> for description). This register contains the correction factor for point n°9. Bits [15:8] are not used, and bits [7:0] are the correction factor for the position at 100% (360°).
08 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is the signal offset correction of the demodulated input signals sin and cos (R1 and R2). It is used for input amplitude offset correction; the defined register offset values are added/subtracted to/from the amplitude values of the receiver coil values.
09 <sub>HEX</sub>	R/W	EEPROM/SWR	This register contains the control bits for selecting the output mode of the sensor (Linear Output Mode or Modulo 360 Output Mode), the type of linearization (pre or post-calibration), the angle offset for linearity correction (0° or -22.5°), the option to reverse the polarity of the receiver coils, and the option to invert the phase polarity of the receiver coils.
0A <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used to define the type of communication interface (SENT, analog, or PWM). It is used for configuration of the output SENT CRC, SENT pause; PWM slope current, PWM frequency, and analog diagnostic level.
0B <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used to control the voltage of the internal VDDT voltage regulator, the current of the Tx excitation coil, the oversampling rate of the ADC, and the PWM output slope time.
0C <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used to set the gain stage value, the adjustment of the integration cycle in relation to the Tx coil period, and the integration time in terms of the ADC sample periods vs. the oversampling rate.
0D <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used to set the CORDIC magnitude upper and lower levels, the upper and lower limits for the Tx coil frequency, the gain and integration time adaptation combined settings, and mixed operation modes.
0E <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used to mask diagnostic register bits to prevent a diagnostic flag setting the output in the diagnostic status.
0F <sub>HEX</sub>	R/W	EEPROM	Internal use only.
10 <sub>HEX</sub>	R/W	EEPROM	Internal use only.

Address	Type	Location	Function
11 <sub>HEX</sub>	R/W	EEPROM/SWR	This register value is used to the trigger actions for the shadow register, diagnostic register, Tx coil alarm, Rx coils alarm, double-error detection for the EEPROM memory, parity error detection on the SWR memory, ADC overflow, AGC offset saturation, Tx coil alarm, and a set of parameters used in test mode only.
13 <sub>HEX</sub>	R	SWR	This register value is used for analog front-end / automatic gain regulation; polarity of R1 and R2 ADC gain; and number of integration cycles for the AGC.
14 <sub>HEX</sub>	R	SWR	This register value is the 13-bit CORDIC raw input signal for <i>Xsine</i> .
15 <sub>HEX</sub>	R	SWR	This register value is the 13-bit CORDIC raw input signal for <i>Ycosine</i> .
16 <sub>HEX</sub>	R	SWR	This register value is the 15-bit CORDIC output angle (0° to 90°).
17 <sub>HEX</sub>	R	SWR	This register value is the 15-bit CORDIC output magnitude.
18 <sub>HEX</sub>	R	SWR	This register value is the 16-bit output spatial angle (0° to 360°) before output calibration and linear error correction.
19 <sub>HEX</sub>	R	SWR	This register value is Position 0, which is the 16-bit output angle (0° to 360°) after output calibration. If linearization is done before the output calibration, this value = Position 1. If linearization is done after the output calibration, this value is not the same value as Position 1.
1A <sub>HEX</sub>	R	SWR	This register value is Position 1, which is the 16-bit output angle (0° to 360°) after output calibration and linearization. This value is always affected by linearization.
1B <sub>HEX</sub>	R	SWR	Internal use only.

Complete information about registers map is available upon request. Contact: <https://en-support.renesas.com/dashboard>

## 5. Programming the ZMID4200 EEPROM

The ZMID4200 EEPROM can be programmed using the ZMID-COMBOARD USB Communication and Programming Board for ZMID Application Modules in conjunction with the *ZMID4200 EVK Application Software*. Instructions for using the software to program the EEPROM are given in the *ZMID4200 Evaluation Kit User Manual*. See section 2.1 for details for obtaining the software and the kit manual.

Additional information and documentation for the ZMID-COMBOARD are provided on the following Renesas web page: <http://www.renesas.com/ZMID4200>

## 6. Glossary

Acronym	Definition
ADC	Analog Digital Converter
AGC	Automatic Gain Control
EEPROM	Electrically Erasable Programmable Read Only Memory
LSB	Least Significant Bit
MSB	Most Significant Bit
OWI	One-Wire Interface
OSR	Over-Sampling Rate
SWR	Shadow Word Register Bank

## 7. Revision History

Revision	Date	Description
1.0	Jul 29, 2021	Initial release.

## Notice

1. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation or any other use of the circuits, software, and information in the design of your product or system. Renesas Electronics disclaims any and all liability for any losses and damages incurred by you or third parties arising from the use of these circuits, software, or information.
2. Renesas Electronics hereby expressly disclaims any warranties against and liability for infringement or any other claims involving patents, copyrights, or other intellectual property rights of third parties, by or arising from the use of Renesas Electronics products or technical information described in this document, including but not limited to, the product data, drawings, charts, programs, algorithms, and application examples.
3. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
4. You shall be responsible for determining what licenses are required from any third parties, and obtaining such licenses for the lawful import, export, manufacture, sales, utilization, distribution or other disposal of any products incorporating Renesas Electronics products, if required.
5. You shall not alter, modify, copy, or reverse engineer any Renesas Electronics product, whether in whole or in part. Renesas Electronics disclaims any and all liability for any losses or damages incurred by you or third parties arising from such alteration, modification, copying or reverse engineering.
6. Renesas Electronics products are classified according to the following two quality grades: "Standard" and "High Quality". The intended applications for each Renesas Electronics product depends on the product's quality grade, as indicated below.
  - "Standard": Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; industrial robots; etc.
  - "High Quality": Transportation equipment (automobiles, trains, ships, etc.); traffic control (traffic lights); large-scale communication equipment; key financial terminal systems; safety control equipment; etc.Unless expressly designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not intended or authorized for use in products or systems that may pose a direct threat to human life or bodily injury (artificial life support devices or systems; surgical implantations; etc.), or may cause serious property damage (space system; undersea repeaters; nuclear power control systems; aircraft control systems; key plant systems; military equipment; etc.). Renesas Electronics disclaims any and all liability for any damages or losses incurred by you or any third parties arising from the use of any Renesas Electronics product that is inconsistent with any Renesas Electronics data sheet, user's manual or other Renesas Electronics document.
7. No semiconductor product is absolutely secure. Notwithstanding any security measures or features that may be implemented in Renesas Electronics hardware or software products, Renesas Electronics shall have absolutely no liability arising out of any vulnerability or security breach, including but not limited to any unauthorized access to or use of a Renesas Electronics product or a system that uses a Renesas Electronics product. RENESAS ELECTRONICS DOES NOT WARRANT OR GUARANTEE THAT RENESAS ELECTRONICS PRODUCTS, OR ANY SYSTEMS CREATED USING RENESAS ELECTRONICS PRODUCTS WILL BE INVULNERABLE OR FREE FROM CORRUPTION, ATTACK, VIRUSES, INTERFERENCE, HACKING, DATA LOSS OR THEFT, OR OTHER SECURITY INTRUSION ("Vulnerability Issues"). RENESAS ELECTRONICS DISCLAIMS ANY AND ALL RESPONSIBILITY OR LIABILITY ARISING FROM OR RELATED TO ANY VULNERABILITY ISSUES. FURTHERMORE, TO THE EXTENT PERMITTED BY APPLICABLE LAW, RENESAS ELECTRONICS DISCLAIMS ANY AND ALL WARRANTIES, EXPRESS OR IMPLIED, WITH RESPECT TO THIS DOCUMENT AND ANY RELATED OR ACCOMPANYING SOFTWARE OR HARDWARE, INCLUDING BUT NOT LIMITED TO THE IMPLIED WARRANTIES OF MERCHANTABILITY, OR FITNESS FOR A PARTICULAR PURPOSE.
8. When using Renesas Electronics products, refer to the latest product information (data sheets, user's manuals, application notes, "General Notes for Handling and Using Semiconductor Devices" in the reliability handbook, etc.), and ensure that usage conditions are within the ranges specified by Renesas Electronics with respect to maximum ratings, operating power supply voltage range, heat dissipation characteristics, installation, etc. Renesas Electronics disclaims any and all liability for any malfunctions, failure or accident arising out of the use of Renesas Electronics products outside of such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of Renesas Electronics products, semiconductor products have specific characteristics, such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Unless designated as a high reliability product or a product for harsh environments in a Renesas Electronics data sheet or other Renesas Electronics document, Renesas Electronics products are not subject to radiation resistance design. You are responsible for implementing safety measures to guard against the possibility of bodily injury, injury or damage caused by fire, and/or danger to the public in the event of a failure or malfunction of Renesas Electronics products, such as safety design for hardware and software, including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult and impractical, you are responsible for evaluating the safety of the final products or systems manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. You are responsible for carefully and sufficiently investigating applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive, and using Renesas Electronics products in compliance with all these applicable laws and regulations. Renesas Electronics disclaims any and all liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. Renesas Electronics products and technologies shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations. You shall comply with any applicable export control laws and regulations promulgated and administered by the governments of any countries asserting jurisdiction over the parties or transactions.
12. It is the responsibility of the buyer or distributor of Renesas Electronics products, or any other party who distributes, disposes of, or otherwise sells or transfers the product to a third party, to notify such third party in advance of the contents and conditions set forth in this document.
13. This document shall not be reprinted, reproduced or duplicated in any form, in whole or in part, without prior written consent of Renesas Electronics.
14. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products.

(Note1) "Renesas Electronics" as used in this document means Renesas Electronics Corporation and also includes its directly or indirectly controlled subsidiaries.

(Note2) "Renesas Electronics product(s)" means any product developed or manufactured by or for Renesas Electronics.

(Disclaimer Rev.5.0-1)

## Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,  
Koto-ku, Tokyo 135-0061, Japan  
[www.renesas.com](http://www.renesas.com)

## Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.

## Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit:  
[www.renesas.com/contact/](http://www.renesas.com/contact/)