



Integrated Device Technology, Inc.  
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## Errata Notification

EN #: **FEN-02-01** Errata Revision #: 0  
Issue Date: January 15, 2002 Effective Date: January 15, 2002  
Product Affected: IDT82V2048BB and IDT82V2058BB

### ERRATA DETAILS:

ATTACHMENT  Yes  No

- Data Sheet Errata
- Device Errata
- Device Manual Errata
- Application Notes Errata
- Other (Please Specify)

Pin description table on page 9 was updated on datasheets for IDT82V2048 and IDT82V2058 products.  
Pin LP1/D1/AD1 corresponds to pin number H3 for the BGA160 package. The previously stated pin number is incorrect.

### IDT Web Site Location:

[http://www.idt.com/products/pages/Transport\\_DS\\_t.html](http://www.idt.com/products/pages/Transport_DS_t.html)

### CUSTOMER NOTIFICATION:

As a result of Errata listed above, IDT has decided to notify you through this Errata Notification. We recommend you to review this notification in details prior to the use of affected product in your application. Please use acknowledgement below or E-Mail to request additional information.

*If you have any questions, please feel free to contact your local IDT distributor or sales representative.*

Customer: \_\_\_\_\_ E-Mail Address: \_\_\_\_\_  
Name/Date: \_\_\_\_\_ Phone #: \_\_\_\_\_  
Title: \_\_\_\_\_ Fax #: \_\_\_\_\_

CUSTOMER COMMENTS: \_\_\_\_\_