



Integrated Device Technology, Inc.  
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## Errata Notification

EN #: SMEN-01-04 Errata Revision #: 00  
 Issue Date: July 23, 2001 Effective Date: July 23, 2001  
 Product Affected: 70V3599, 70V659 and 70V7519 Families (BF, BC and DR Packages)

**ERRATA DETAILS:**

**ATTACHMENT**     Yes     No

- Data Sheet Errata                      JTAG Timing Parameter  $t_{JCD}$  not correct on Datasheets
- Device Manual Errata
- Application Notes Errata
- Other (Please Specify)

Changed JTAG Timing Parameter  $t_{JCD}$  to 25ns from 20ns. (See JTAG AC Electrical Characteristics Table)

Symbol	Parameter	Min.	Max.	Units
$t_{JCD}$	JTAG Data Output	--	25	ns

**IDT Web Site Location:**    <http://www.idt.com/products/pages/Multi-Ports.html>

**CUSTOMER NOTIFICATION:**

As a result of Errata listed above, IDT has decided to notify you through this Errata Notification. We recommend you to review this notification in details prior to the use of affected product in your application. Please use acknowledgement below or E-Mail to request additional information.

*If you have any questions, please feel free to contact your local IDT distributor or sales representative.*

Customer: _____	E-Mail Address: _____
Name/Date: _____	Phone #: _____
Title: _____	Fax #: _____

**CUSTOMER COMMENTS:** \_\_\_\_\_