

RL78/L1C

R01AN2306EG0100

Rev.1.00

Utilising the LIN in Slave Mode for e² studio

Sep 11, 2014

Introduction

The LIN Slave sample code demonstrates UART0 module in LIN Slave mode. The UART0 has features that are applicable to the LIN SYNC BREAK and SYNC frame structure.

Target Device

RL78/L1C

Development environment

IDE: e² studio

Compiler: GNURL78 v13.02 -ELF

Hardware: Renesas Starter Kit for RL78/L1C

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1. Installation

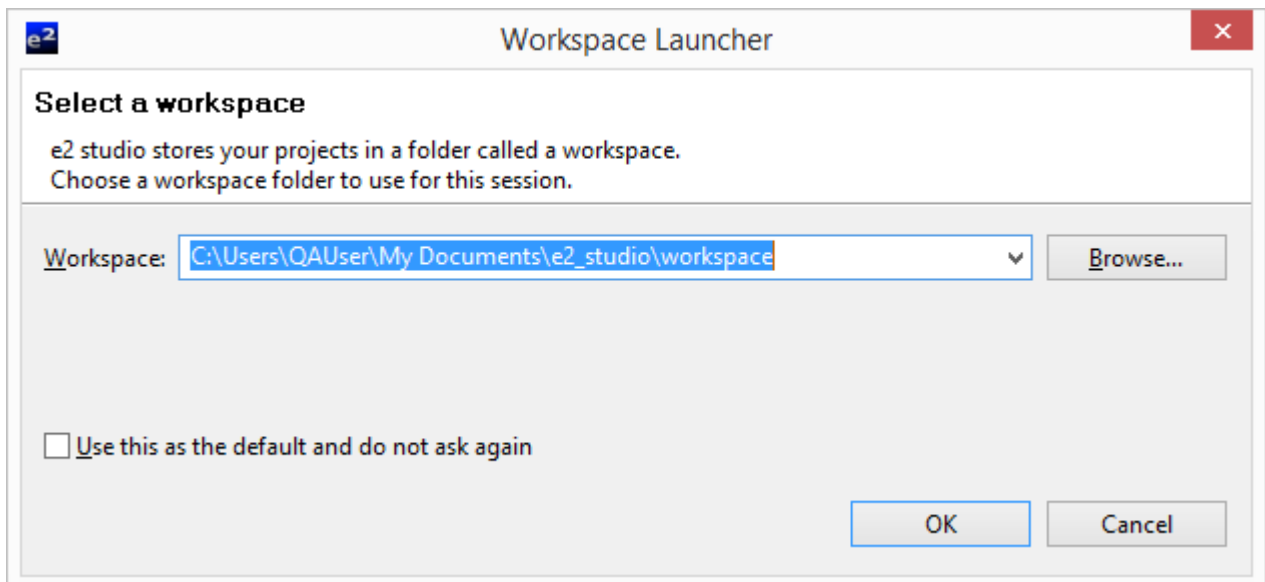
This section assumes e² studio IDE is already installed on the user's personal computer (PC). It is also assumed that the following software and versions are installed:

- Renesas e² studio Version 2.02.00.13 or later
- Application Leading Tool for RL78 Version 1.01.00.02
- GNURL78 Version 13.02-ELF

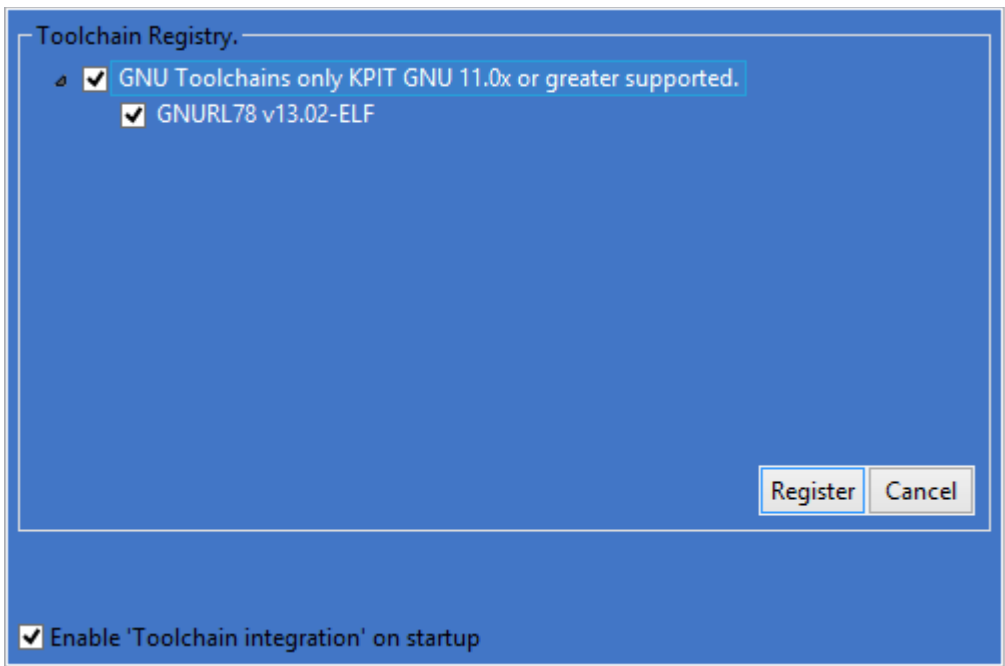
Create a new folder and name it as 'RSKRL78L1C_Workspace'. Copy the zipped file 'an_r01an2125eg0100_rl7811c_lin_slave.zip', available in the Application Note package downloaded from the website, to this folder. Extract the 'an_r01an2125eg0100_rl7811c_lin_slave.zip' file to the 'RSKRL78L1C_Workspace' folder.

2. Creating the Project Workspace

Open e² studio IDE by clicking the Windows Start button, select All Programs > Renesas Electronics e2 studio > Renesas e2 studio.

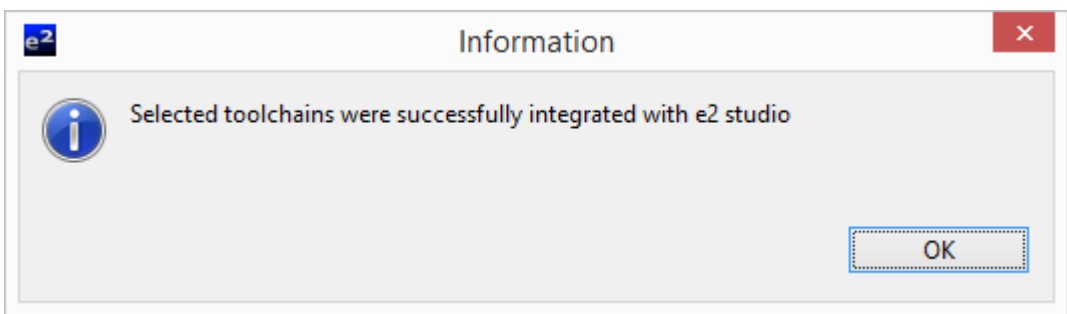


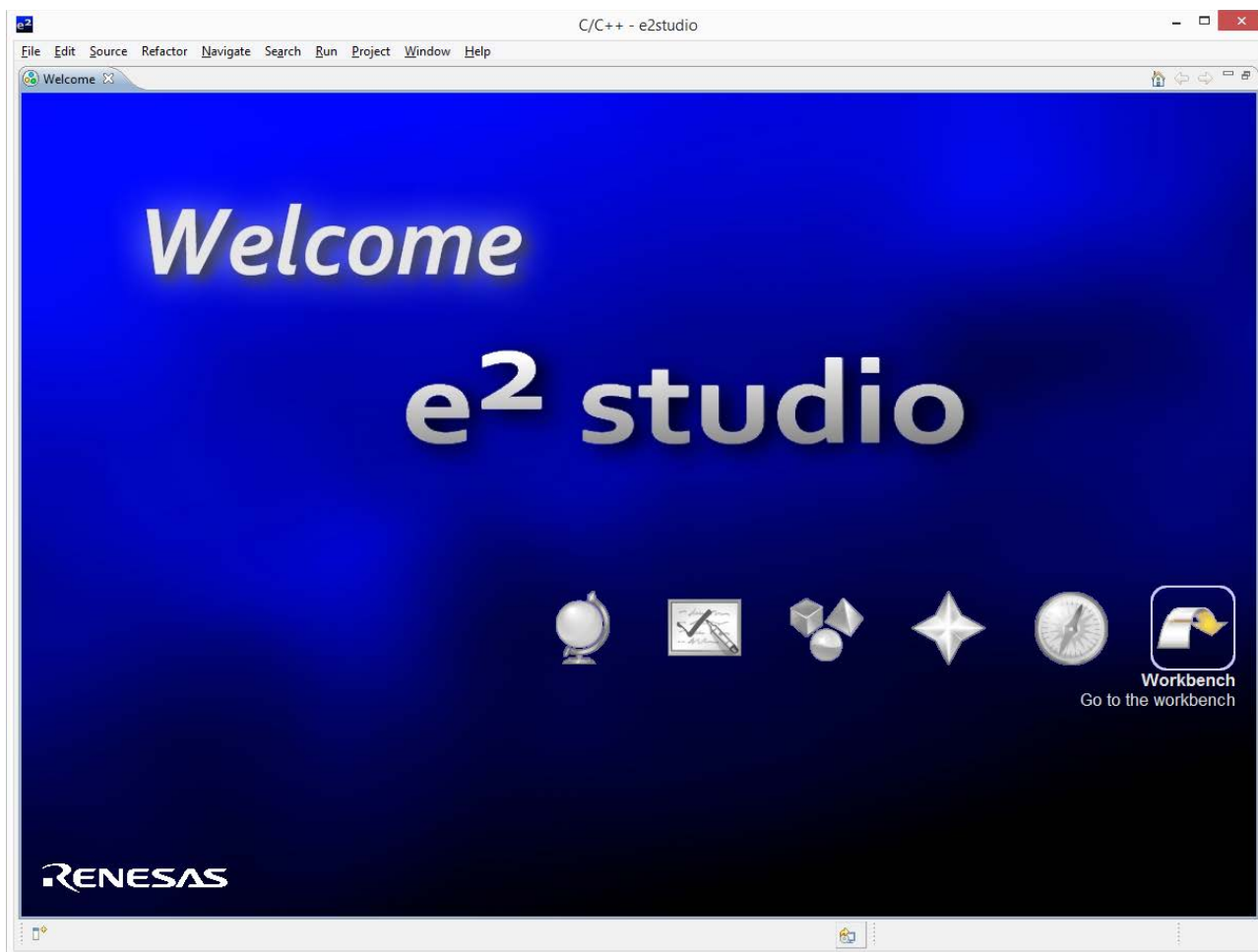
Select <OK>



Select 'GNU Toolchains only...' and 'GNURL78 v13.02-ELF' checkboxes. Click 'Register'. A dialog will appear. Click <OK>.

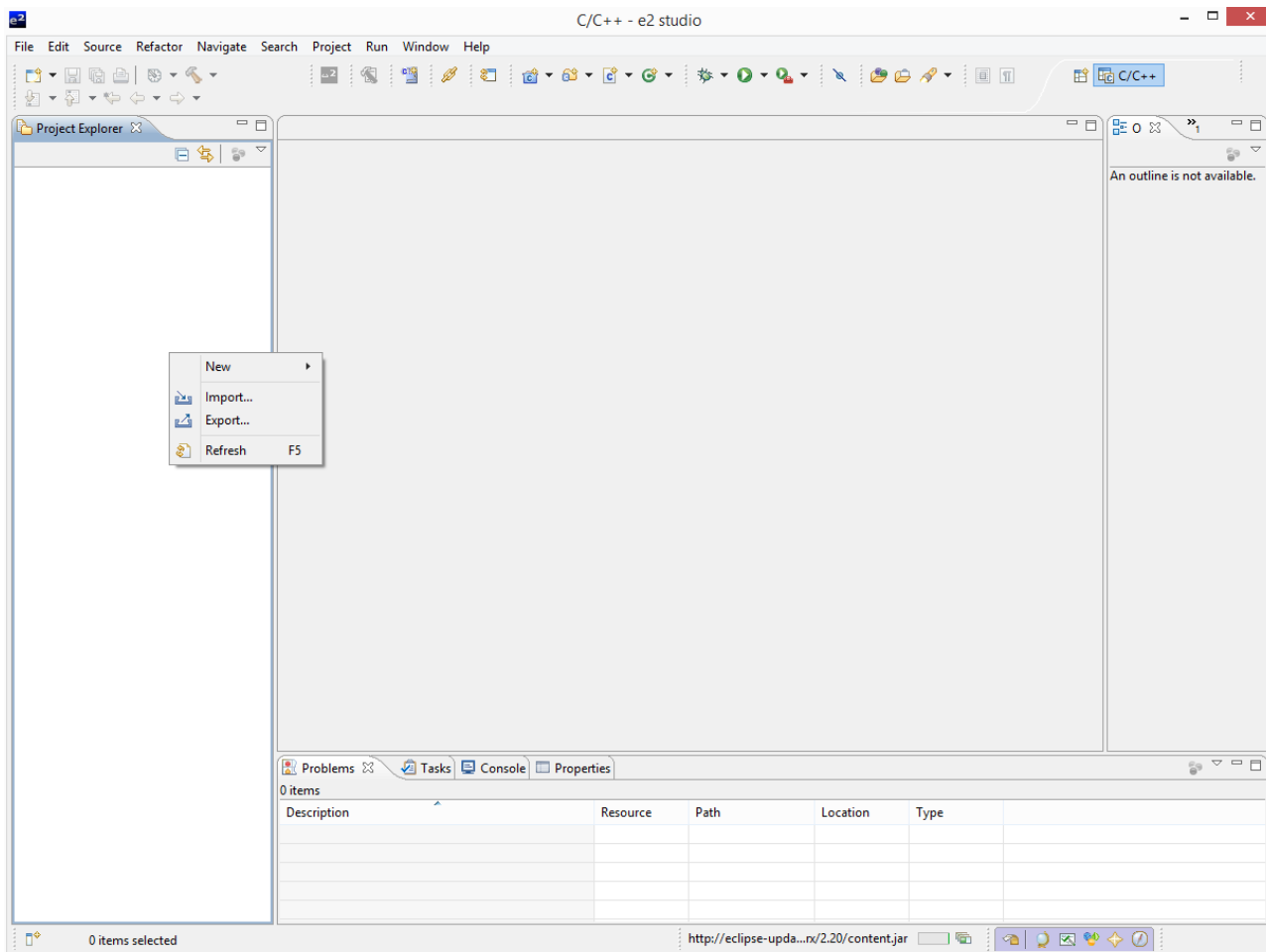
"Selected Toolchains were successfully integrated with e2studio". Click <OK>.



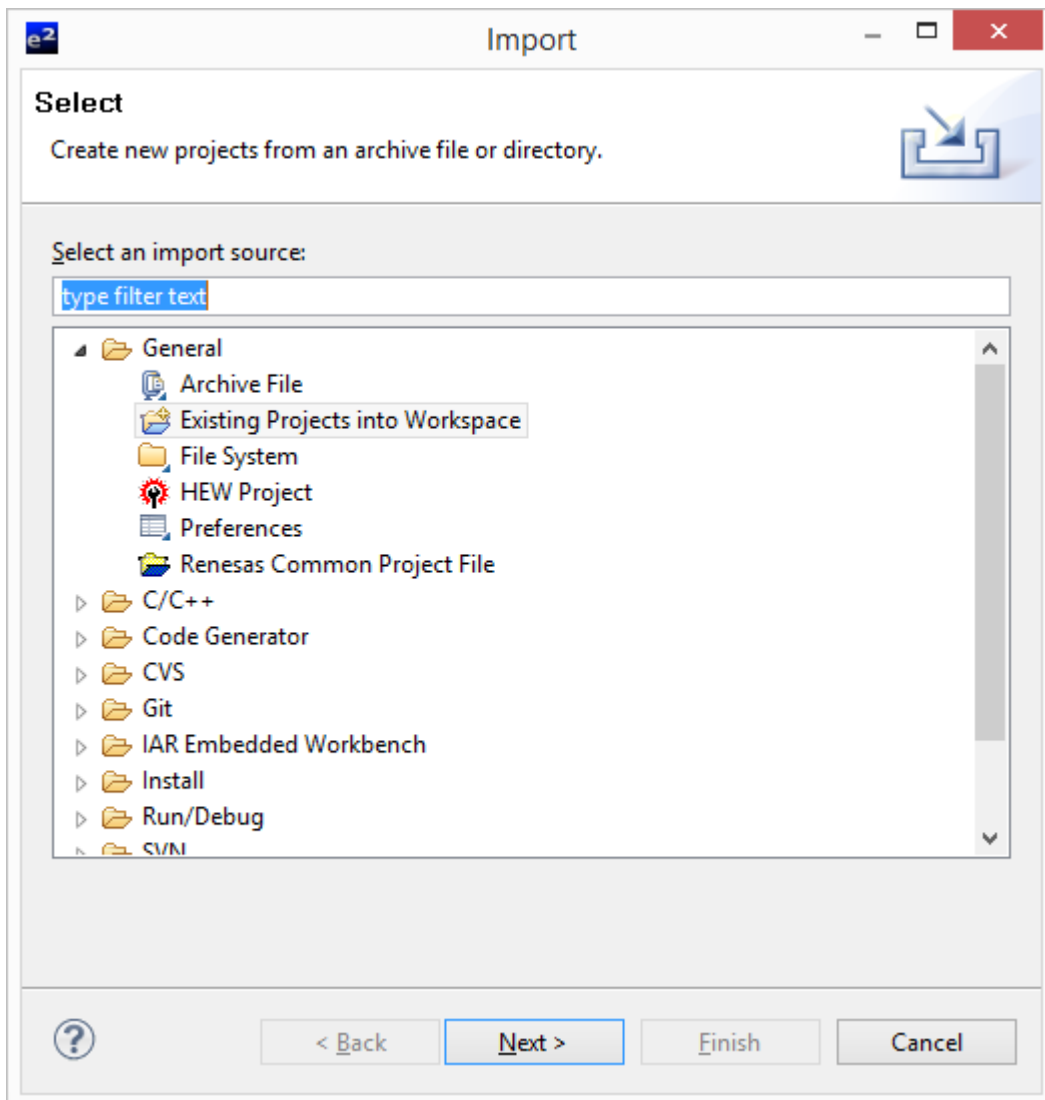


On the welcome screen, select 'Go to the Workbench' icon as shown above.

1. Once the e² studio environment has initialised, right click in the project explorer window and click <Import...>



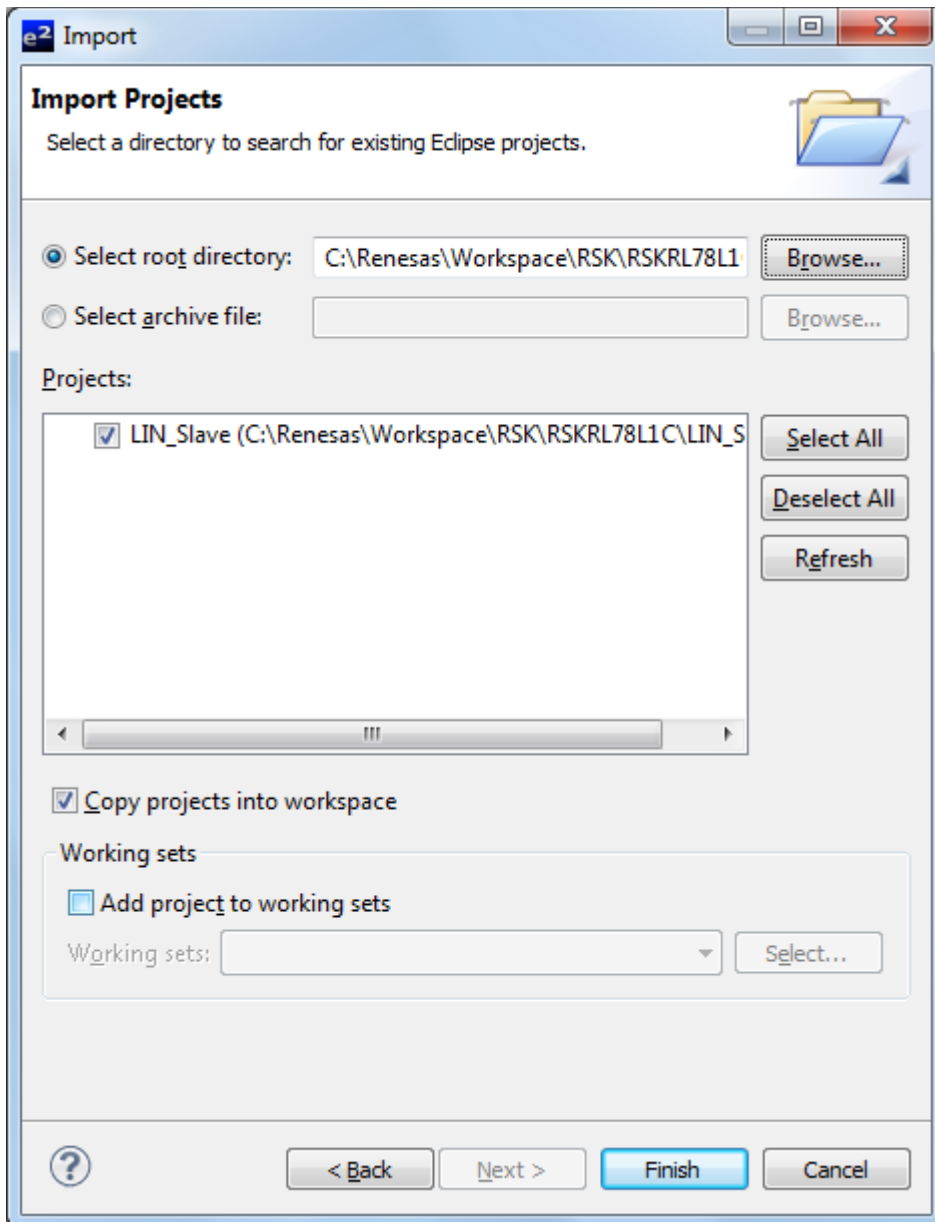
2. The Import dialog will now appear. Expand the “General” folder icon, and select “Existing Projects into Workspace”, then click ‘Next’.



3. The Import Dialog will now appear and specify the project to import. Click the “Browse” button and locate the directory created in section 1: ‘RSKRL78L1C_Workspace’.

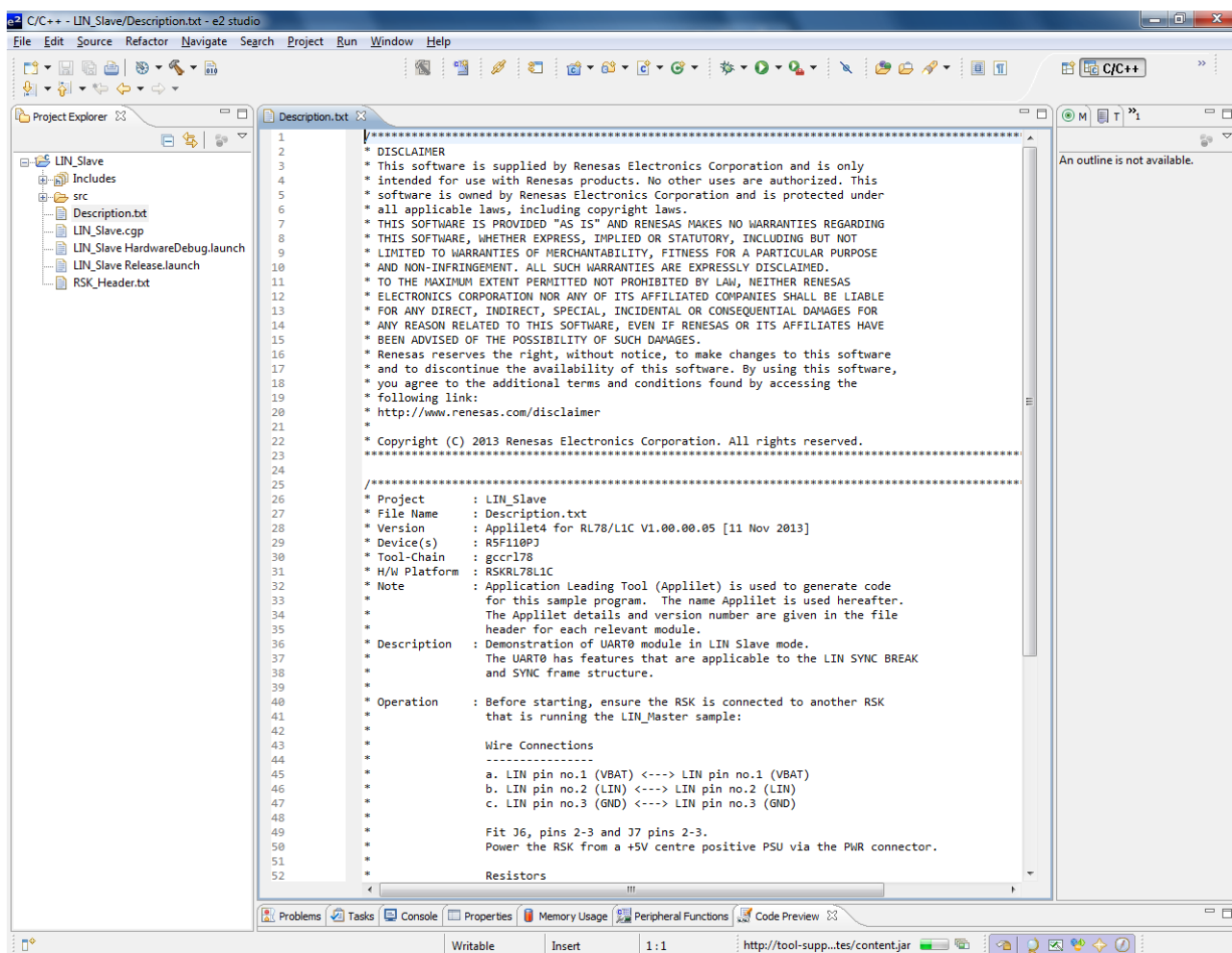
Also ensure that the ‘Copy projects into workspace’ option is ticked, and then click <Finish>

The IDE e² studio will load the project.



3. Opening Sample Code and Source

Once the project has been opened, the source code and all dependent files can be opened in the editor by expanding the folders in the Project Tree window and double clicking the files listed. All files have been grouped according to their file type.



4. Source Code Functionality

The source code project is specifically written to run on the appropriate RSK. However, this source code can be useful as an example even without the RSK.

The project was written using source files containing API functions generated using Code Generator. The project will contain a C source file 'r_cg_main.c'. This source file includes the C function main(). All source files and dependent files whose filenames are prefixed with 'r_cg_' were generated using Applilet4 (Application Leading Tool). For more information, refer to Description.txt.

5. Appendix

Example of comment block with code functionality.

```

/*****
* Project      : LIN_Slave
* File Name    : Description.txt
* Version     : Applilet4 for RL78/L1C V1.00.00.05 [11 Nov 2013]
* Device(s)   : R5F110PJ
* Tool-Chain  : gccrl78
* H/W Platform : RSKRL78L1C
* Note        : Application Leading Tool (Applilet) is used to generate code
*              for this sample program. The name Applilet is used hereafter.
*              The Applilet details and version number are given in the file
*              header for each relevant module.
* Description  : Demonstration of UART0 module in LIN Slave mode.
*              The UART0 has features that are applicable to the LIN SYNC BREAK
*              and SYNC frame structure.
*
* Operation   : Before starting, ensure the RSK is connected to another RSK
*              that is running the LIN_Master sample:
*
*              Wire Connections
*              -----
*              a. LIN pin no.1 (VBAT) <---> LIN pin no.1 (VBAT)
*              b. LIN pin no.2 (LIN) <---> LIN pin no.2 (LIN)
*              c. LIN pin no.3 (GND) <---> LIN pin no.3 (GND)
*
*              Fit J6, pins 2-3 and J7 pins 2-3.
*              Power the RSK from a +5V centre positive PSU via the PWR connector.
*
*              Resistors
*              -----
*              Remove R58 and R73.
*
* Instructions :
*              1. Compile the sample code, and download to the RSK. Click the 'Resume'
*                 button to start program execution. Click again if the program stops at main.
*              2. Adjust the potentiometer on the slave RSK. The ADC value
*                 will be displayed on the slave LCD and transmitted to the master
*                 board, where it will also be displayed on the LCD.

```

- * 3. The ADC result displayed on the LCD connected to Master
- * RSK and Slave RSK should match. Data transmission is
- * checksum protected and if the LIN master detects a checksum
- * error the master LCD will display CF in the LCD. If
- * this occurs check the connection between the boards and
- * restart both RSKs.
- *

*****/

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Revision History

| Rev. | Date | Description | |
|------|--------------|-------------|----------------------|
| | | Page | Summary |
| 1.0 | Sep 11, 2014 | - | First edition issued |

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Handling of Unused Pins

Handle unused pins in accordance with the directions given under Handling of Unused Pins in the manual.

- The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.

2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

- The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.
In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

3. Prohibition of Access to Reserved Addresses

Access to reserved addresses is prohibited.

- The reserved addresses are provided for the possible future expansion of functions. Do not access these addresses; the correct operation of LSI is not guaranteed if they are accessed.

4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.

5. Differences between Products

Before changing from one product to another, i.e. to a product with a different part number, confirm that the change will not lead to problems.

- The characteristics of an MPU or MCU in the same group but having a different part number may differ in terms of the internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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