

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics Corporation

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## 4518 Group

### Reference Information for System Development Of 4518 Group

#### 1. Difference in function between 4519 Group and 4518 Group

Parameter	4519 Group	4518 Group
The number of pin	42	32
Port	Ports P32, P33	-
	Port P4 (P40/AIN4 to P43/AIN7)	-
	Port P5 (P50 to P53)	-
A/D converter	8-channel	4-channel

\* They have each memory expansion. Refer to each data sheet.

#### 2. When the 4519 Group User's Manual is used for the 4518 Group system development, to replace the following description is required.

Pages and section titles shown below correspond to The 4519 Group User's Manual Rev.1.00.

Chap.	Page	Section Title	Contents
Chap. 1	Hardware		Refer to the 4518 Group Datasheet.
Chap.2	Application		
P2-3	2.1.1 I/O ports (4) Port P3		4518 Group does not have Ports P32 and P33. Accordingly, when the IAP3 instruction is executed, bits 2 and 3 of register A are undefined.
P2-3	2.1.1 I/O ports (5) Port P4		This section is not required.
P2-4	2.1.1 I/O ports (6) Port P5		This section is not required.
P2-7	Table 2.1.4 A/D control register Q2		Q2 <sub>3</sub> is not used. As for details, refer to the 4518 Group Datasheet.
P2-10	2.1.2 Related registers (10) Port output structure control register FR3		This section is not required.
P2-14	Table 2.1.13 Connections of unused pins		4518 Group does not have P32, P33, P40/AIN4 to P43/AIN7 and P50 to P53.
P2-52	2.4 A/D converter		A 4-channel A/D converter
P2-52	Fig. 2.4.1 A/D converter structure		4518 Group does not have IAP4 and OP4A instructions, and P40/AIN4 to P43/AIN7. 4-channel multi-plexed analog switch
P2-53	Table 2.4.2 A/D control register Q1		Q1 <sub>2</sub> to Q1 <sub>0</sub> =100,101,110,111: Not available As for details, refer to the 4518 Group Datasheet. Please replace the Note 2 to the following: Set register Q2 before select AIN0 to AIN3 by register Q1.
P2-54	Table 2.4.3 A/D control register Q2		Q2 <sub>3</sub> is not used. As for details, refer to the 4518 Group Datasheet.

Chap.	Page	Section Title	Contents
Chap.2 Application			
P2-57	2.4.3 Notes on use (6) Analog input pins		Replace the description to the following: When analog input functions of P60/AIN0 to P63/AIN3 pins are selected, input/output of port P6 cannot be used.
P2-69	Fig. 2.6.4 Internal state at reset		4518 Group does not have port output structure control register FR3.
P2-72	Table 2.8.1 Functions and states retained at RAM back-up mode		4518 Group does not have port output structure control register FR3.
Chap. 3 Appendix			
P3-2 to 10	3.1 Electrical characteristics		Refer to the 4518 Group Datasheet.
P3-13	Table 3.3.1 Connections of unused pins		4518 Group does not have P32, P33, P40/AIN4 to P43/AIN7 and P50 to P53.
P3-18	3.3.6 Notes on A/D conversion (6) Analog input pins		Replace the description to the following: When analog input functions of P60/AIN0 to P63/AIN3 pins are selected, input/output of port P6 cannot be used.
P3-27	3.5 Package outline		Refer to the 4518 Group Datasheet.

## Revision Record

Rev.	Date	Page	Description
			Summary
1.00	Sep.01.05	—	First edition issued

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