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April 1st, 2010
Renesas Electronics Corporation

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H8/300H Tiny Series

Four-Digit Decimal Counter (DECNT)

Introduction

Increments a four-digit binary-coded-decimal counter (BCD counter) by one.

Target Device

H8/300H Tiny Series

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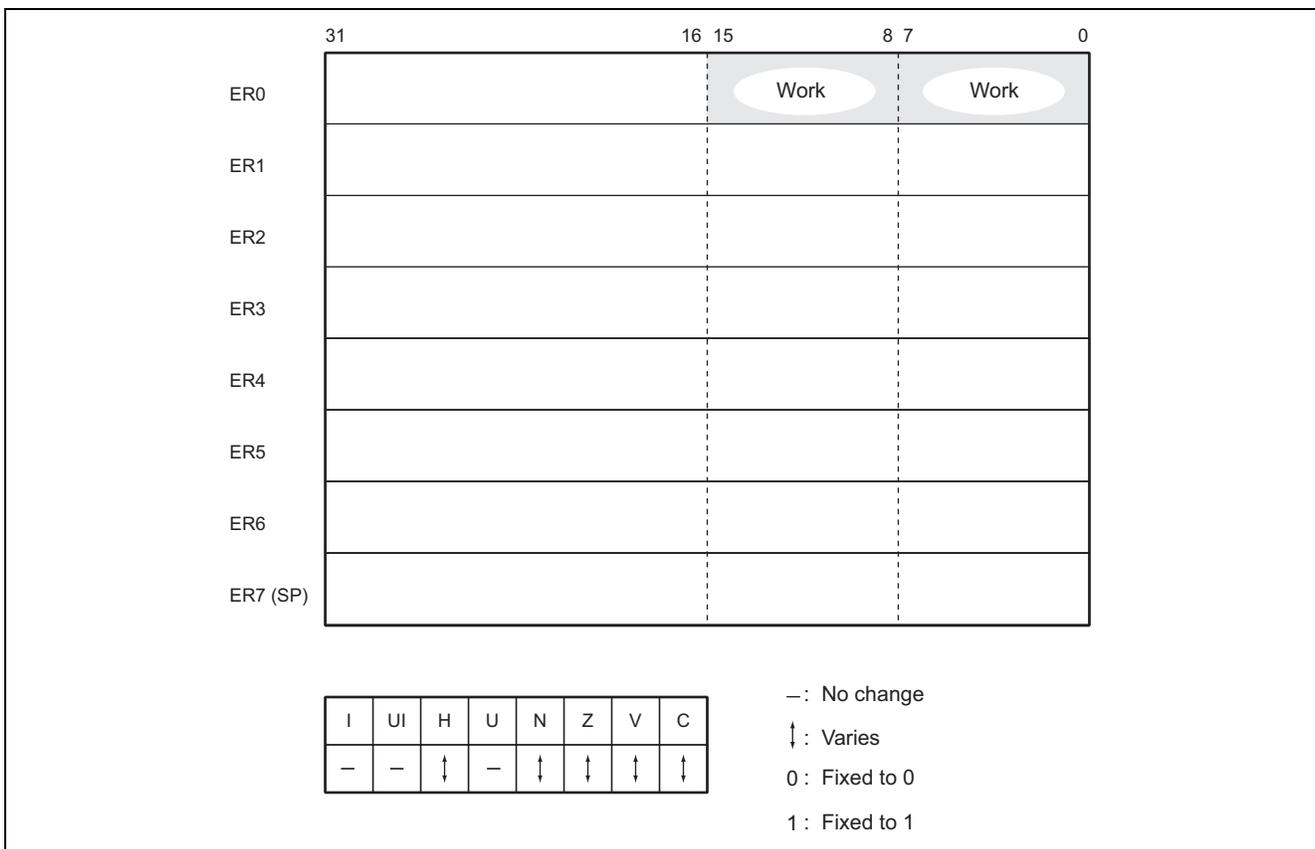
1. Function

1. Increments a four-digit binary-coded-decimal counter (BCD counter) by one.
2. This function is convenient for counting numbers of interrupts (external interrupts, timer interrupts, etc.).

2. Arguments

Contents	Storage Location	Data Length (Bytes)
Input	—	—
Output	Four-digit BCD counter	DCNTR (RAM)
	Occurrence of counter overflow	C flag (CCR)

3. Changes to Internal Registers and Flags



4. Programming Specifications

Program memory (bytes)
18
Data memory (bytes)
2
Stack (bytes)
0
Number of cycles
28
Re-entrant
No
Relocatable
Yes
Interrupts during execution
Yes

5. Description

5.1 Description of Functions

1. The arguments are as follows.

DCNTR: Used as a four-digit BCD counter that is incremented on each execution of the DECNT subroutine.

C flag (CCR): Indicates the counter's state after execution of DECNT.

C flag = 1: Indicates a counter overflow (see figure 2).

C flag = 0: Indicates that the counter has been incremented normally.

2. Figures 1 and 2 illustrate the execution of the DECNT subroutine. Executing DECNT increments the four-digit BCD counter as shown in figure 1.

5.2 Usage Notes

If the counter overflows, the counter value is cleared to "0", as shown in figure 2.

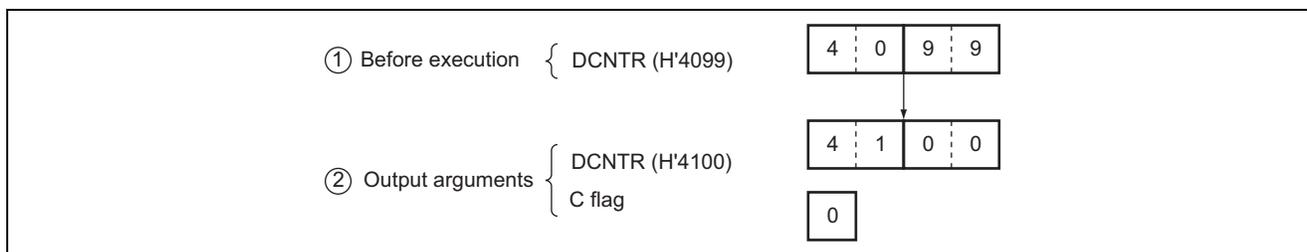


Figure 1 Example of DECNT Execution

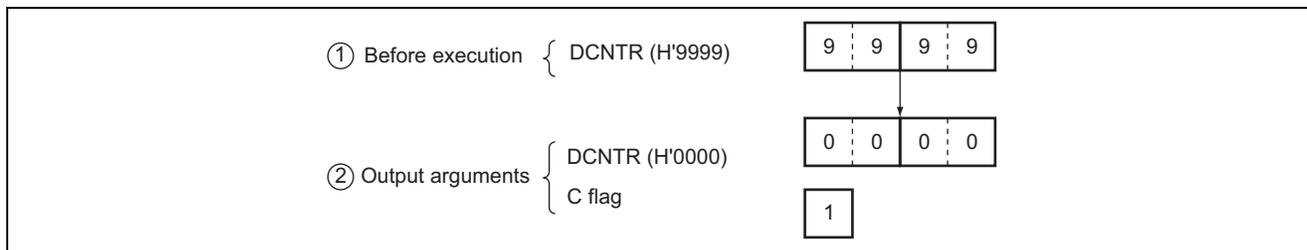
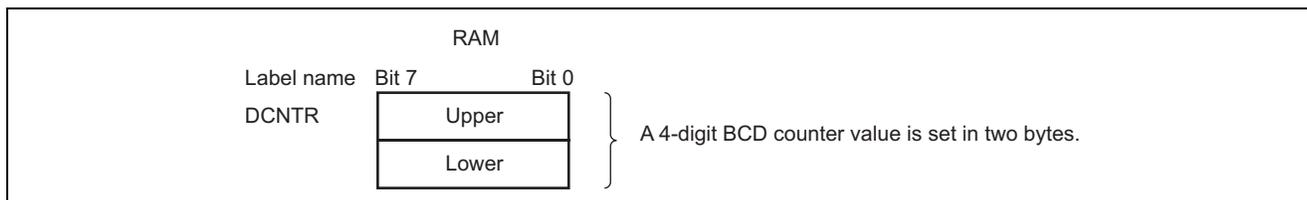
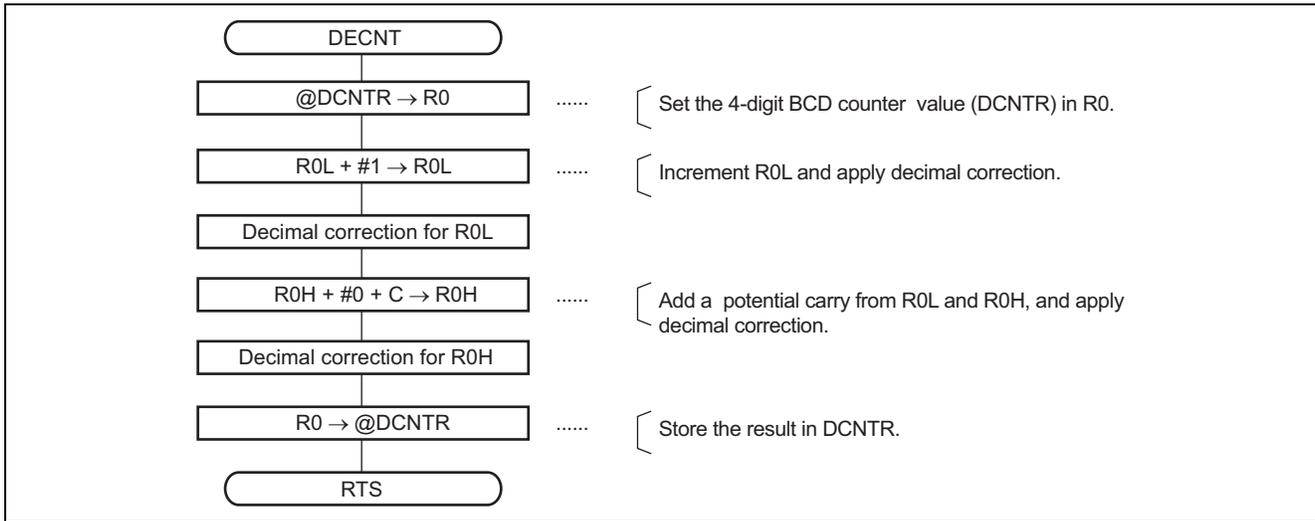


Figure 2 Counter Overflow

5.3 Description of Data Memory



6. Flowchart



7. Program Listing

```

1          1          ;*****
2          2          ;*
3          3          ;*          NAME : 4 DIGIT BCD COUNTER (DECNT)
4          4          ;*
5          5          ;*****
6          6          ;*
7          7          ;*          ENTRY: NOTHING
8          8          ;*
9          9          ;*          RETURN: DCNTR          (BCD COUNTER)
10         10         ;*          C flag of CCR (C=0: TRUE,  C=1: OVERFLOW)*
11         11         ;*
12         12         ;*****
13         13         ;
14         14         .CPU          300HN
15         15         .SECTION     DECNT_code, CODE, ALIGN=2
16         16         .EXPORT     DECNT
17         17         ;
18         18         DCNTR .EQU     $          ;Entry point
19         19         MOV.W       @DCNTR,R0   ;Load DCNTR to R0
20         20         ADD.B       #'01,R0L    ;R0L + #'01    --> R0L
21         21         DAA         R0L        ; Decimal-adjust R0L
22         22         ;
23         23         ADDX.B      #'00,R0H    ;R0H + #'00 + C --> R0H
24         24         DAA         R0H        ;Decimal-adjust R0H
25         25         MOV.W       R0,@DCNTR  ;Store R0 in DCNTR
26         26         RTS
27         27         ;-----
28         28         ;
29         29         .SECTION     DATA_data, DATA, ALIGN=2
30         30         .EXPORT     DCNTR
31         31         ;
32         32         DCNTR .DATA.W  H'0000
33         33         ;
34         34         ;-----
35         35         ;
36         36         .END

```

****TOTAL ERRORS 0
****TOTAL WARNINGS 0

Revision Record

Rev.	Date	Description	
		Page	Summary
2.00	Feb.28.06	—	Format has been changed from Hitachi version to Renesas version.

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